flowPHASE0 — 600V/75A

Features

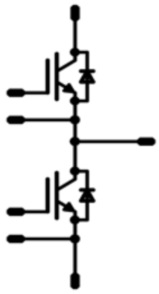
- Trench Fieldstop IGBT[3] technology
- 2-clip housing in 12mm and 17mm height
- Compact and low inductance design
- AlN substrate for improved performance

flow0 housing


Target Applications

- Motor Drive
- UPS

Schematic



Types

- FZ062PA075SA01
- F0062PA075SA01

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Inverter Transistor				
Collector-emitter break down voltage	V_{CE}		600	V
DC collector current	I_C	$T_j=T_jmax$ — T_h=80°C T_c=80°C	91 110	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by T_jmax	225	A
Power dissipation per IGBT	P_{tot}	$T_j=T_jmax$ — T_h=80°C T_c=80°C	196 297	W
Gate-emitter peak voltage	V_{GE}		±20	V
Short circuit ratings	t_{SC} V_{CC}	T_j≤150°C V_{GE}=15V	6 360	μs V
Maximum Junction Temperature	T_jmax		175	°C
Inverter Diode				
Peak Repetitive Reverse Voltage	V_{RRM}	T_j=25°C	600	V
DC forward current	I_F	$T_j=T_jmax$ — T_h=80°C T_c=80°C	80 90	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_jmax	160	A
Power dissipation per Diode	P_{tot}	$T_j=T_jmax$ — T_h=80°C T_c=80°C	130 197	W
Maximum Junction Temperature	T_jmax		175	°C

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition		Value	Unit
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{op}			-40…+(Tjmax - 25)	°C
Insulation Properties					
Insulation voltage	V_{is}	t=2s	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm

Characteristic Values

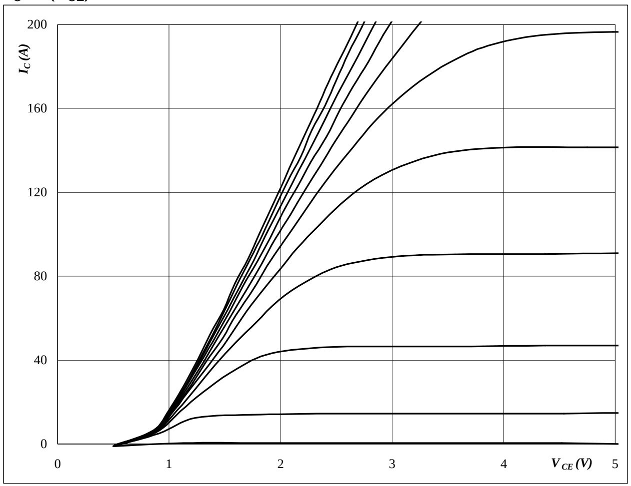
Parameter	Symbol	Conditions	V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_C [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max	Unit
Inverter Transistor										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,0008	Tj=25°C Tj=150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		75	Tj=25°C Tj=150°C	1	1,63 1,87	2,1	V
Collector-emitter cut-off current incl. Diode	I_{CES}		0	600		Tj=25°C Tj=150°C			0,03	mA
Gate-emitter leakage current	I_{GES}		20	0		Tj=25°C Tj=150°C			700	nA
Integrated Gate resistor	R_{gint}							4		Ω
Turn-on delay time	$t_{d(on)}$	Rgoff=2 Ω Rgon=2 Ω	±15	300	75	Tj=25°C Tj=150°C		123 132		ns
Rise time	t_r					Tj=25°C Tj=150°C		15 21		
Turn-off delay time	$t_{d(off)}$					Tj=25°C Tj=150°C		169 199		
Fall time	t_f					Tj=25°C Tj=150°C		87 105		
Turn-on energy loss per pulse	E_{on}					Tj=25°C Tj=150°C		0,52 0,89		mWs
Turn-off energy loss per pulse	E_{off}					Tj=25°C Tj=150°C		1,68 2,26		
Input capacitance	C_{ies}	f=1MHz	0	25		Tj=25°C		4700		pF
Output capacitance	C_{oss}							300		
Reverse transfer capacitance	C_{rss}							145		
Gate charge	Q_{Gate}		±15			Tj=25°C		465		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal foil thickness=76um Kunze foil KU-ALF5						0,48		K/W
Thermal resistance chip to case per chip	R_{thJC}									
Inverter Diode										
Diode forward voltage	V_F				50	Tj=25°C Tj=150°C	1	1,64 1,59	2,1	V
Peak reverse recovery current	I_{RRM}	Rgon=2 Ω	±15	300	75	Tj=25°C Tj=150°C		93,621 110,6		A
Reverse recovery time	t_{rr}					Tj=25°C Tj=150°C		100,9 128,2		ns
Reverse recovered charge	Q_{rr}					Tj=25°C Tj=150°C		3,24 6,5		μC
Peak rate of fall of recovery current	di(rec)max /dt					Tj=25°C Tj=150°C		6615 4412		A/μs
Reverse recovered energy	Erec					Tj=25°C Tj=150°C		0,75 1,57		mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal foil thickness=76um Kunze foil KU-ALF5						0,73		K/W
Thermal resistance chip to case per chip	R_{thJC}									

Output Inverter

Figure 1 Output inverter IGBT

Typical output characteristics

$I_C = f(V_{CE})$



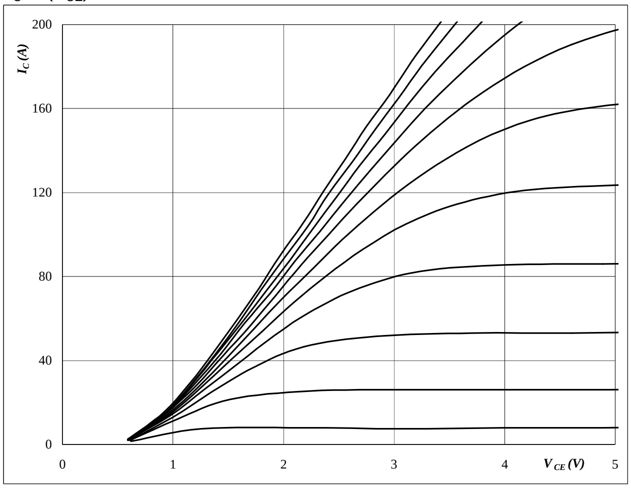
At

t_p = 350 µs
T_j = 25 °C
V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2 Output inverter IGBT

Typical output characteristics

$I_C = f(V_{CE})$



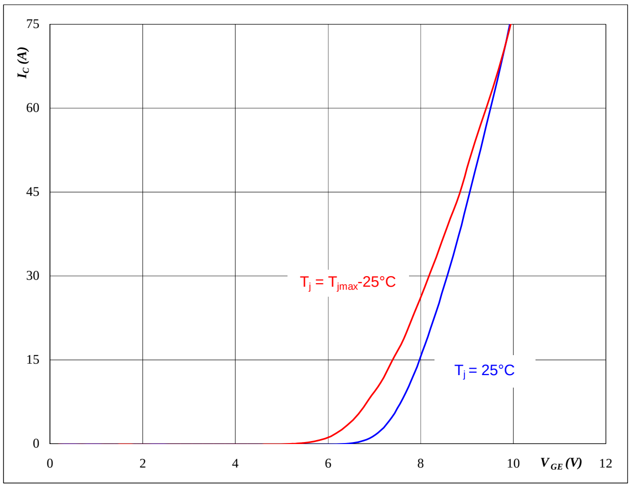
At

t_p = 350 µs
T_j = 150 °C
V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3 Output inverter IGBT

Typical transfer characteristics

$I_C = f(V_{GE})$



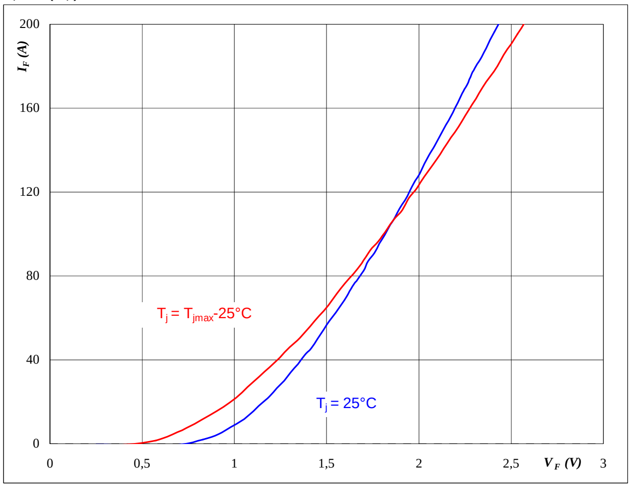
At

t_p = 350 µs
V_{CE} = 10 V

Figure 4 Output inverter FRED

Typical diode forward current as a function of forward voltage

$I_F = f(V_F)$



At

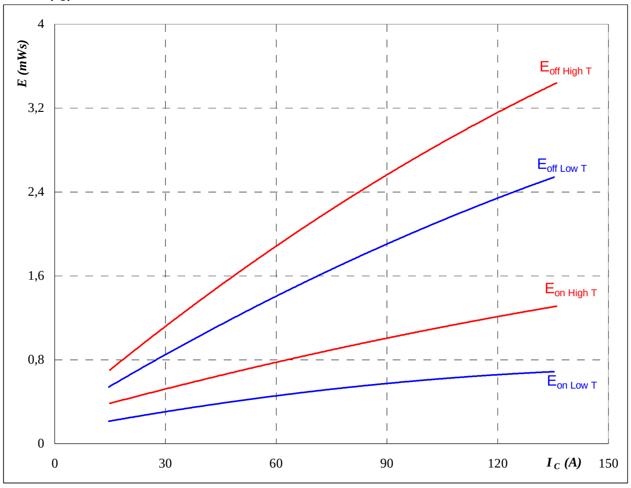
t_p = 350 µs

Output Inverter

Figure 5 Output inverter IGBT

Typical switching energy losses as a function of collector current

E = f(I_C)



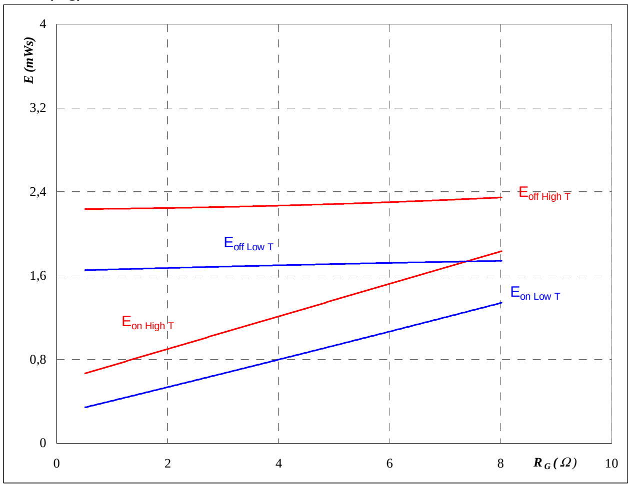
With an inductive load at

T_j =	25/150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	2	Ω
R_{goff} =	2	Ω

Figure 6 Output inverter IGBT

Typical switching energy losses as a function of gate resistor

E = f(R_G)



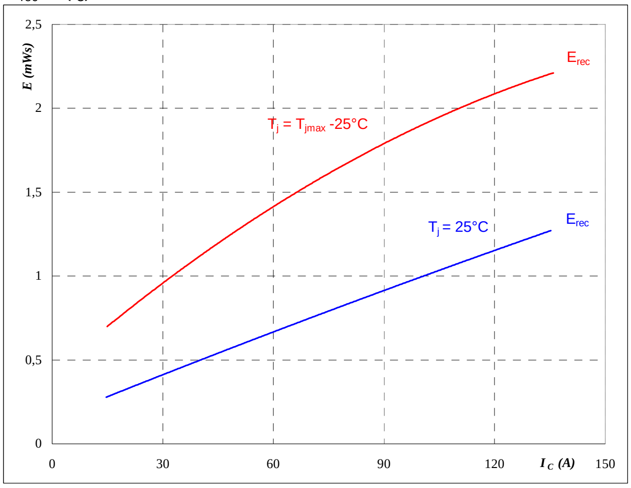
With an inductive load at

T_j =	25/150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
I_C =	75	A

Figure 7 Output inverter IGBT

Typical reverse recovery energy loss as a function of collector current

E_{rec} = f(I_C)



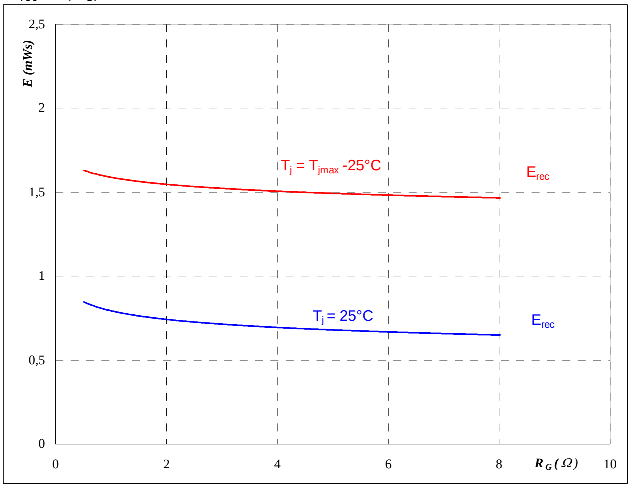
With an inductive load at

T_j =	25/150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	2	Ω

Figure 8 Output inverter IGBT

Typical reverse recovery energy loss as a function of gate resistor

E_{rec} = f(R_G)



With an inductive load at

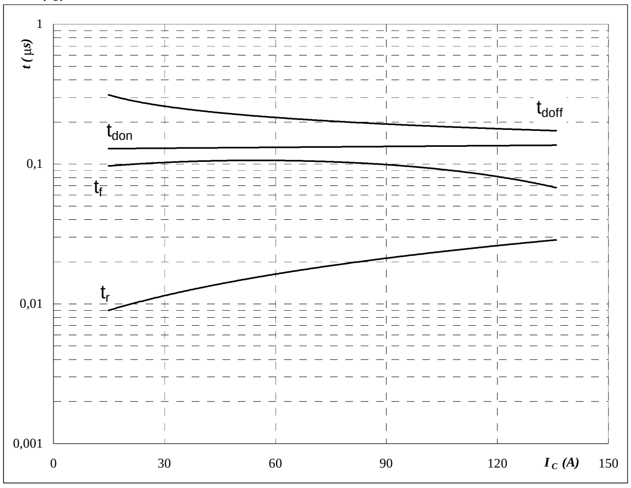
T_j =	25/150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
I_C =	75	A

Output Inverter

Figure 9 Output inverter IGBT

Typical switching times as a function of collector current

t = f(I_C)



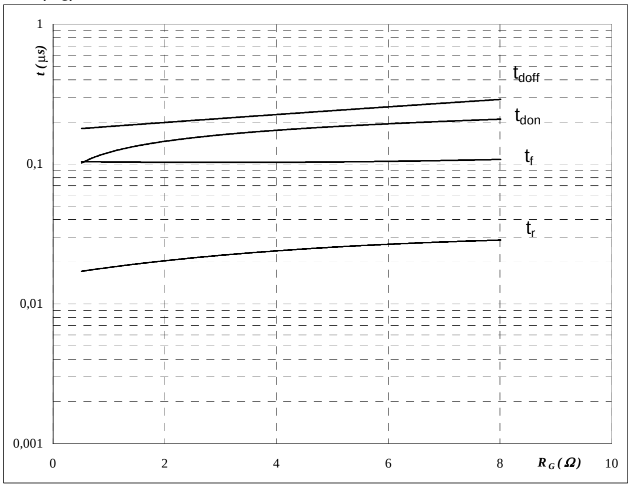
With an inductive load at

T_j =	150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	2	Ω
R_{goff} =	2	Ω

Figure 10 Output inverter IGBT

Typical switching times as a function of gate resistor

t = f(R_G)



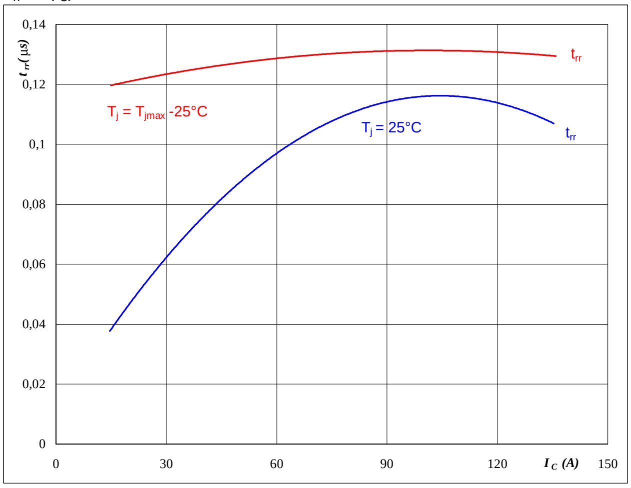
With an inductive load at

T_j =	150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
I_C =	75	A

Figure 11 Output inverter FRED

Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



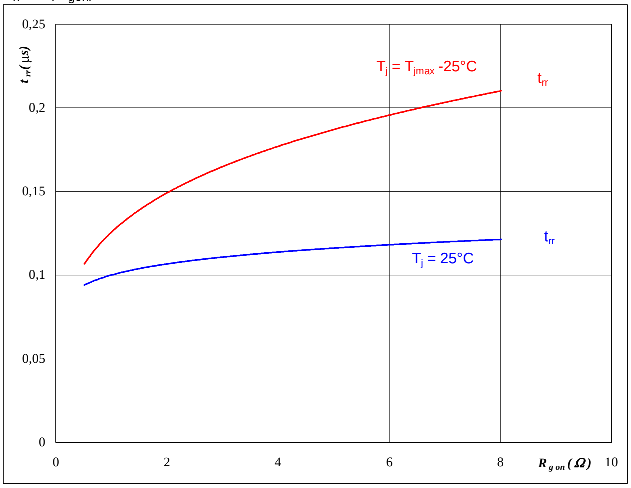
At

T_j =	25/150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	2	Ω

Figure 12 Output inverter FRED

Typical reverse recovery time as a function of IGBT turn on gate resistor

t_{rr} = f(R_{gon})



At

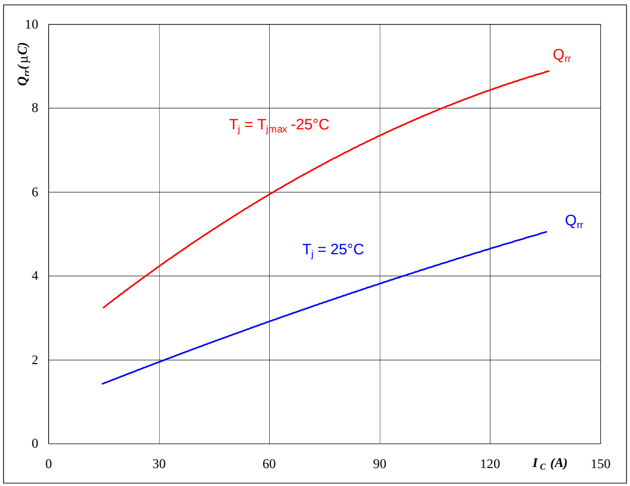
T_j =	25/150	°C
V_R =	300	V
I_F =	75	A
V_{GE} =	±15	V

Output Inverter

Figure 13 Output inverter FRED

Typical reverse recovery charge as a function of collector current

$Q_{rr} = f(I_C)$



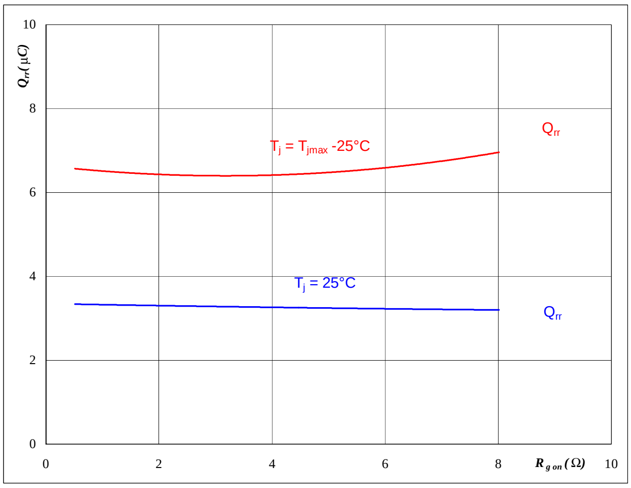
At

T_j =	25/150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	2	Ω

Figure 14 Output inverter FRED

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$Q_{rr} = f(R_{gon})$



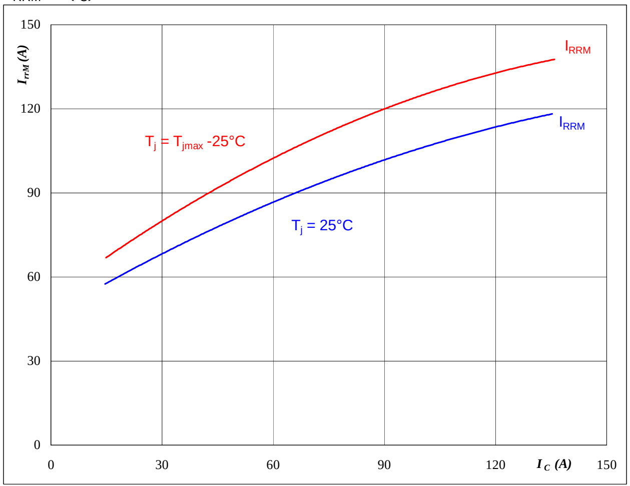
At

T_j =	25/150	°C
V_R =	300	V
I_F =	75	A
V_{GE} =	±15	V

Figure 15 Output inverter FRED

Typical reverse recovery current as a function of collector current

$I_{RRM} = f(I_C)$



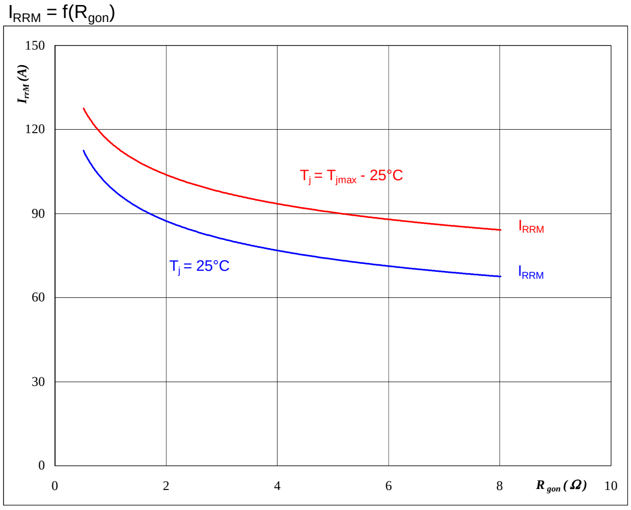
At

T_j =	25/150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	2	Ω

Figure 16 Output inverter FRED

Typical reverse recovery current as a function of IGBT turn on gate resistor

$I_{RRM} = f(R_{gon})$



At

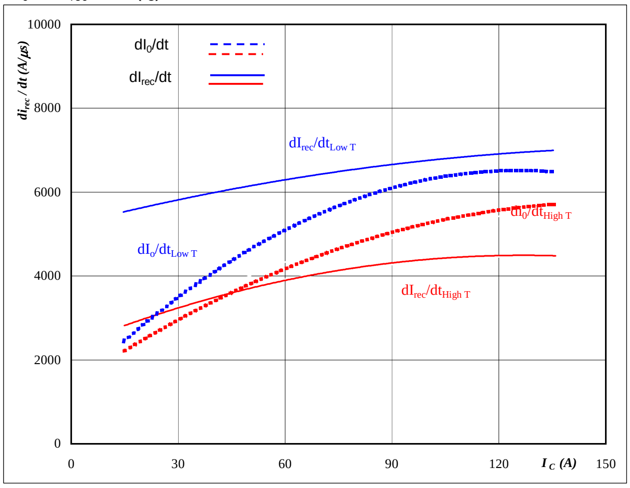
T_j =	25/150	°C
V_R =	300	V
I_F =	75	A
V_{GE} =	±15	V

Output Inverter

Figure 17 Output inverter FRED

Typical rate of fall of forward and reverse recovery current as a function of collector current

$dI_0/dt, dI_{rec}/dt = f(I_C)$



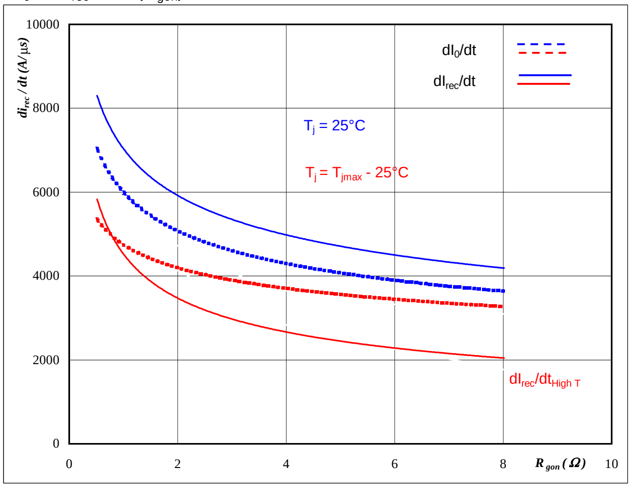
At

T_j =	25/150	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	2	Ω

Figure 18 Output inverter FRED

Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor

$dI_0/dt, dI_{rec}/dt = f(R_{gon})$



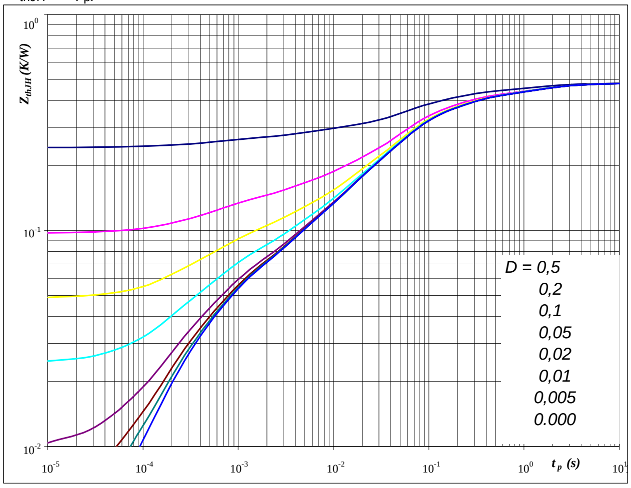
At

T_j =	25/150	°C
V_R =	300	V
I_F =	75	A
V_{GE} =	±15	V

Figure 19 Output inverter IGBT

IGBT transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



At

D =	t_p / T	
R_{thJH} =	0,48	K/W

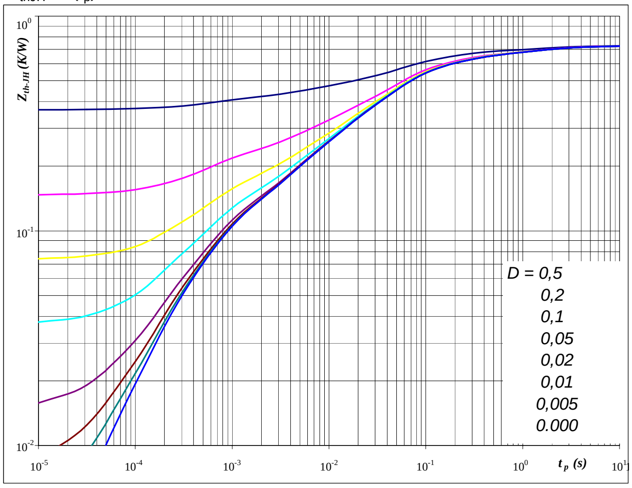
IGBT thermal model values

R (C/W)	Tau (s)
0,02	9,5E+00
0,07	1,2E+00
0,12	1,6E-01
0,19	4,0E-02
0,05	3,4E-03
0,04	4,0E-04

Figure 20 Output inverter FRED

FRED transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



At

D =	t_p / T	
R_{thJH} =	0,73	K/W

FRED thermal model values

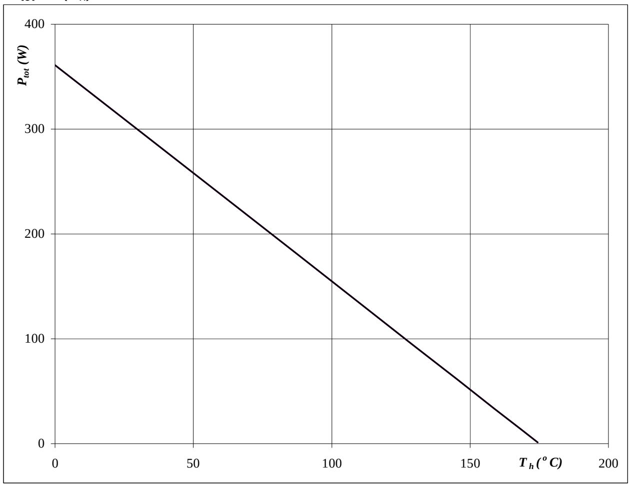
R (C/W)	Tau (s)
0,02	9,5E+00
0,09	1,1E+00
0,14	1,4E-01
0,29	3,7E-02
0,11	4,7E-03
0,09	4,8E-04

Output Inverter

Figure 21 Output inverter IGBT

Power dissipation as a function of heatsink temperature

$P_{tot} = f(T_h)$



At

$T_j =$ 175 °C

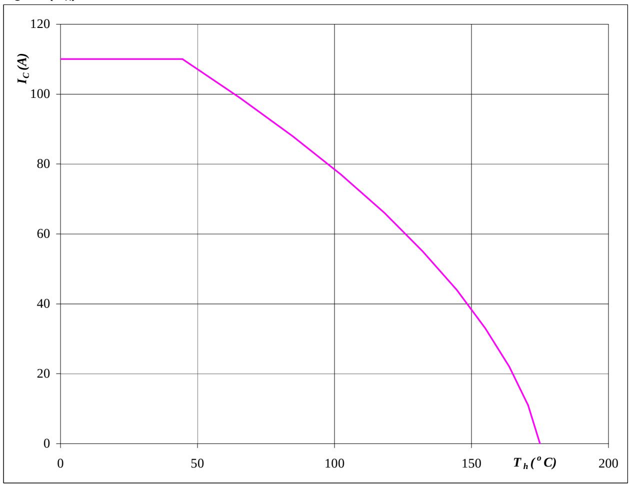
single heating

overall heating

Figure 22 Output inverter IGBT

Collector current as a function of heatsink temperature

$I_C = f(T_h)$



At

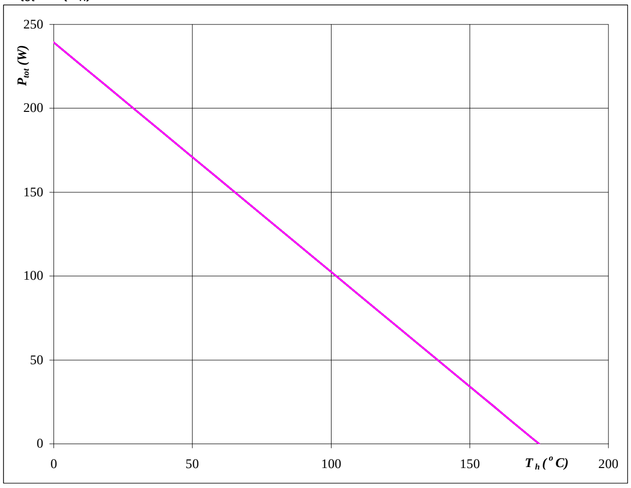
$T_j =$ 175 °C

$V_{GE} =$ 15 V

Figure 23 Output inverter FRED

Power dissipation as a function of heatsink temperature

$P_{tot} = f(T_h)$



At

$T_j =$ 175 °C

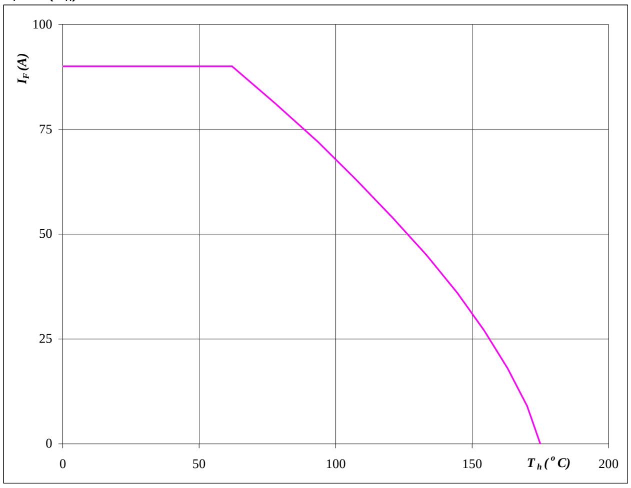
single heating

overall heating

Figure 24 Output inverter FRED

Forward current as a function of heatsink temperature

$I_F = f(T_h)$



At

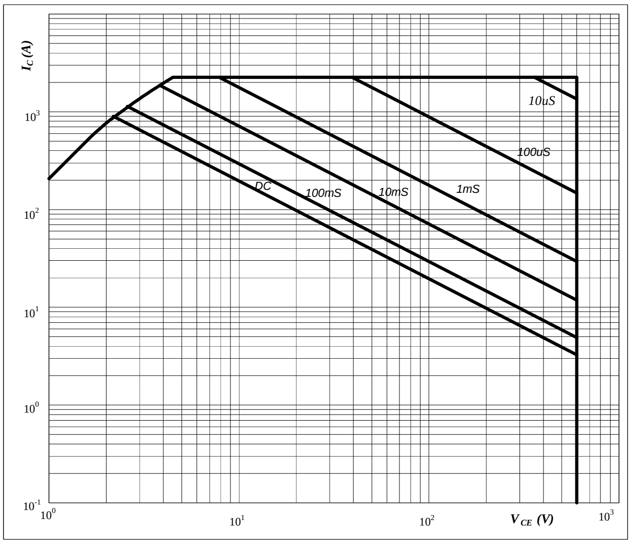
$T_j =$ 175 °C

Output Inverter

Figure 25	Output inverter IGBT

Safe operating area as a function of collector-emitter voltage

$I_C = f(V_{CE})$



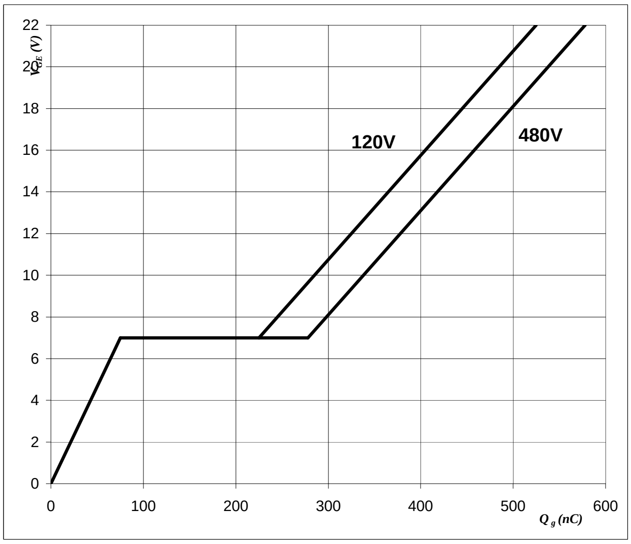
At

D =	single pulse	
T_h =	80	ºC
V_{GE} =	±15	V
T_j =	T_{jmax}	ºC

Figure 26	Output inverter IGBT

Gate voltage vs Gate charge

$V_{GE} = f(Q_{GE})$



At

I_C =	75	A

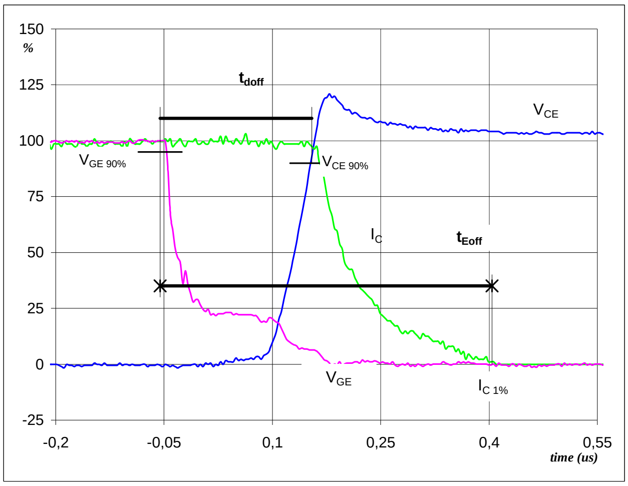
Switching Definitions Output Inverter

General conditions

T_j	=	150 °C
R_{gon}	=	2 Ω
R_{goff}	=	2 Ω

Figure 1 Output inverter IGBT

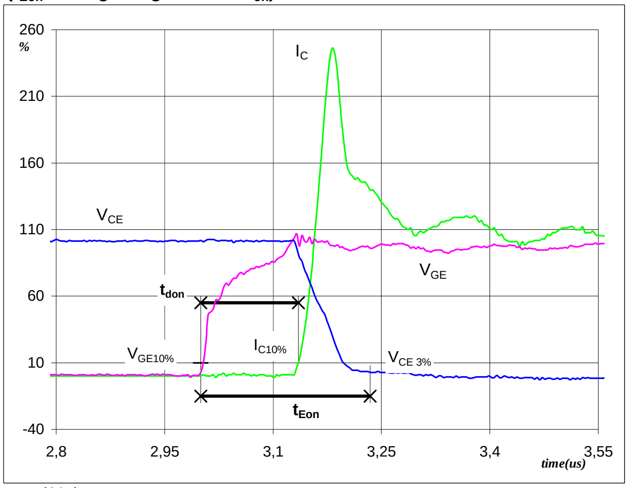
Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff}
(t_{Eoff} = integrating time for E_{off})



V_{GE} (0%) =	-15	V
V_{GE} (100%) =	15	V
V_C (100%) =	300	V
I_C (100%) =	75	A
t_{doff} =	0,20	µs
t_{Eoff} =	0,46	µs

Figure 2 Output inverter IGBT

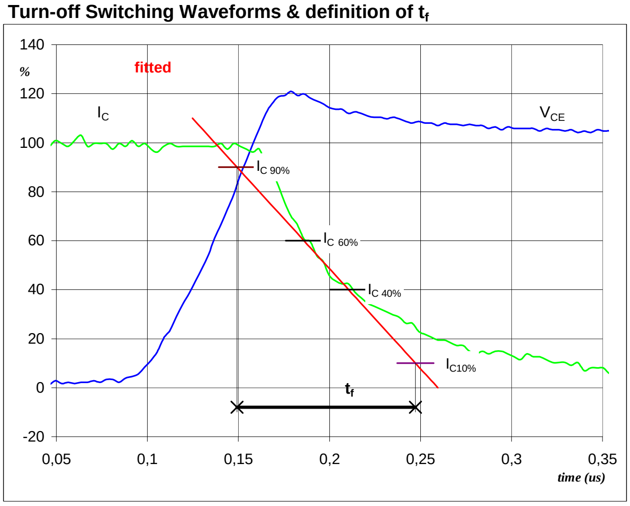
Turn-on Switching Waveforms & definition of tdon, t_{Eon}
(t_{Eon} = integrating time for E_{on})



V_{GE} (0%) =	-15	V
V_{GE} (100%) =	15	V
V_C (100%) =	300	V
I_C (100%) =	75	A
t_{don} =	0,13	µs
t_{Eon} =	0,23	µs

Figure 3 Output inverter IGBT

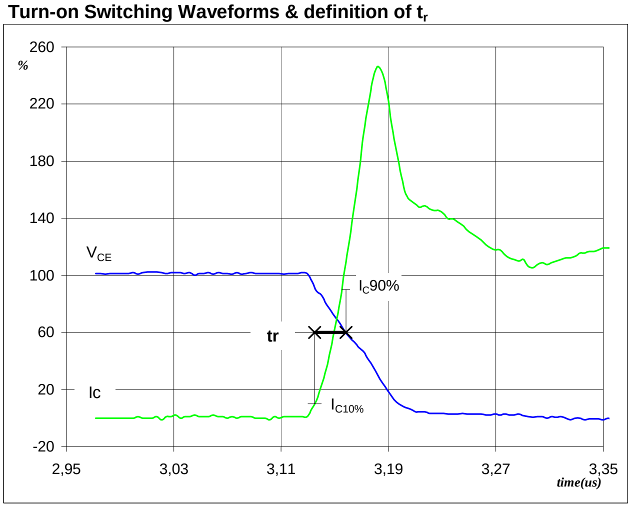
Turn-off Switching Waveforms & definition of t_f



V_C (100%) =	300	V
I_C (100%) =	75	A
t_f =	0,10	µs

Figure 4 Output inverter IGBT

Turn-on Switching Waveforms & definition of t_r

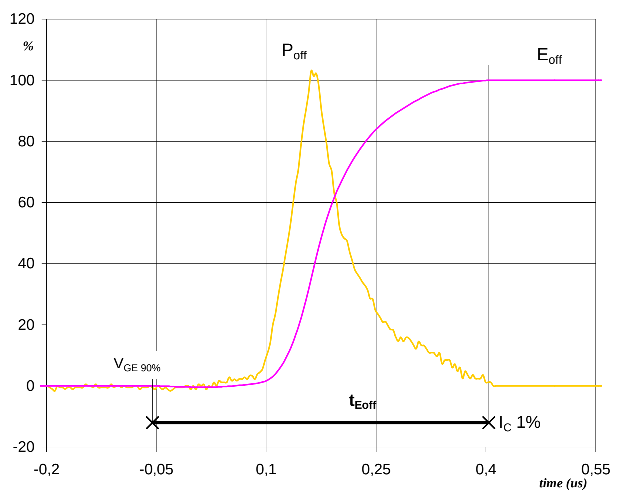


V_C (100%) =	300	V
I_C (100%) =	75	A
t_r =	0,02	µs

Switching Definitions Output Inverter

Figure 5 Output inverter IGBT

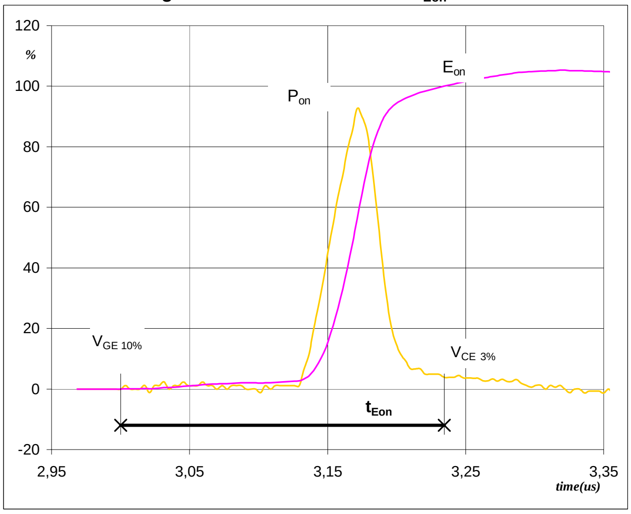
Turn-off Switching Waveforms & definition of t_{Eoff}



P_{off} (100%) =	22,63	kW
E_{off} (100%) =	2,27	mJ
t_{Eoff} =	0,46	µs

Figure 6 Output inverter IGBT

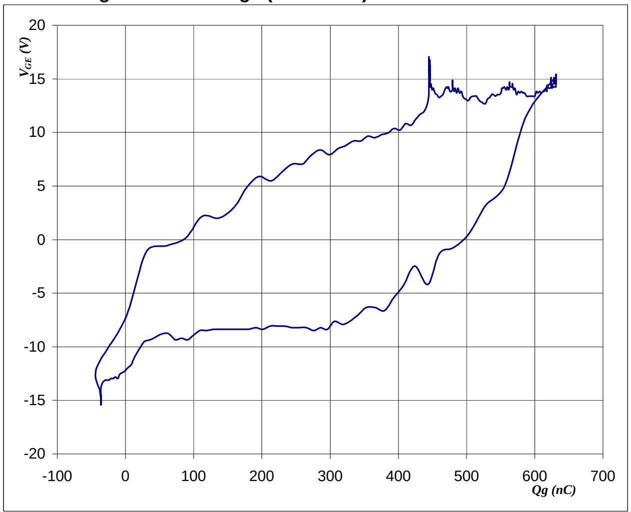
Turn-on Switching Waveforms & definition of t_{Eon}



P_{on} (100%) =	22,63	kW
E_{on} (100%) =	0,89	mJ
t_{Eon} =	0,23	µs

Figure 7 Output inverter FRED

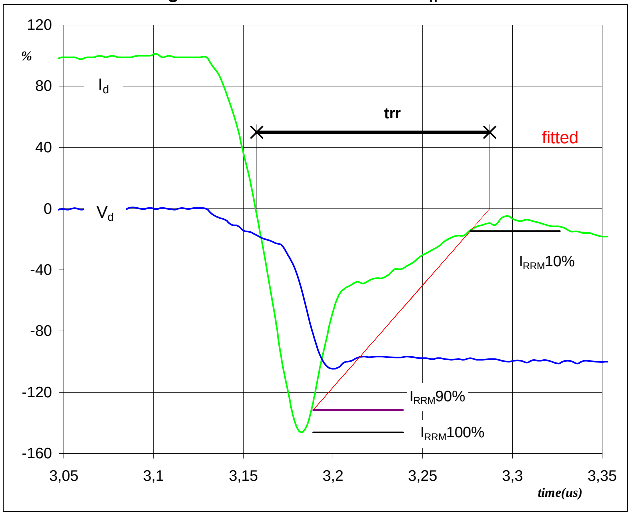
Gate voltage vs Gate charge (measured)



V_{GEoff} =	-15	V
V_{GEon} =	15	V
V_C (100%) =	300	V
I_C (100%) =	75	A
Q_g =	12785,80	nC

Figure 8 Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{rr}

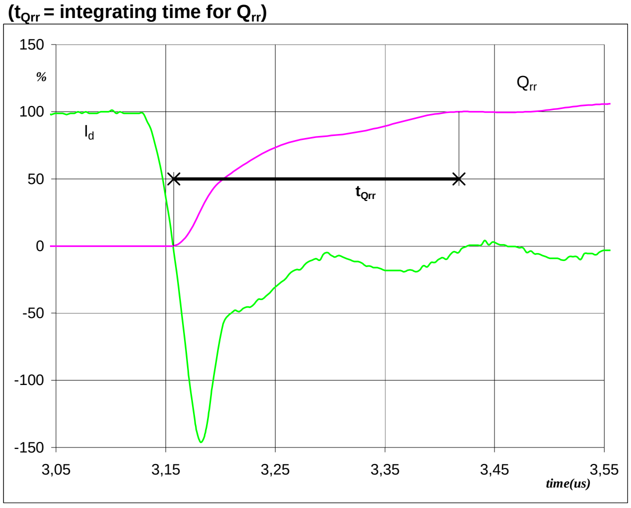


V_d (100%) =	300	V
I_d (100%) =	75	A
I_{RRM} (100%) =	-110	A
t_{rr} =	0,13	µs

Switching Definitions Output Inverter

Figure 9 Output inverter FRED

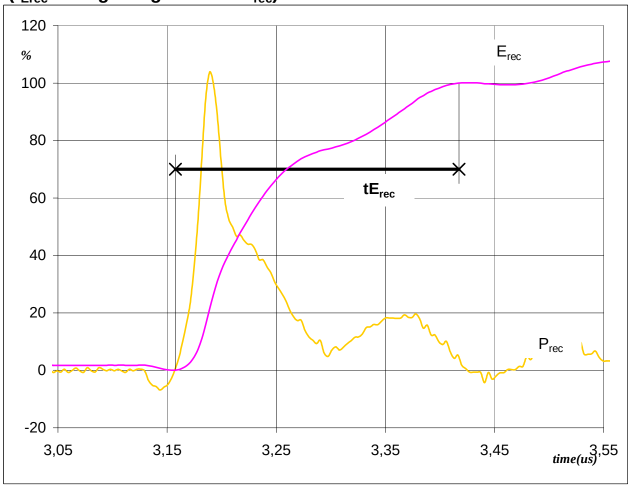
Turn-on Switching Waveforms & definition of t_{Qrr}
(t_{Qrr} = integrating time for Q_{rr})



I_d (100%) =	75	A
Q_{rr} (100%) =	6,46	μC
t_{Qrr} =	0,26	μs

Figure 10 Output inverter FRED

Turn-on Switching Waveforms & definition of t_{Erec}
(t_{Erec}= integrating time for E_{rec})



P_{rec} (100%) =	22,63	kW
E_{rec} (100%) =	1,54	mJ
t_{Erec} =	0,26	μs

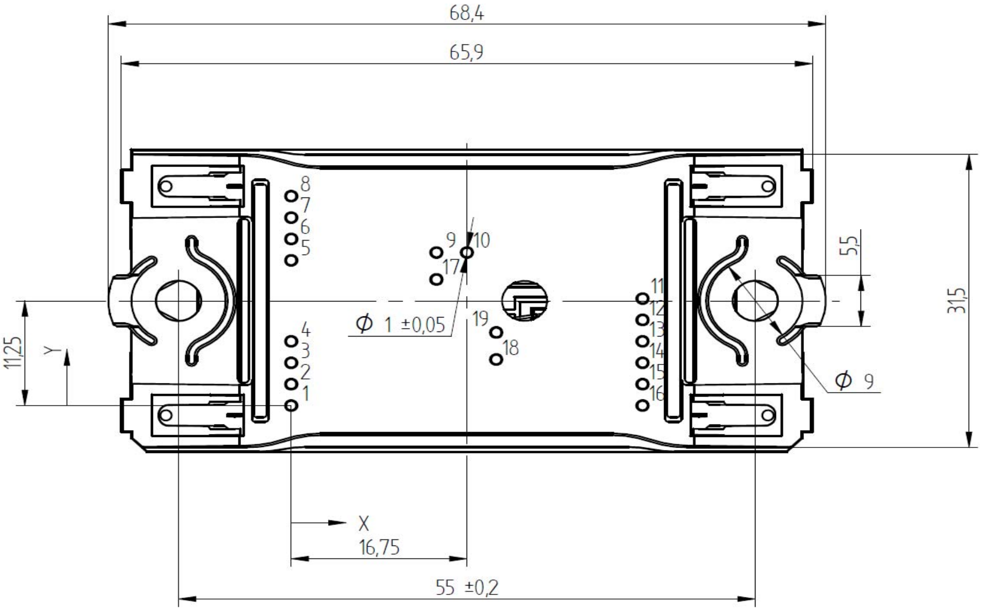
Ordering Code and Marking - Outline - Pinout

Ordering Code & Marking

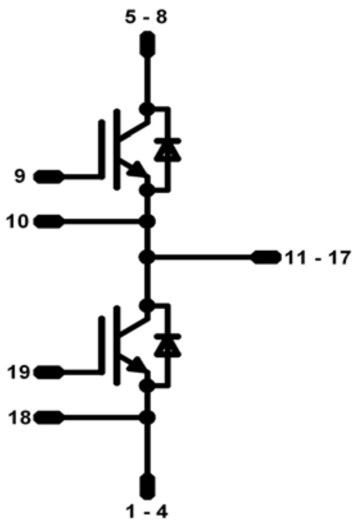
Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste 12mm housing	10-FZ062PA075SA01-P993F18	P993F18	P993F18
without thermal paste 17mm housing	10-F0062PA075SA01-P993F19	P993F19	P993F19

Outline

Pin table		
Pin	X	Y
1	0	0
2	0	2,3
3	0	4,6
4	0	6,9
5	0	15,6
6	0	17,9
7	0	20,2
8	0	22,5
9	13,85	16,45
10	16,75	16,45
11	33,5	11,5
12	33,5	9,2
13	33,5	6,9
14	33,5	4,6
15	33,5	2,3
16	33,5	0
17	13,85	13,55
18	19,55	4,95
19	19,55	7,85



Pinout



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data may be published at a later date. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.
Final	Full Production	This datasheet contains final specifications. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.

LIFE SUPPORT POLICY

Vincotech products are not authorised for use as critical components in life support devices or systems without the express written approval of Vincotech.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.