



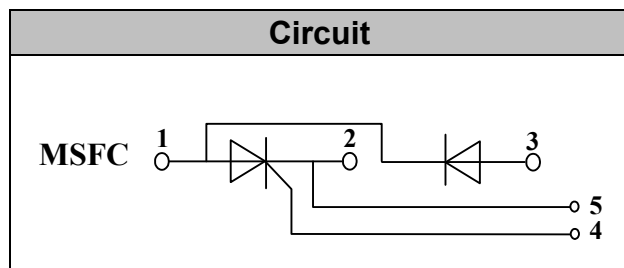
Thyristor/Diode Modules

V_{RRM} / V_{DRM} 800 to 1600V
I_{FAV} / I_{TAV} 90Amp

Applications

- Power Converters
- Lighting Control
- DC Motor Control and Drives
- Heat and temperature control

Circuit



Features

- International standard package
- High Surge Capability
- Glass passivated chip
- Simple Mounting
- Heat transfer through aluminum oxide DBC ceramic isolated metal baseplate
- UL E243882 approved

Module Type

TYPE	V _{RRM} /V _{DRM}	V _{RSM}
MSFC90-08	800V	900V
MSFC90-12	1200V	1300V
MSFC90-16	1600V	1700V

◆Diode

Maximum Ratings

Symbol	Item	Conditions	Values	Units
I _D	Output Current(D.C.)	T _c =85°C	90	A
I _{FSM}	Surge forward current	t=10ms T _{vj} =45°C	2000	A
i ² t	Circuit Fusing Consideration		20000	A ² s
Visol	Isolation Breakdown Voltage(R.M.S)	a.c.50HZ;r.m.s.;1min	3000	V
T _{vj}	Operating Junction Temperature		-40 to +125	°C
T _{stg}	Storage Temperature		-40 to +125	°C
M _t	Mounting Torque	To terminals(M5)	3±15%	Nm
M _s		To heatsink(M6)	5±15%	Nm
Weight	Module (Approximately)		100	g

Thermal Characteristics

Symbol	Item	Conditions	Values	Units
R _{th(j-c)}	Thermal Impedance, max.	Junction to Case	0.14	°C/W
R _{th(c-s)}	Thermal Impedance, max.	Case to Heatsink	0.10	°C/W

Electrical Characteristics

Symbol	Item	Conditions	Values			Units
			Min.	Typ.	Max.	
V _{FM}	Forward Voltage Drop, max.	T=25°C I _F =300A			1.65	V
I _{RRM}	Repetitive Peak Reverse Current, max.	T _{vj} =25°C V _{RD} =V _{RRM} T _{vj} =125°C V _{RD} =V _{RRM}	≤0.5 ≤6			mA mA

◆Thyristor

Maximum Ratings

Symbol	Item	Conditions	Values	Units
I_{TAV}	Average On-State Current	Sine 180°; $T_c=85^{\circ}\text{C}$	90	A
I_{TSM}	Surge On-State Current	$T_{VJ}=45^{\circ}\text{C}$ $t=10\text{ms}$, sine $T_{VJ}=125^{\circ}\text{C}$ $t=10\text{ms}$, sine	2000 1750	A
i^2t	Circuit Fusing Consideration	$T_{VJ}=45^{\circ}\text{C}$ $t=10\text{ms}$, sine $T_{VJ}=125^{\circ}\text{C}$ $t=10\text{ms}$, sine	20000 15000	A ² s
Visol	Isolation Breakdown Voltage(R.M.S)	a.c.50HZ;r.m.s.;1min	3000	V
T_{vj}	Operating Junction Temperature		-40 to +130	°C
T_{stg}	Storage Temperature		-40 to +125	°C
M_t	Mounting Torque	To terminals(M5)	$3 \pm 15\%$	Nm
M_s		To heatsink(M6)	$5 \pm 15\%$	Nm
di/dt	Critical Rate of Rise of On-State Current	$T_{VJ}=T_{VJM}$, $2/3V_{DRM}$, $I_G=500\text{mA}$ $Tr<0.5\mu\text{s}$, $tp>6\mu\text{s}$	150	A/ μs
dv/dt	Critical Rate of Rise of Off-State Voltage, min.	$T_J=T_{VJM}$, $2/3V_{DRM}$ linear voltage rise	1000	V/ μs
a	Maximum allowable acceleration		50	m/s^2

Thermal Characteristics

Symbol	Item	Conditions	Values	Units
$R_{th(j-c)}$	Thermal Impedance, max.	Junction to Case	0.28	°C/W
$R_{th(c-s)}$	Thermal Impedance, max.	Case to Heatsink	0.20	°C/W

Electrical Characteristics

Symbol	Item	Conditions	Values			Units
			Min.	Typ.	Max.	
V_{TM}	Peak On-State Voltage, max.	$T=25^{\circ}\text{C}$ $I_T=300\text{A}$			1.72	V
I_{RRM}/I_{DRM}	Repetitive Peak Reverse Current, max. / Repetitive Peak Off-State Current, max.	$T_{VJ}=T_{VJM}$, $V_R=V_{RRM}$, $V_D=V_{DRM}$			20	mA
V_{TO}	On state threshold voltage	For power-loss calculations only ($T_{VJ}=125^{\circ}\text{C}$)			0.9	V
r_T	Value of on-state slope resistance. max	$T_{VJ}=T_{VJM}$			2	mΩ
V_{GT}	Gate Trigger Voltage, max.	$T_{VJ}=25^{\circ}\text{C}$, $V_D=6\text{V}$			3	V
I_{GT}	Gate Trigger Current, max.	$T_{VJ}=25^{\circ}\text{C}$, $V_D=6\text{V}$			150	mA
V_{GD}	Non-triggering gate voltage, max.	$T_{VJ}=125^{\circ}\text{C}$, $V_D=2/3V_{DRM}$			0.25	V
I_{GD}	Non-triggering gate current, max.	$T_{VJ}=125^{\circ}\text{C}$, $V_D=2/3V_{DRM}$			6	mA
I_L	Latching current, max.	$T_{VJ}=25^{\circ}\text{C}$, $R_G=33\Omega$		300	600	mA
I_H	Holding current, max.	$T_{VJ}=25^{\circ}\text{C}$, $V_D=6\text{V}$		150	250	mA
tgd	Gate controlled delay time	$T_{VJ}=25^{\circ}\text{C}$, $I_G=1\text{A}$, $diG/dt=1\text{A}/\mu\text{s}$	1			us
tq	Circuit commutated turn-off time	$T_{VJ}=T_{VJM}$	100			us

Performance Curves

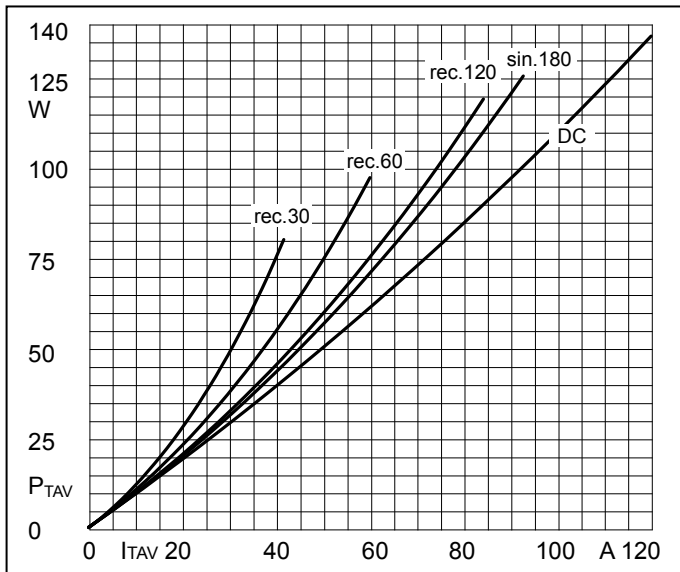


Fig1. Power dissipation

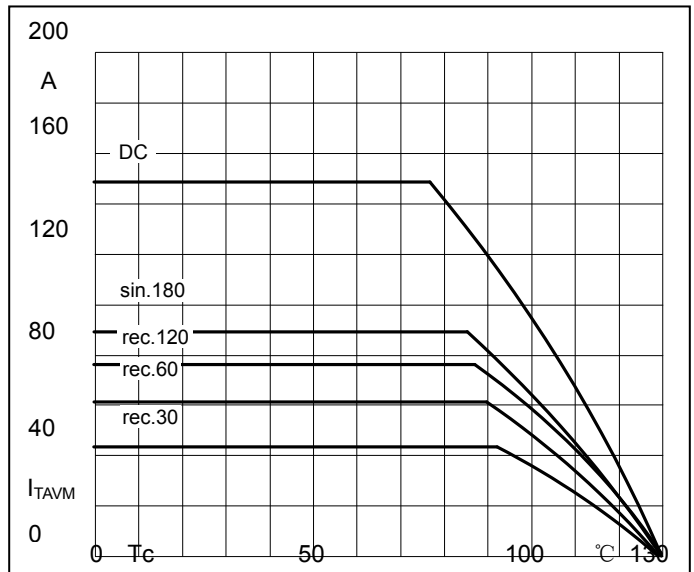


Fig2. Forward Current Derating Curve

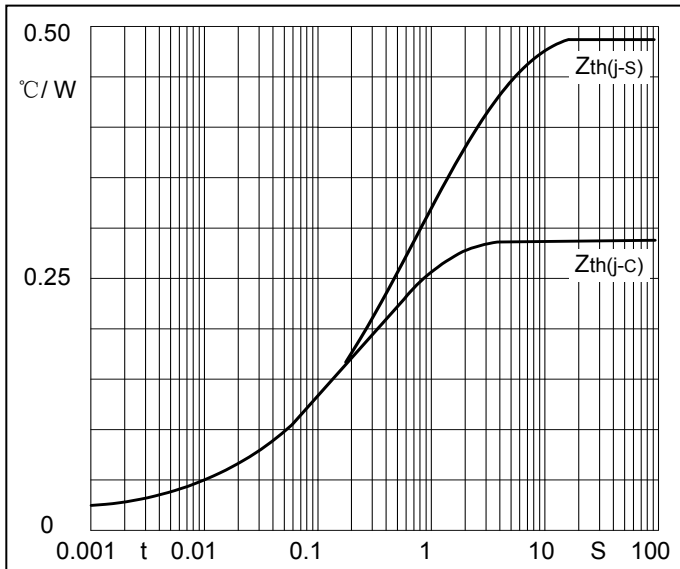


Fig3. Transient thermal impedance

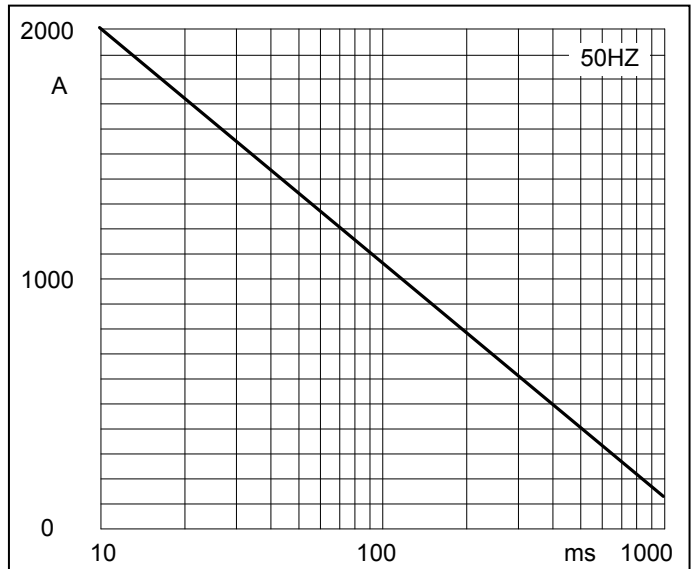


Fig4. Max Non-Repetitive Forward Surge Current

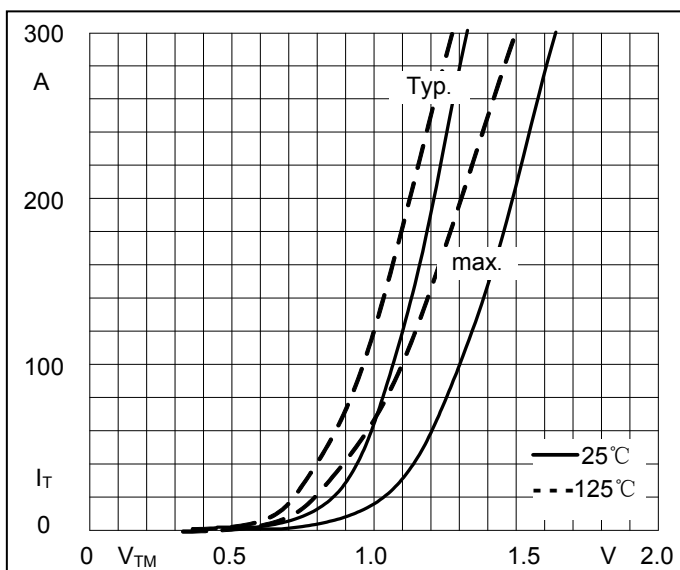


Fig5. Forward Characteristics

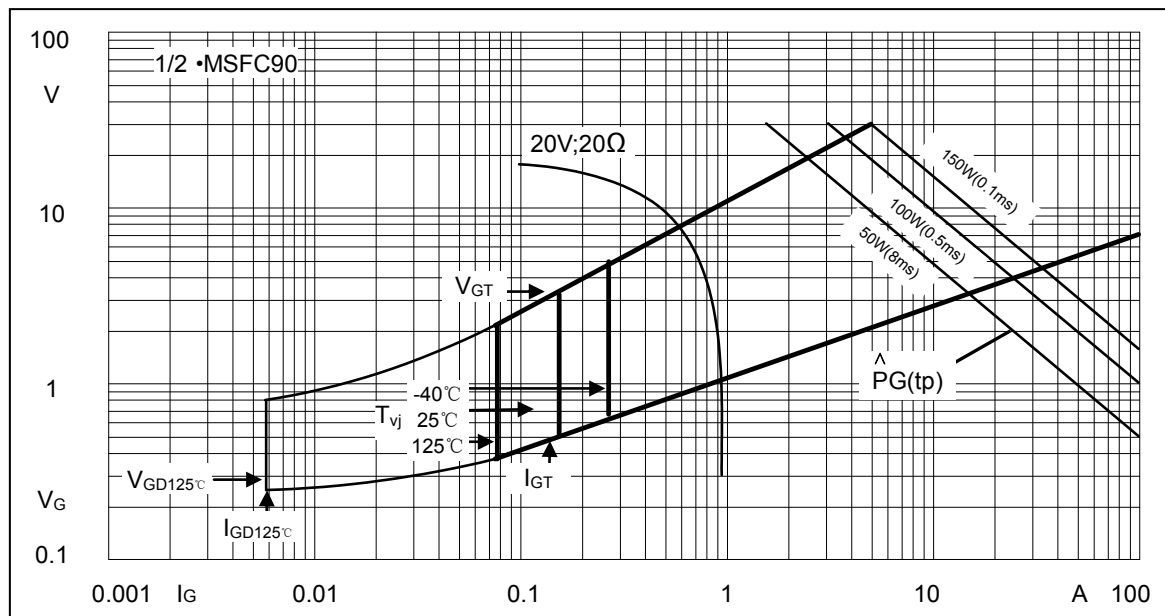
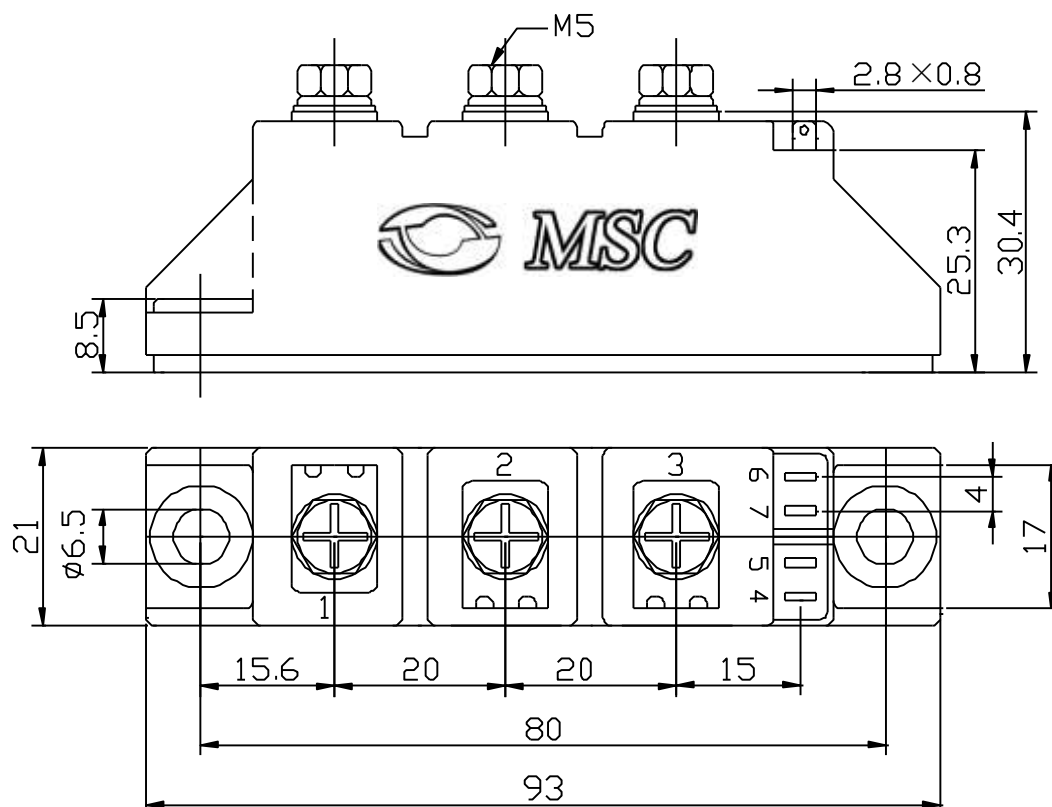


Fig6. Gate trigger Characteristics

Package Outline Information

CASE: F1



Dimensions in mm