

## Evaluation Board for the **AD5791** 1 ppm 20-Bit, $\pm 1$ LSB INL, Voltage Output DAC with Single and Dual Supply

### FEATURES

**Full-featured evaluation board for the **AD5791** with the **ADP5070** power solution**  
**Power Solution generated from single 3.3 V supply.**  
**PC control in conjunction with Analog Devices, Inc., **EVAL-SDP-CB1Z** SDP**  
**PC software for control**

### EVALUATION KIT CONTENTS

**EVAL-AD5791SDZ evaluation board**  
**EV-ADR445-REFZ reference board**  
**AD5791 evaluation software**

### GENERAL DESCRIPTION

This user guide supports the EVAL-AD5791SDZ evaluation board, Revision B. **UG-185** supports previous revisions of the evaluation board.

The EVAL-AD5791SDZ is a full-featured evaluation board, designed for users to easily evaluate all features of the **AD5791** voltage output, 20-bit digital-to-analog converter (DAC). The **AD5791** pins are accessible at on-board connectors for external connection. The evaluation board can be controlled by two means: via the on-board connector (J12) or via the system demonstration platform (SDP) connector (J14).

The evaluation board also integrates a power solution utilizing the **ADP5070** switching regulator and linear regulators (**ADP7118** and **ADP7182**) to generate a bipolar supply of up to  $-15$  V and  $+15$  V from a  $+3.3$  V single supply. Alternatively, the DAC can be supplied with linear power supplies via the on-board connectors (J11 and J13).

A daughter board connected to the top right of the EVAL-AD5791SDZ includes a voltage reference to externally apply to the DAC.

The **EVAL-SDP-CB1Z** SDP board allows the EVAL-AD5791SDZ evaluation board to be controlled through the USB port of a Windows®-based PC featuring Windows XP or later when using the **AD5791** evaluation software.

The **AD5791** is a high precision, 20-bit DAC, designed to meet the requirements of precision control applications. The output range of the **AD5791** is configured by two reference voltage inputs. The device is specified to operate with a dual power supply of up to 33 V.

Complete specifications for the **AD5791** are available in the **AD5791** data sheet available from Analog Devices, which should be consulted in conjunction with this user guide when using the evaluation board.

### EVALUATION BOARD PHOTOGRAPH



Figure 1.

TABLE OF CONTENTS

Features .....	1	Software Installation .....	8
General Description .....	1	Software Operation .....	8
Evaluation Board Photograph.....	1	Main Window .....	9
Revision History .....	2	Evaluation Board Schematics and Artwork.....	11
Evaluation Board Hardware.....	3	AD5791 Carrier Board .....	11
Power Supplies and Default Link Options .....	3	ADR445 Reference Voltage Daughter Board .....	18
Power Solution (ADP5070)—Single Supply Option .....	4	LTZ1000 Reference Voltage Daughter Board .....	20
Bench Power Supply—Dual Supply Option .....	4	Ordering Information.....	22
Voltage Reference Daughter Board .....	4	Bill of Materials.....	22
On-Board Connectors .....	7		
Evaluation Board Software .....	8		

REVISION HISTORY

1/2018—Revision 0: Initial Version

## EVALUATION BOARD HARDWARE

### POWER SUPPLIES AND DEFAULT LINK OPTIONS

The EVAL-AD5791SDZ evaluation board can be powered using the on-board **ADP5070** from a single voltage. The voltage sources available are an external single supply via the J11 connector and a supply sourced from the SDP controller board.

Alternatively, the J13 connector can provide power to the board, instead of the **ADP5070**, and it is intended for use with well-regulated bench supplies. See Figure 2 for a functional block diagram.

With any of the possible options, set the link options on the evaluation board for the required operating setup first, before supplying the evaluation board.

Each supply is decoupled to the relevant ground plane with 10  $\mu$ F and 0.1  $\mu$ F capacitors. Each device supply pin is again decoupled with a 10  $\mu$ F and 0.1  $\mu$ F capacitor pair to the relevant ground plane.

The analog and digital planes are connected at one location close to the DAC. To avoid ground loop problems, do not connect AGND and DGND elsewhere in the system.

**Table 1. Quick Start Jumper Configuration for **ADP5070** and Bench Supply**

Link No.	<b>ADP5070</b> with LDOs	<b>ADP5070</b>	Bench Supply
LK2	Inserted	Inserted	Removed
LK5	Removed	Inserted	Inserted
LK6	Removed	Inserted	N/A
LK7	Removed	Inserted	N/A
LK8	B	A	A
LK9	B	B	A
LK10	B	B	A

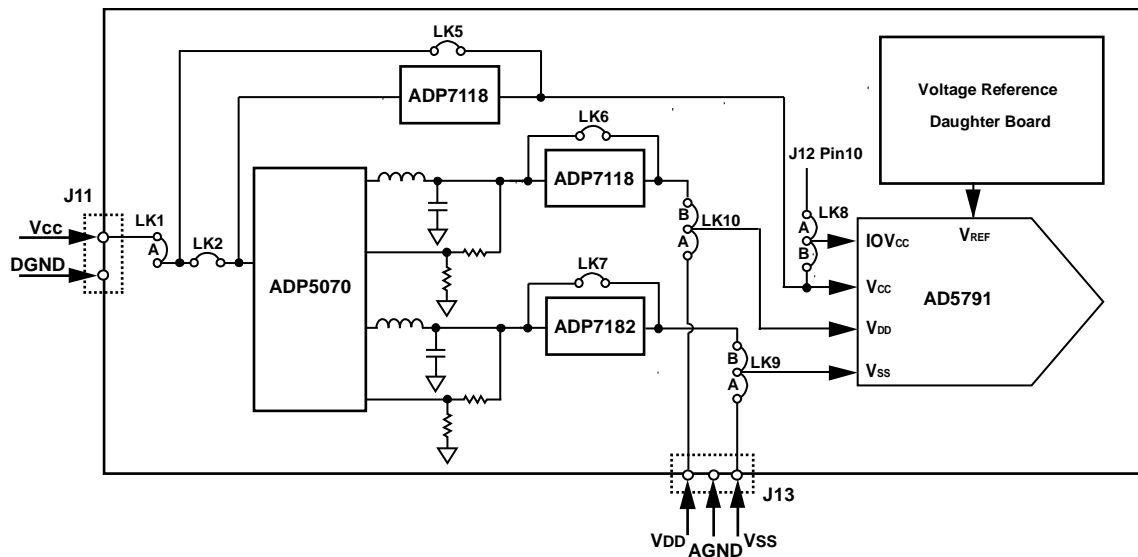


Figure 2. Powering the EVAL-AD5791SDZ Evaluation Board

POWER SOLUTION (ADP5070)—SINGLE-SUPPLY OPTION

The EVAL-AD5791SDZ board is populated with an ADP5070 switching regulator. This regulator is preceded by voltage regulators (ADP7118 and ADP7182) that can be bypassed if required. The supplies generated from the ADP5070 alone or with the addition of the voltage regulators are -15 V and +15 V from a +3.3 V single supply. Link LK6 and Link LK7 must be inserted when the voltage regulators are bypassed.

The circuit was designed using the Analog Devices ADIsimPower toolset, which selects the components, generates the schematic and bill of materials, and displays the performance specifications. Visit the ADP5070 product page to download the design tools.

The ADP5070 requires a minimum voltage supply of 3.3 V for correct operation. Following the jumper configuration in Table 1 for the ADP5070 alone or the ADP5070 with low dropout regulators (LDOs) options, the evaluation board is supplied via the on-board J11 connector with an external 3.3 V single supply.

The J11 connector can be supplied with a range of 3.3 V to 5.5 V when Link LK5 is inserted, or with a range of 3.3 V to 18 V when Link LK5 is removed.

Link LK1 must be inserted to Position A at all times. Refer to Table 3 for full link options.

BENCH POWER SUPPLY—DUAL SUPPLY OPTION

The evaluation board can be powered using a bench supply to allow all output voltage ranges of the AD5791. A headroom and footroom of at least 2.5 V is required on the dual supply. It is important that the voltage across the negative analog supply (VSS) and positive analog supply (VDD) does not exceed the absolute maximum rating of 34 V. Otherwise, device reliability can be affected.

Following the jumper configuration in Table 1 for the bench supply configuration, supply the evaluation board with a dual supply of the VSS = -15 V and the VDD = +15 V via the J13 connector. The AD5791 also requires a 3.3 V single supply to be applied to the VCC and IOVCC pins that can be sourced via the J11 connector or the on-board J12 connector. Select the position of Link LK8 depending on the preferred source to supply the VCC pin and the IOVCC pin. Select the position of Link LK1 to Position A at all times. Refer to Table 3 for full link options.

VOLTAGE REFERENCE DAUGHTER BOARD

The daughter board inserted into Connector J1, Connector J4, and Connector J9 (available at the top right corner of the EVAL-AD5791SDZ evaluation board) includes a voltage reference. The voltage supplied by the voltage reference is gained up and inverted to provide the positive and negative reference voltages required by the AD5791, which are routed to the EVAL-AD5791SDZ board via the J4 connector.

ADR445 Reference Board

The EVAL-AD5791SDZ evaluation kit provides the EV-ADR445-REFZ reference board to complete the hardware required to evaluate the AD5791.

The ADR445 is a 5 V low noise reference with 3 ppm/°C maximum temperature drift and 2.25 μV p-p noise specifications across the operating temperature range. Figure 3 shows the typical integral nonlinearity (INL) performance.

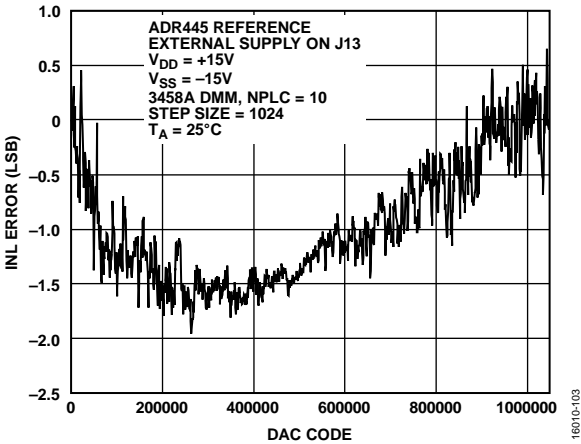


Figure 3. AD5791 with ADR445 INL Performance

Link JP1 selects the source of the reference voltage between the ADR445 and an external 5 V reference voltage applied at connector VR\_EXT. Refer to Table 2 for the link details.

Table 2. JP1 Link Reference Voltage Selection

JP1 Link Position	Reference Voltage Selection
A	ADR445
B	5 V external reference voltage applied at VR_EXT connector

### LTZ1000 Reference Board

The EV-LTZ1000-REFZ reference board, including the Linear Technology LTZ1000 voltage reference, is also available to evaluate the [AD5791](#).

The LTZ1000 reference board components maintain the 1 ppm accuracy of the [AD5791](#). The LTZ1000 is a 7.2 V ultraprecision reference specified with 0.05 ppm/°C temperature drift and ultralow 1.2  $\mu$ V p-p noise. The voltage reference is used in conjunction with low drift amplifiers ([ADA4077-2](#)) and a low drift, thermally matched resistor for the scaling and gain circuits. To reduce thermal errors due to air currents flowing over the reference board, it is recommended to place a cover over the reference board. Figure 4 shows the typical INL performance.

A minimum external unipolar supply of 3.3 V is required to supply the EVAL-AD5791SDZ and EV-LTZ1000-REFZ board combination. Alternatively, external dual supplies can be used to supply the motherboard and daughterboard.

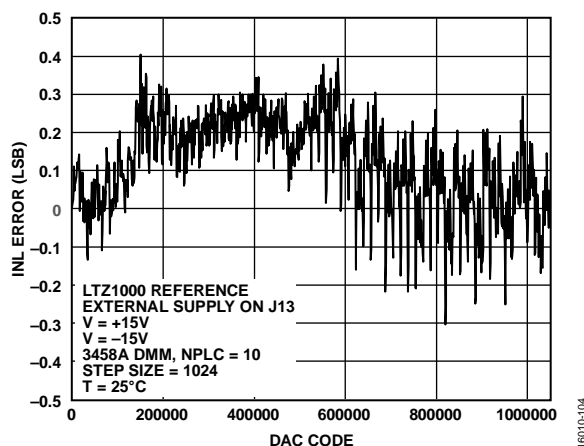


Figure 4. [AD5791](#) with LTZ1000 INL Performance

Table 3. Link Options

Link No.	Description
LK1	This link selects the source of the digital power supply from Connector J11. Position A must be selected at all times. Position A selects the source from the SDP board. Position B selects the source from Connector J12.
LK2	This link selects whether or not the power solution on the board is used to supply the AD5791. When this link is inserted, the ADP5070 dc-to-dc switch is used to supply the AD5791 in single supply. When this link is removed, the ADP5070 dc-to-dc switch is bypassed.
LK3	This link selects the state of the LDAC pin. When this link is inserted, LDAC is at logic low. When this link is removed, LDAC is at logic high.
LK4	This link selects the state of the RESET pin. When this link is inserted, RESET is at logic low. When this link is removed, RESET is at logic high.
LK5	This link selects the voltage source for the digital supply V <sub>CC</sub> pin. When this link is inserted, the digital supply is sourced from an external 3.3 V single supply (Connector J11) with Link LK1 on position A. Note that the J11 connector can be supplied with a range of 3.3 V to 5.5 V when Link LK5 is inserted. When this link is removed, the digital supply is sourced from the adjusted voltage generated by the ADP5070 and regulated by the ADP7118. Note that the J11 connector can be supplied with a range of 3.3 V to 18 V when Link LK5 is removed.
LK6	This link selects whether the ADP7118 regulator is included in the V <sub>DD</sub> source circuit. When this link is inserted, the ADP7118 regulator is bypassed. When this link is removed, the ADP7118 regulator adjusts the positive analog supply generated by the ADP5070 power solution.
LK7	This link selects whether the ADP7182 regulator is included in the V <sub>SS</sub> source circuit. When this link is inserted, the ADP7182 regulator is bypassed. When this link is removed, the ADP7182 regulator adjusts the negative analog supply generated by the ADP5070 power solution.
LK8	This link selects the voltage source for the IOV <sub>CC</sub> pin. Position A selects an externally applied voltage at Pin 10 of Connector J12. Position B connects IOV <sub>CC</sub> to V <sub>CC</sub> .
LK9	This link selects the voltage source for the negative analog supply V <sub>SS</sub> . Position A selects the source from the voltage externally applied at V <sub>SS</sub> of Connector J13. Position B selects the source from the negative voltage generated by the ADP5070 and adjusted by an ADP7182 regulator, depending on the position of Link LK7.
LK10	This link selects the voltage source for the positive analog supply V <sub>DD</sub> . Position A selects the source from the voltage externally applied at V <sub>DD</sub> of Connector J13. Position B selects the source from the positive voltage generated by the ADP5070 and adjusted by an ADP7118 regulator, depending on the position of Link LK6.
LK11	This link selects the state of the CLR pin. When this link is inserted, CLR is at logic low. When this link is removed, CLR is at logic high.

## ON-BOARD CONNECTORS

Table 4 shows the connectors on the EVAL-AD5791SDZ.

**Table 4. On-Board Connectors**

Connector	Function
J1 to J9	Voltage reference daughter board connectors
J11	Digital power supply connector
J12	Digital interface pin header connector
J13	Analog power supply connector
J14	SDP board connector
VO	DAC output connector
VO_BUF	Buffered DAC output connector
VR_EXT	5 V voltage reference input connector

## Connector J12 Pin Descriptions

Figure 5 and Table 5 show the Connector J12 pins.

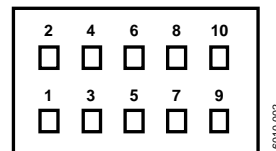


Figure 5. Connector J12 Pin Configuration

**Table 5. Connector J12 Pin Descriptions**

Pin No.	Description
1	CLR
2	$\overline{\text{LDAC}}$
3	$\overline{\text{RESET}}$
4	SCLK
5	SDIN
6	SDO
7	$\overline{\text{SYNC}}$
8	DGND
9	DGND
10	IOVCC

## EVALUATION BOARD SOFTWARE

### SOFTWARE INSTALLATION

The AD5791 evaluation kit includes self installing software on a CD. The software is compatible with Windows XP or later Windows based PCs. If the setup file does not run automatically, you can run **setup.exe** from the CD.

Install the evaluation software before connecting the evaluation board and SDP board to the USB port of the PC to ensure that the evaluation system is correctly recognized when connected to the PC.

1. After installation from the CD is complete, power up the AD5791 evaluation board as described in the Power Supplies and Default Link Options section. Connect the SDP board to the AD5791 evaluation board and then to the USB port of your PC using the supplied cable.
2. When the evaluation system is detected, proceed through any dialog boxes that appear. This completes the installation.

### SOFTWARE OPERATION

To launch the software, complete the following steps:

1. From the **Start** menu, select **Analog Devices – AD5791 > AD5791 Evaluation Software**. The main window of the software displays (see Figure 7).
2. If the evaluation system is not connected to the USB port when the software is launched, a connectivity error is displayed (see Figure 6). Connect the evaluation board to the USB port of the PC, wait a few seconds, and click **Rescan**. Follow the instructions.

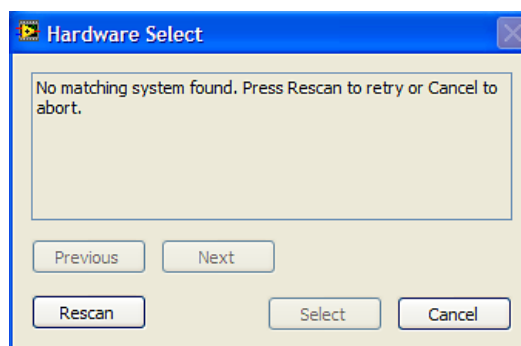


Figure 6. Connectivity Error Alert



Figure 7. Main Window

## MAIN WINDOW

The main window is divided into three tabs: **Configure**, **Program Voltage**, and **Measure DAC Output**.

### Configure

The **Configure** tab allows access to the control register, clearcode register, software control register, and DAC register, and also allows control of the RESET, CLR, and LDAC pins, as shown in Figure 7.

### Program Voltage

The **Program Voltage** tab programs the DAC register with a value calculated from the three entered values: the positive voltage reference (VREFP), the negative voltage reference (VREFN), and the desired output voltage input to the **Program Voltage** field, as shown in Figure 8.

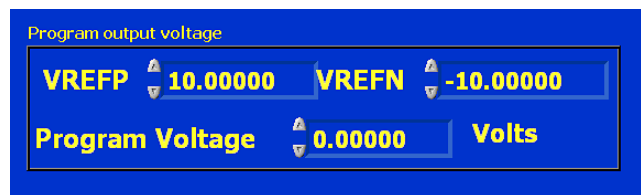


Figure 8. Program Voltage Window

### Measure DAC Output

The **Measure DAC Output** section allows the PC to control an Agilent 3458A multimeter to measure and log the DAC output voltage.

The multimeter is controlled over a general-purpose interface bus (GPIB). Once connected to the PC, the multimeter should first be configured via its front panel before taking a measurement. Figure 10 shows the measurement options. The software runs through a sequence of steps, programming the DAC register and measuring the DAC output voltage. The sequence begins with the software programming the DAC with the **Start Code** value, incrementing the programmed value at each step by the **Code Step** value, and finishing when the programmed value reaches the **Stop Code** value. A delay between measurements can be inserted if required. The GPIB address of the multimeter must be specified.

To begin the measurement, click the **START** button. The measurement can be halted at any time by clicking the **STOP** button. When the measurement is completed, a dialog box appears to allow the data to be saved as a spreadsheet file with three columns of data. The first column is DAC code, the second column is DAC voltage in volts, and the third column is INL error in least significant bits (LSBs), as shown in Figure 9. A graph of both DAC output voltage vs. DAC code and INL error vs. DAC code is displayed on screen. In the measurement example shown in Figure 10, measurements are taken in 1024 code steps beginning at Code 0 and finishing at Code 1,047,552, in total 1023 measurements. With the number of power line cycles (NPLC) setting on the multimeter set to 1, the measurement takes ~75 sec to complete. To complete an all codes measurement requiring 1,048,576 measurement points takes ~21 hours to complete.

	A	B	C
	0	-10.0002	0
	1024	-9.98066	0.120122
	2048	-9.96113	0.086736
	3072	-9.9416	0.13781
	4096	-9.92206	0.150508
	5120	-9.90253	0.109467
	6144	-9.883	0.183557
	7168	-9.86347	0.157878
	8192	-9.84394	0.201298
0	9216	-9.8244	0.252373
1	10240	-9.80487	0.242002
2	11264	-9.78534	0.085883
3	12288	-9.76581	0.090874
4	13312	-9.74628	-0.03458

Figure 9. Saved Data Format

If an Agilent 3458A multimeter is not connected to the PC, the software steps through the codes without taking any measurements.

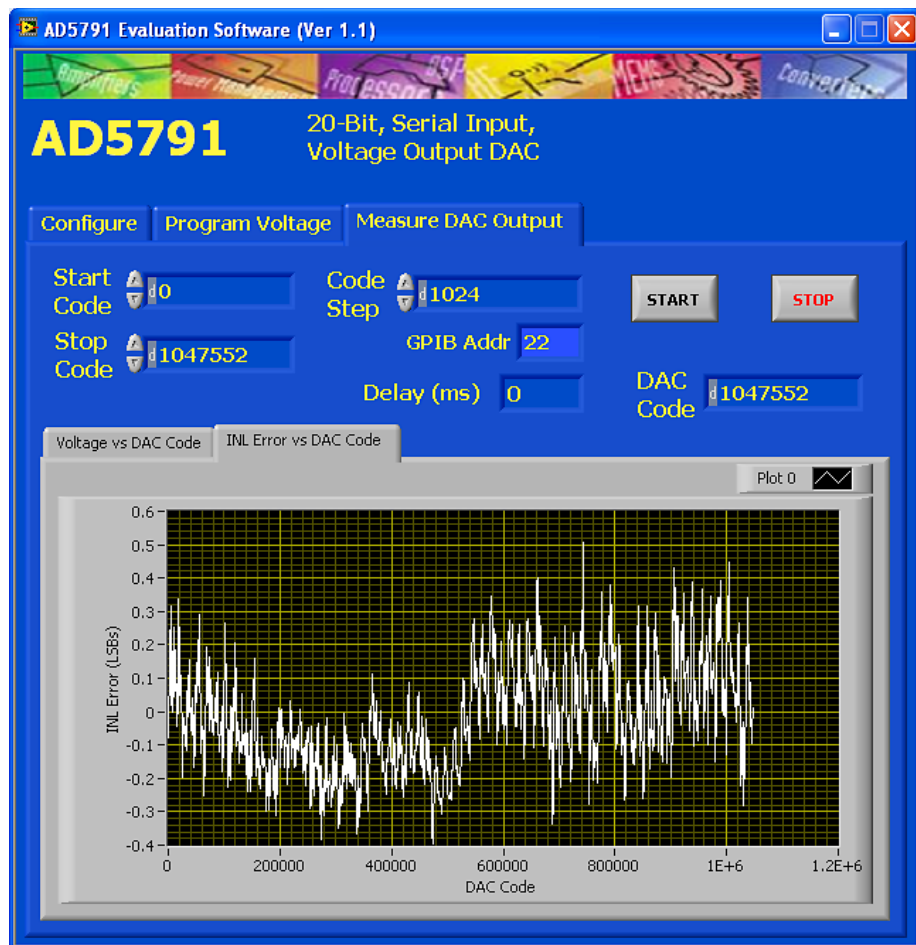


Figure 10. Measure DAC Output Window

## EVALUATION BOARD SCHEMATICS AND ARTWORK

## AD5791 CARRIER BOARD

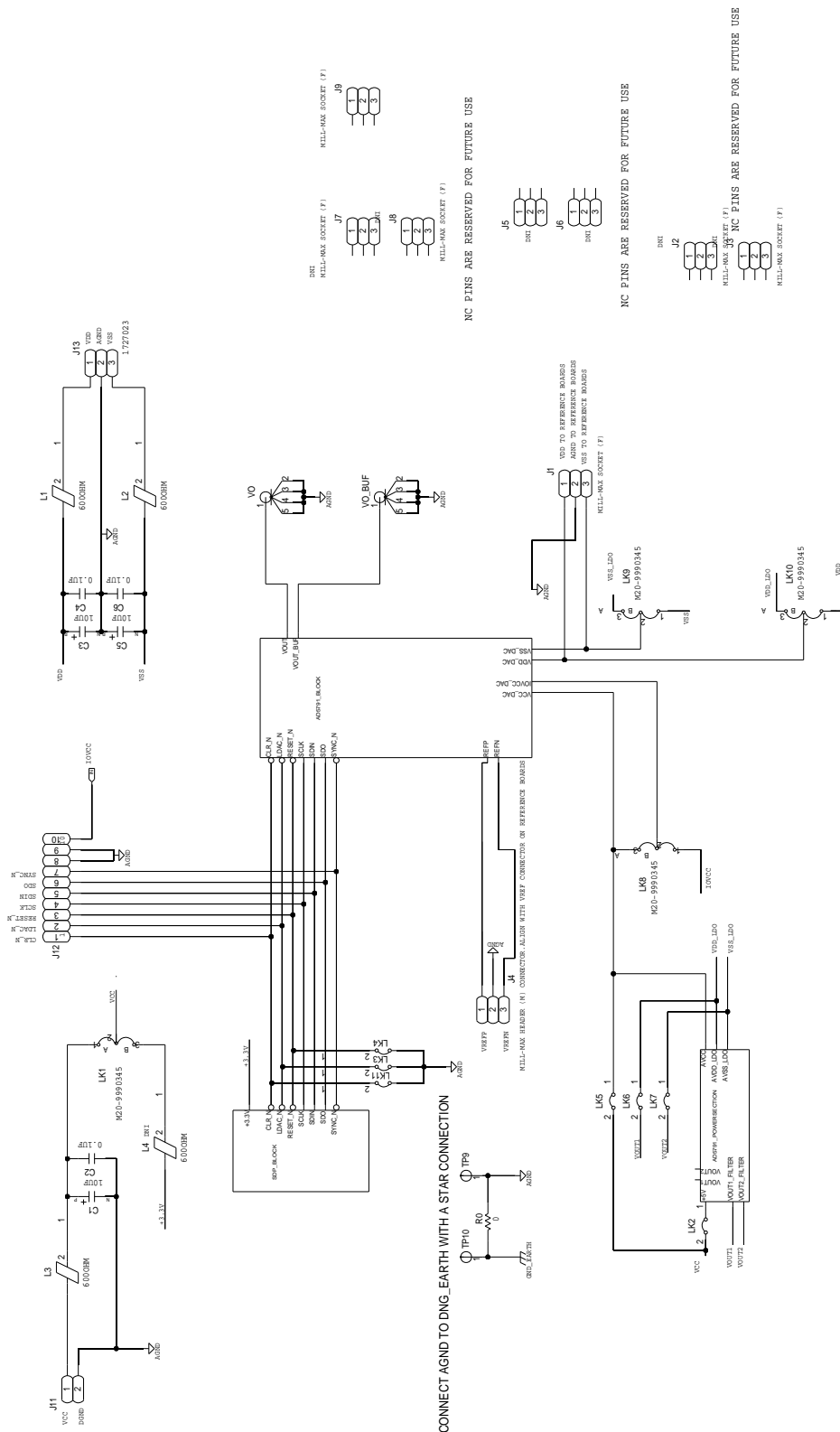


Figure 11. Schematic of the Main AD5791 Circuitry

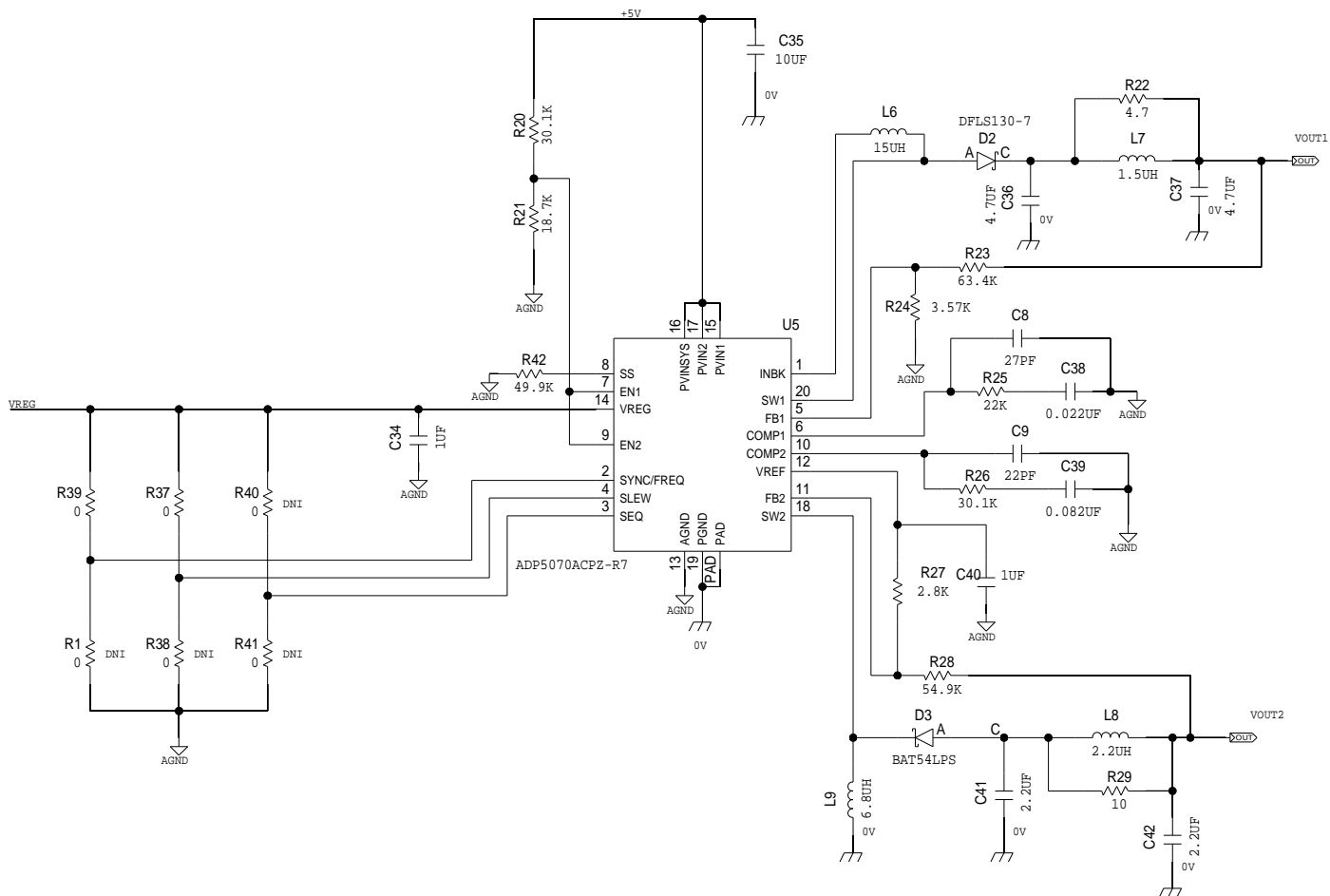


Figure 12. Schematic of the ADP5070 DC-to-DC Switch Circuitry

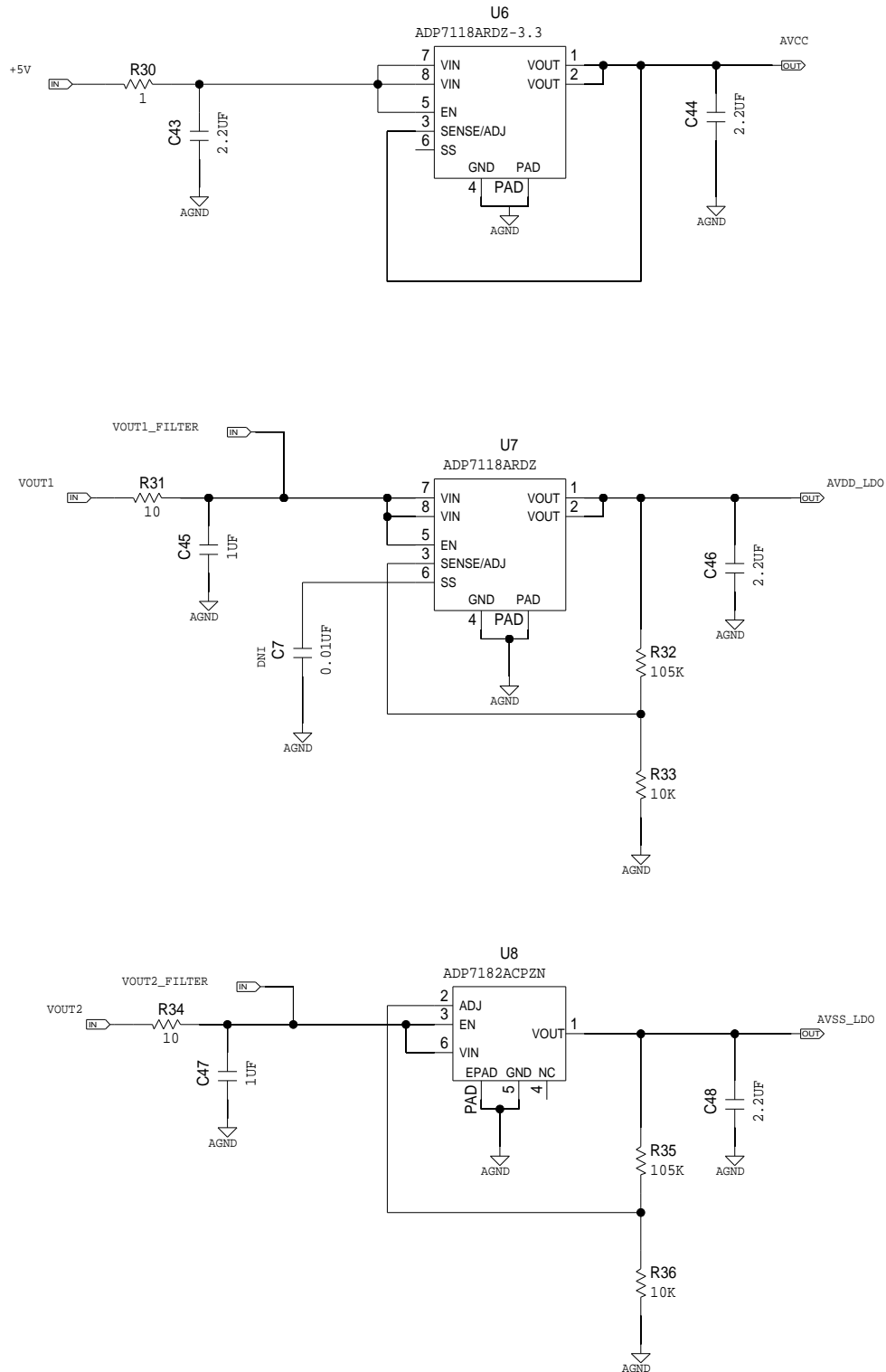


Figure 13. Schematic of the LDOs (ADP7118 and ADP7182) from the Power Solution Circuitry

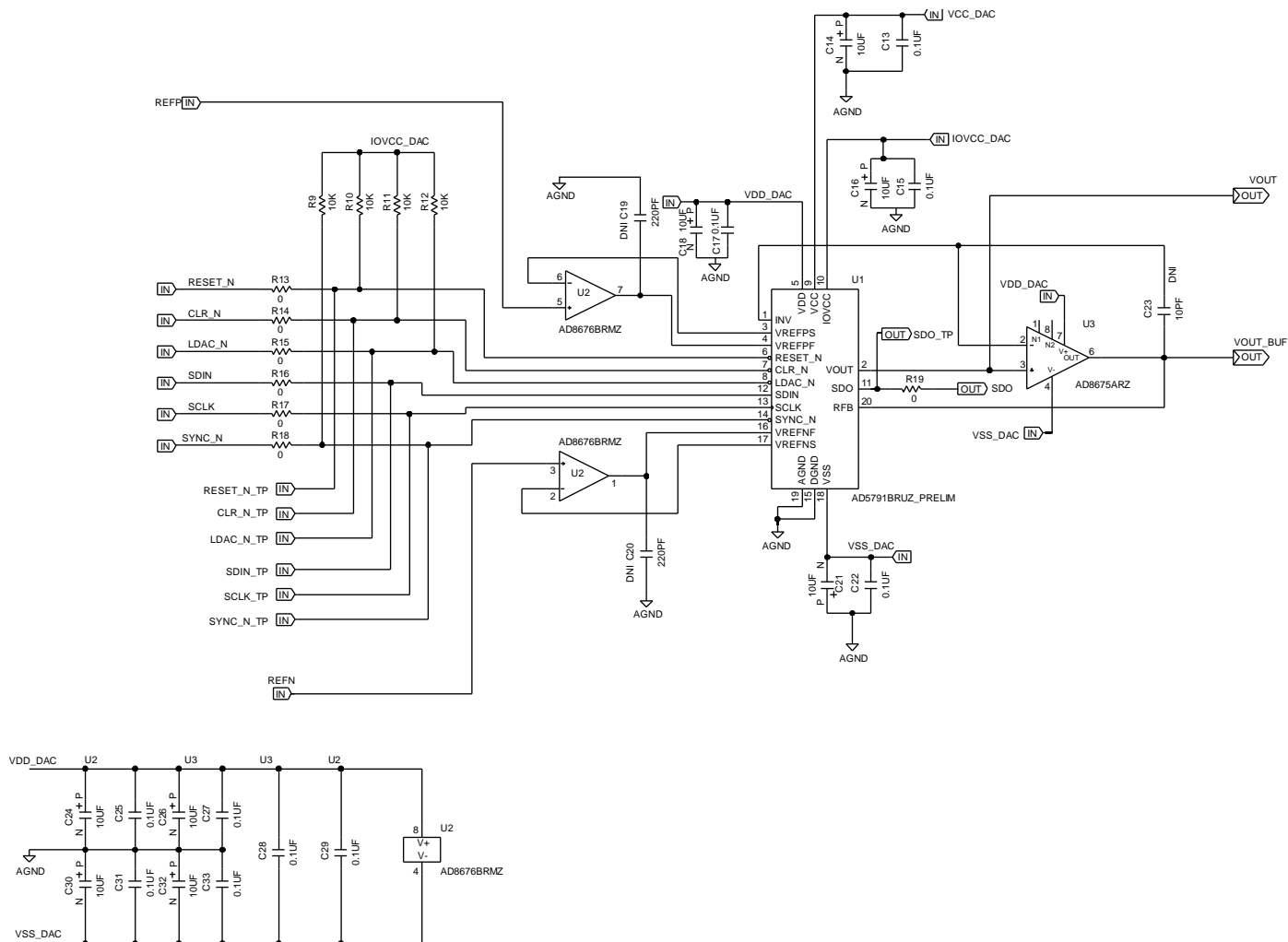


Figure 14. Schematic of the AD5791 Circuitry

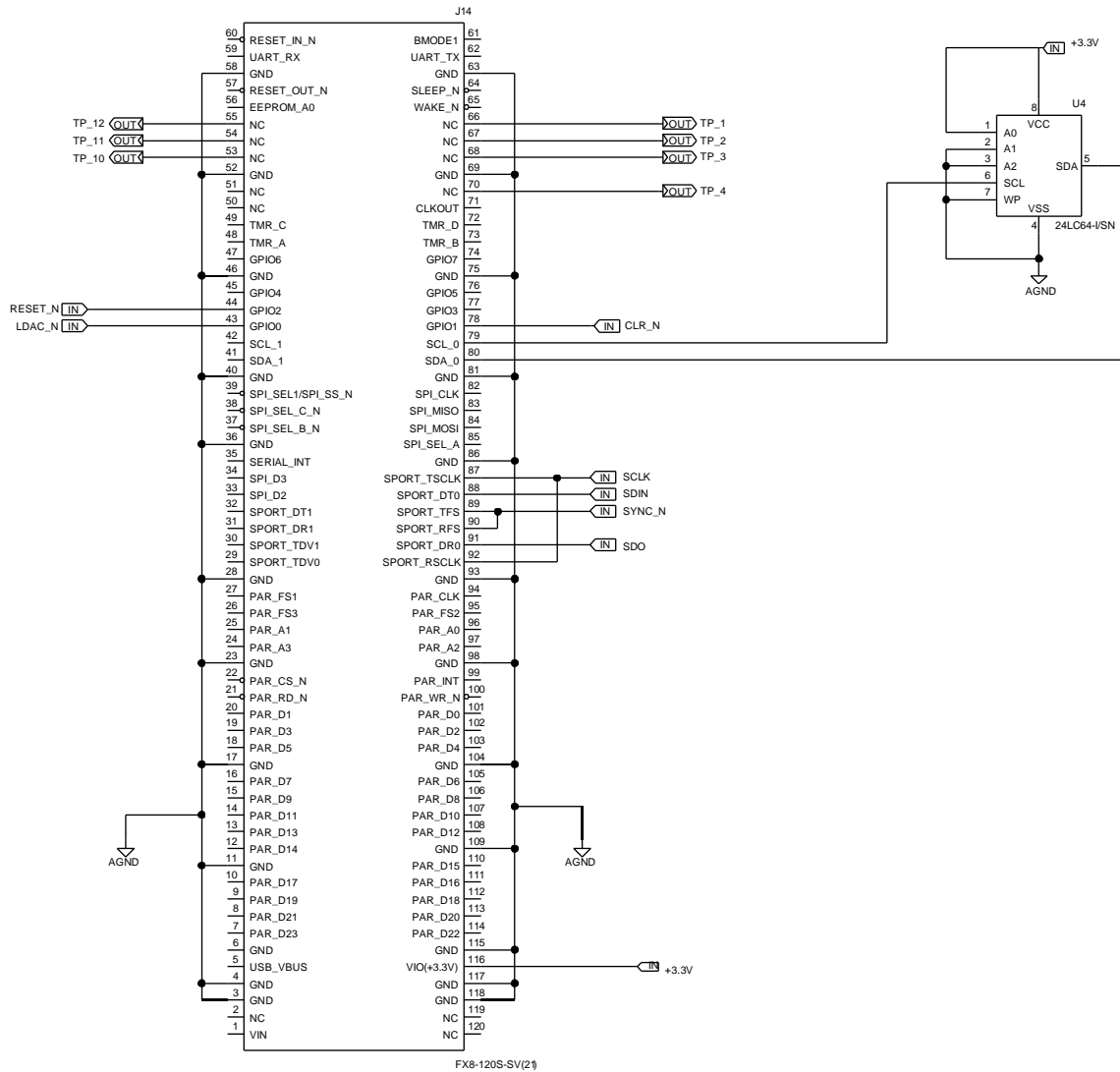
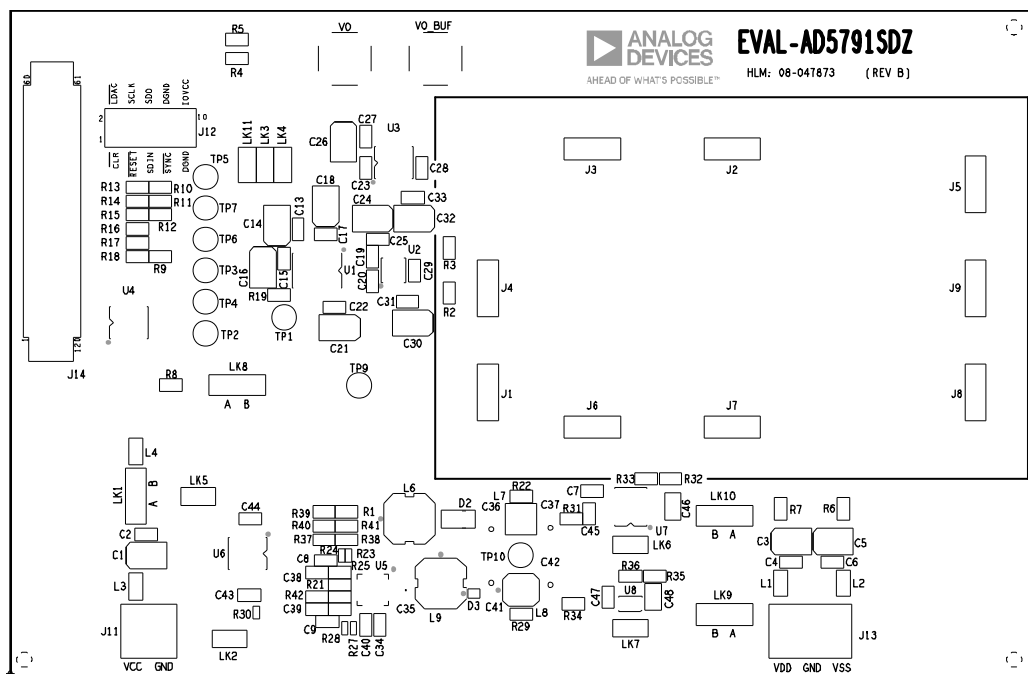


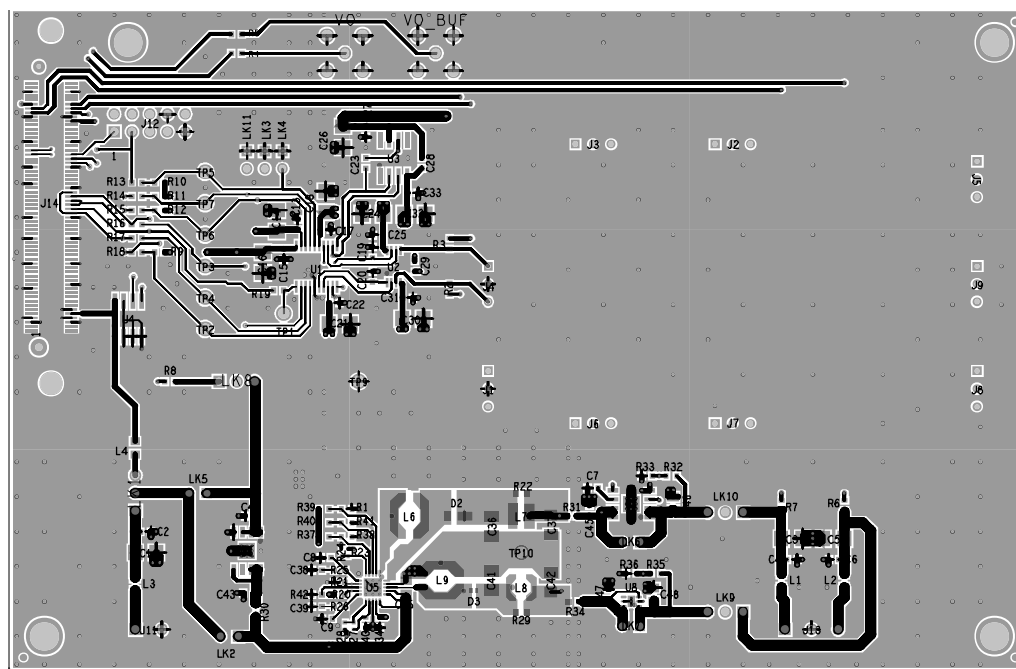
Figure 15. Schematic of the SDP Board Connector

16010-014



16010-015

Figure 16. Component Placement Schematic



16010-016

Figure 17. Top Printed Circuit Board (PCB) Layer Schematic

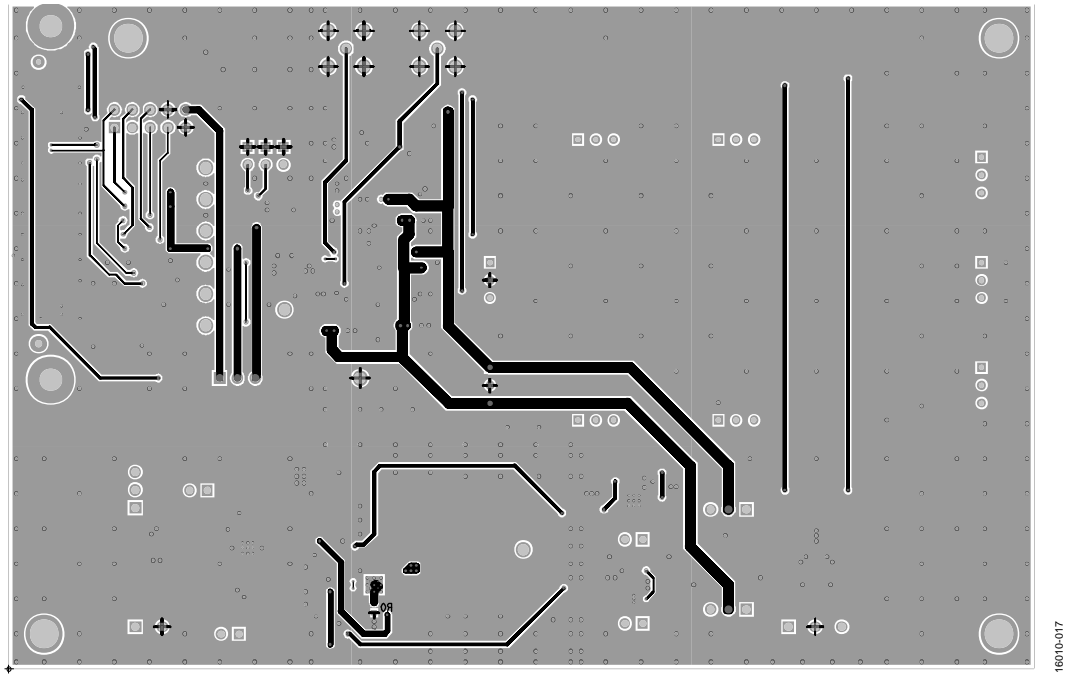
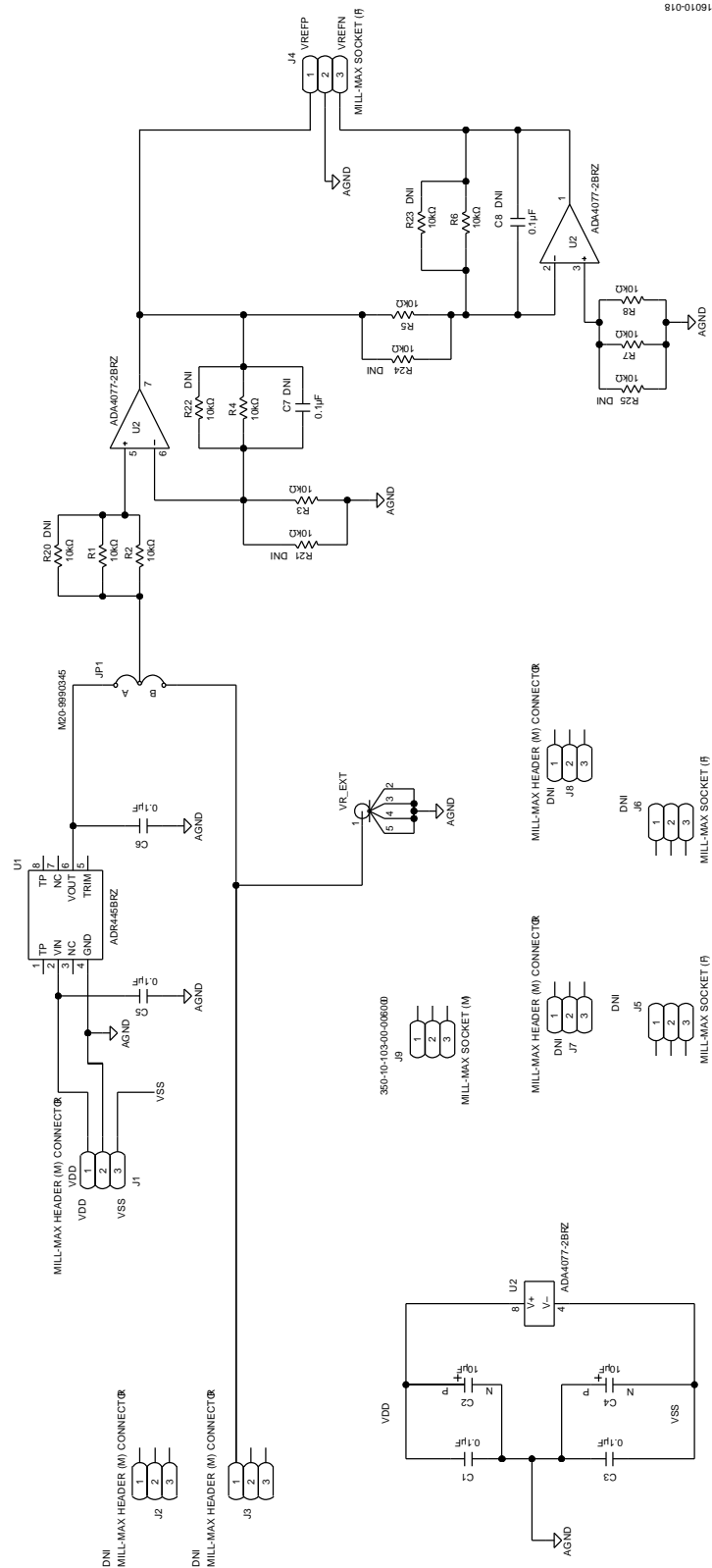
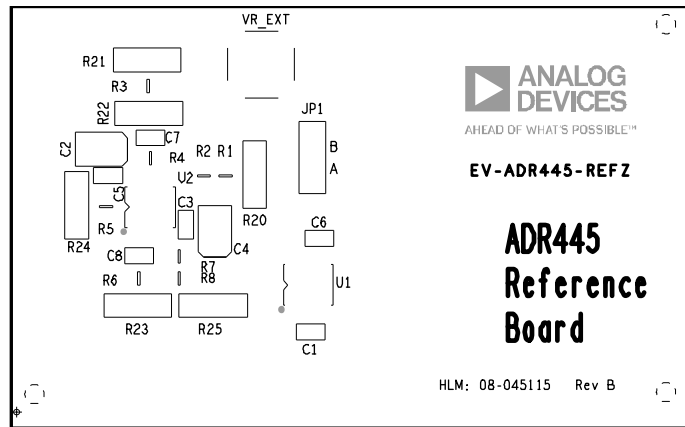


Figure 18. Bottom PCB Layer Schematic

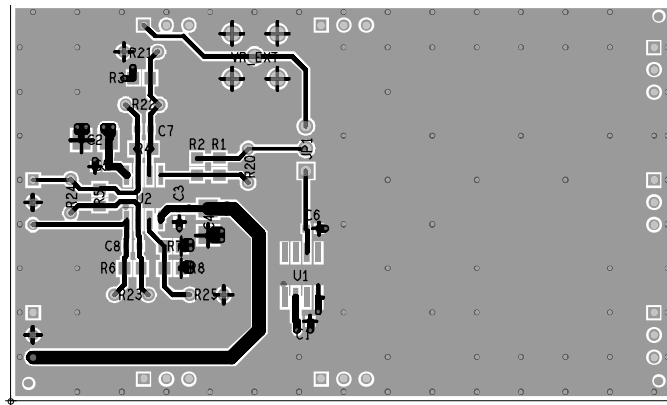
## ADR445 REFERENCE VOLTAGE DAUGHTER BOARD

Figure 19. Schematic of the [EV-ADR445-REF](#)



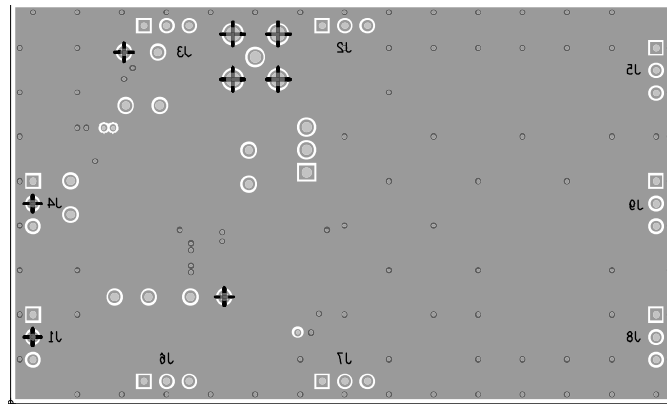
16010-020

Figure 20. EV-ADR445-REFZ Component Placement Schematic



16010-021

Figure 21. EV-ADR445-REFZ Top PCB Layer Schematic



16010-022

Figure 22. EV-ADR445-REFZ Bottom PCB Layer Schematic

## LTZ1000 REFERENCE VOLTAGE DAUGHTER BOARD

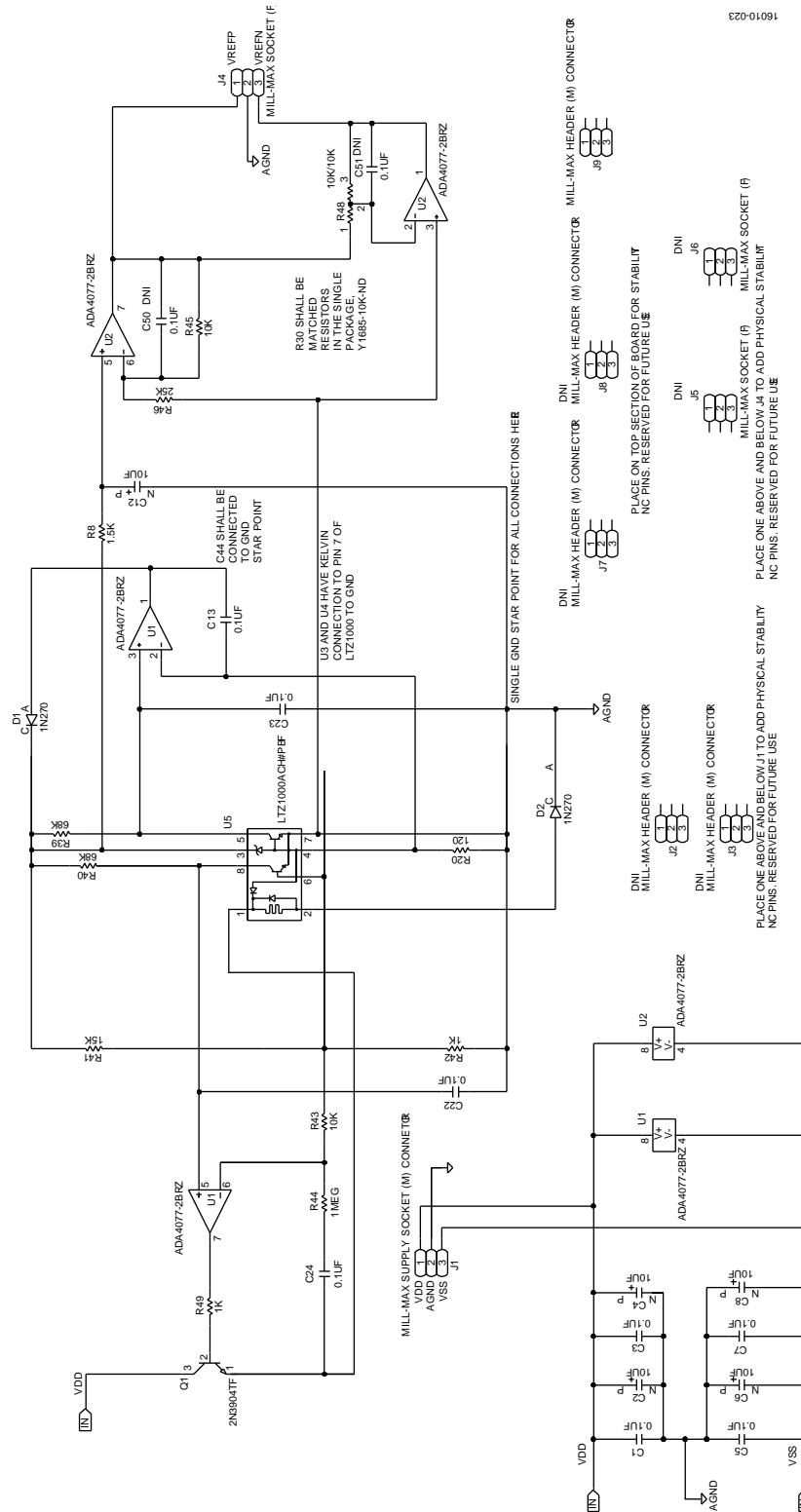


Figure 23. Schematic of the EV-LTZ1000-REFZ

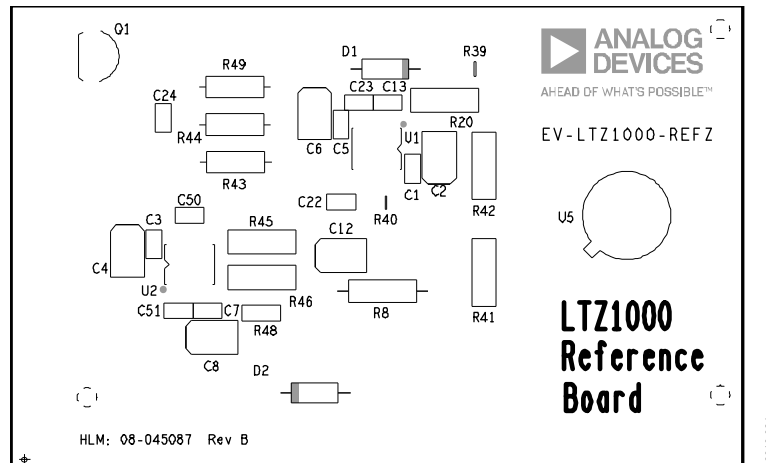


Figure 24. EV-LTZ1000-REFZ Component Placement Schematic

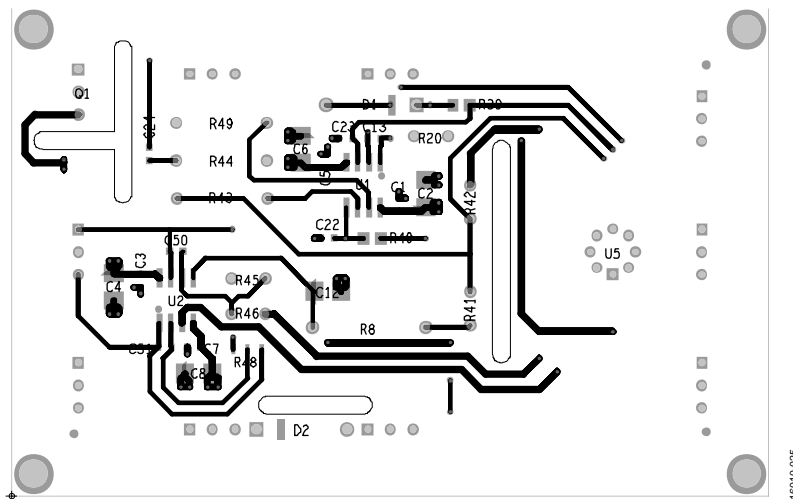


Figure 25. EV-LTZ1000-REFZ Top PCB Layer Schematic

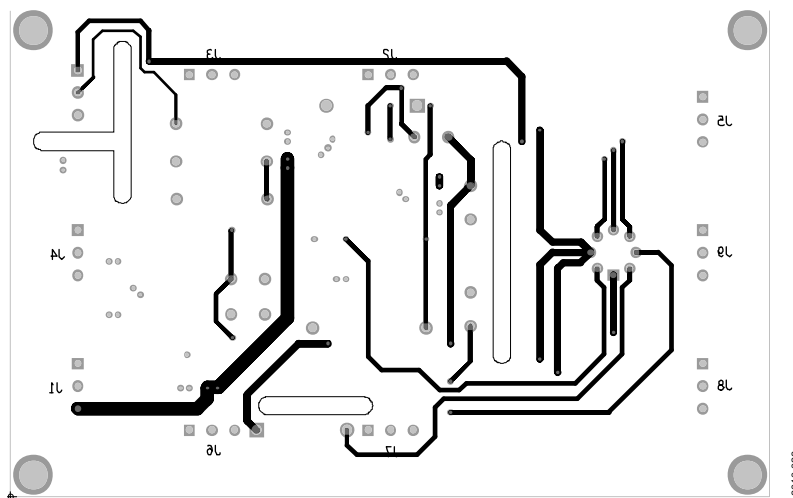


Figure 26. EV-LTZ1000-REFZ Bottom PCB Layer Schematic

## ORDERING INFORMATION

## BILL OF MATERIALS

Table 6. AD5791 Carrier Board

Reference Designator	Part Description	Part Number
C1, C3, C5, C14, C16, C18, C21, C24, C26, C30, C32	Capacitors, 3528, 10 $\mu$ F	TAJB106K016RNJ
C2, C4, C6, C13, C15, C17, C22, C25, C27 to C29, C31, C33	Capacitors, 0603, 25 V, 0.1 $\mu$ F, $\pm$ 10%	C1608X8R1E104K
C34, C40	Capacitors, 0603, 6.3 V, 1 $\mu$ F, $\pm$ 20%	JMK107B7105MA
C35	Capacitor, 0805, 10 V, 10 $\mu$ F, $\pm$ 20%	TACH106M010XTA
C36, C37	Capacitors, 1206, 50 V, 4.7 $\mu$ F, $\pm$ 20%	C3216X7R1H475M160AC
C38	Capacitor, 0603, 16 V, 0.022 $\mu$ F, $\pm$ 10%	0603YC223KAT2A
C39	Capacitor, 0603, 16 V, 0.082 $\mu$ F, $\pm$ 10%	CL10B823K08NNNC
C41, C42	Capacitors, 1206, 25 V, 2.2 $\mu$ F, $\pm$ 10%	C3216X7R1E225K
C43, C44	Capacitors, 0603, 10 V, 2.2 $\mu$ F, $\pm$ 10%	GRM188R71A225KE15D
C45, C47	Capacitors, 0603, 16 V, 1 $\mu$ F, $\pm$ 10%	GRM188R61C105KA93D
C46, C48	Capacitors, 0805, 25 V, 2.2 $\mu$ F, $\pm$ 10%	GRM21BR71E225KA73L
C8	Capacitor, 0603, 50 V, 27 pF, $\pm$ 5%	2238 867 15279
C9	Capacitor, 0603, 50 V, 22 pF, $\pm$ 5%	CC0603JRNPO9BN220
D2	Schottky diode	DFLS130-7
D3	Schottky diode	BAT54LPS
J1, J9	3-position female header, single-row connectors, 2.54 mm pitch	310-13-103-41-001000
J11	2-position terminal block (3.81 mm pitch)	1727010
J12	10-position male header connector (3.81 mm pitch)	M20-9980546
J13	3-pin terminal block (3.81 mm pitch)	1727023
J14	120-way connector, 0.6 mm pitch	FX8-120S-SV(21)
J4	3-position male header single row connector, 2.54 mm pitch	350-10-103-00-006000
L1, L2, L3	Surface-mount power inductors	7427920415
L6	Surface-mount power inductor	LPS6235-153MRB
L7	Surface-mount power inductor	XFL4020-152MEB
L8	Surface-mount power inductor	LPS4018-222MRB
L9	Surface-mount power inductor	LPS6235-682MRB
LK1, LK8, LK9, LK10	3-pin single-inline (SIL) header and shorting links	M20-9990345 + M7567-05
LK2 to LK7, LK11	2-contact headers, two rows, through hole	69157-102
R0, R13 to R19, R37, R39	Resistors, 0 $\Omega$ , 0.0625 W, 1%, 0603	MC0603WG00000T5E-TC
R9 to R12	Resistors, 10 k $\Omega$ , 0.0625 W, 1%, 0603	MC0063W0603110K
R20, R26	Resistors, 30.1 k $\Omega$ , 0.1 W, 1%, 0603	ERJ-3EKF3012V
R21	Resistor, 18.7 k $\Omega$ , 0.1 W, 1%, 0603	ERJ-3EKF1872V
R22	Resistor, 4.7 $\Omega$ , 0.1 W, 1%, 0603	ERJ-3RQF4R7V
R23	Resistor, 63.4 k $\Omega$ , 0.0625 W, 1%, 0402	ERJ-2RKF6342X
R24	Resistor, 3.57 k $\Omega$ , 0.1 W, 1%, 0402	ERJ-2RKF3571X
R25	Resistor, 22 k $\Omega$ , 0.1 W, 1%, 0603	CRCW060322K0FKEA
R27	Resistor, 2.8 k $\Omega$ , 0.1 W, 1%, 0402	ERJ-2RKF2801X
R28	Resistor, 54.9 k $\Omega$ , 0.1 W, 1%, 0402	ERJ-2RKF5492X
R29, R31, R34	Resistors, 10 $\Omega$ , 0.1 W, 1%, 0603	ERJ-3EKF10R0V
R30	Resistor, 1 $\Omega$ , 0.0625 W, 5%, 0402	CRCW04021R00JNED
R32, R35	Resistors, 105 k $\Omega$ , 0.1 W, 1%, 0603	ERJ-3EKF1053V
R33, R36	Resistors, 10 k $\Omega$ , 0.1 W, 1%, 0603	ERJ-3EKF1002V
R42	Resistor, 49.9 k $\Omega$ , 0.1 W, 1%, 0603	ERJ-3EKF4992V
TP1 to TP7, TP9, TP10	Red test points	TP-104-01-02

Reference Designator	Part Description	Part Number
U1	1 ppm 20-bit, $\pm 1$ LSB INL, voltage output DAC	<a href="#">AD5791BRUZ</a>
U2	Ultraprecision, 36 V, 2.8 nV/ $\sqrt{\text{Hz}}$ dual rail-to-rail output op amp	<a href="#">AD8676BRMZ</a>
U3	36 V precision, 2.8 nV/ $\sqrt{\text{Hz}}$ rail-to-rail output op amp	<a href="#">AD8675ARZ</a>
U4	64 kb I <sup>2</sup> C serial electrically erasable programmable read only memory (EEPROM)	24LC64-I/SN
U5	1 A/0.6 A dc to dcc switching regulator with independent positive and negative outputs	<a href="#">ADP5070ACPZ-R7</a>
U6	20 V, 200 mA, low noise, complementary metal-oxide-semiconductor (CMOS) LDO linear regulator, 3.3 V fixed output voltage	<a href="#">ADP7118ARDZ-3.3</a>
U7	20 V, 200 mA, low noise, CMOS LDO linear regulator	<a href="#">ADP7118ARDZ</a>
U8	–200 mA, low noise linear regulator	<a href="#">ADP7182ACPZN</a>
VO, VO_BUF	Straight PCB mount subminiature Version B (SMB) jacks, 50 $\Omega$	1-1337482-0

Table 7. [ADR445](#) Daughter Board

Reference Designator	Part Description	Part Number
C1, C3, C5, C6	Capacitors, 0603, X8R, 25 V, 0.1 $\mu\text{F}$ , $\pm 10\%$	C1608X8R1E104K
C2, C4	Capacitors, 3528, 16 V, 10 $\mu\text{F}$ , $\pm 10\%$	TAJB106K016RNJ
J1, J9	3-position male header, single-row connectors, 2.54 mm pitch	350-10-103-00-006000
J4	3-position female header, single-row connector, 2.54 mm pitch	310-13-103-41-001000
JP1	3-position header connector	M20-9990345 + M7567-05
R1 to R8	Resistors, 10 k $\Omega$ , 0.125 W, 0.01%, 0805	ERA-6AEB103V
U1	Ultralow noise 5 V voltage reference	<a href="#">ADR445BRZ</a>
U2	High precision dual amplifier	<a href="#">ADA4077-2BRZ</a>
VR_EXT	Straight PCB mount SMB jack, 50 $\Omega$	1-1337482-0

Table 8. LTZ1000 Daughter Board

Reference Designator	Part Description	Part Number
C1, C3, C5, C7, C13, C22, C23, C24	Capacitors, 0603, 25 V, 0 $\mu\text{F}$ , $\pm 10\%$	C1608X8R1E104K
C2, C4, C6, C8, C12	Capacitors, 3528, 16 V, 1 $\mu\text{F}$ , $\pm 10\%$	TAJB106K016RNJ
D1, D2	Radio frequency (RF)/pin diodes	1N270
J1, J9	3-position male header single row connectors, 2.54 mm pitch	350-10-103-00-006000
J4	3-position female header single row connector, 2.54 mm pitch	310-13-103-41-001000
Q1	Single bipolar junction transistor	2N3904TF
R20	Resistor, 120 $\Omega$ , 0.6 W, 0.01%, through hole	Y1453120R000T9
R39, R40	Resistors, 68 k $\Omega$ , 0.1 W, 0.1%, 0805	PCF0805-13-68K-B-T1
R41	Resistor, 15 k $\Omega$ , 0.6 W, 0.01%, through hole	Y145315K0000T9
R42	Resistor, 1 k $\Omega$ , 0.6 W, 0.01%, through hole	Y14531K00000T9
R43	Resistor, 10 k $\Omega$ , 0.6 W, 1%, through hole	MRS25000C1002FRP00
R44	Resistor, 1 M $\Omega$ , 0.6 W, 1%, through hole	MRS25000C1004FCT00
R45	Resistor, 10 k $\Omega$ , 0.6 W, 0.005%, through hole	Y145310K0000V9L
R46	Resistor, 25 k $\Omega$ , 0.6 W, 0.01%, through hole	Y145325K0000T9
R48	Resistor, 10 k $\Omega$ /10 k $\Omega$ , 0.1W, 0.01%, 1505	Y1685V0001TT9
R49	Resistor, 1 k $\Omega$ , 0.6 W, 1%, through hole	MRS25000C1001FRP00
R8	Resistor, 1.5 k $\Omega$ , 0.25 W, 1%, through hole	CMF-551501FT-1
U1, U2	High precision dual amplifiers	<a href="#">ADA4077-2BRZ</a>
U5	Ultraprecision reference	LTZ1000ACH#PBF

## NOTES

I<sup>2</sup>C refers to a communications protocol originally developed by Philips Semiconductors (now NXP Semiconductors).

**ESD Caution**

**ESD (electrostatic discharge) sensitive device.** Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

**Legal Terms and Conditions**

By using the evaluation board discussed herein (together with any tools, components documentation or support materials, the "Evaluation Board"), you are agreeing to be bound by the terms and conditions set forth below ("Agreement") unless you have purchased the Evaluation Board, in which case the Analog Devices Standard Terms and Conditions of Sale shall govern. Do not use the Evaluation Board until you have read and agreed to the Agreement. Your use of the Evaluation Board shall signify your acceptance of the Agreement. This Agreement is made by and between you ("Customer") and Analog Devices, Inc. ("ADI"), with its principal place of business at One Technology Way, Norwood, MA 02062, USA. Subject to the terms and conditions of the Agreement, ADI hereby grants to Customer a free, limited, personal, temporary, non-exclusive, non-sublicensable, non-transferable license to use the Evaluation Board FOR EVALUATION PURPOSES ONLY. Customer understands and agrees that the Evaluation Board is provided for the sole and exclusive purpose referenced above, and agrees not to use the Evaluation Board for any other purpose. Furthermore, the license granted is expressly made subject to the following additional limitations: Customer shall not (i) rent, lease, display, sell, transfer, assign, sublicense, or distribute the Evaluation Board; and (ii) permit any Third Party to access the Evaluation Board. As used herein, the term "Third Party" includes any entity other than ADI, Customer, their employees, affiliates and in-house consultants. The Evaluation Board is NOT sold to Customer; all rights not expressly granted herein, including ownership of the Evaluation Board, are reserved by ADI. **CONFIDENTIALITY.** This Agreement and the Evaluation Board shall all be considered the confidential and proprietary information of ADI. Customer may not disclose or transfer any portion of the Evaluation Board to any other party for any reason. Upon discontinuation of use of the Evaluation Board or termination of this Agreement, Customer agrees to promptly return the Evaluation Board to ADI. **ADDITIONAL RESTRICTIONS.** Customer may not disassemble, decompile or reverse engineer chips on the Evaluation Board. Customer shall inform ADI of any occurred damages or any modifications or alterations it makes to the Evaluation Board, including but not limited to soldering or any other activity that affects the material content of the Evaluation Board. Modifications to the Evaluation Board must comply with applicable law, including but not limited to the RoHS Directive. **TERMINATION.** ADI may terminate this Agreement at any time upon giving written notice to Customer. Customer agrees to return to ADI the Evaluation Board at that time. **LIMITATION OF LIABILITY.** THE EVALUATION BOARD PROVIDED HEREUNDER IS PROVIDED "AS IS" AND ADI MAKES NO WARRANTIES OR REPRESENTATIONS OF ANY KIND WITH RESPECT TO IT. ADI SPECIFICALLY DISCLAIMS ANY REPRESENTATIONS, ENDORSEMENTS, GUARANTEES, OR WARRANTIES, EXPRESS OR IMPLIED, RELATED TO THE EVALUATION BOARD INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTY OF MERCHANTABILITY, TITLE, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF INTELLECTUAL PROPERTY RIGHTS. IN NO EVENT WILL ADI AND ITS LICENSORS BE LIABLE FOR ANY INCIDENTAL, SPECIAL, INDIRECT, OR CONSEQUENTIAL DAMAGES RESULTING FROM CUSTOMER'S POSSESSION OR USE OF THE EVALUATION BOARD, INCLUDING BUT NOT LIMITED TO LOST PROFITS, DELAY COSTS, LABOR COSTS OR LOSS OF GOODWILL. ADI'S TOTAL LIABILITY FROM ANY AND ALL CAUSES SHALL BE LIMITED TO THE AMOUNT OF ONE HUNDRED US DOLLARS (\$100.00). **EXPORT.** Customer agrees that it will not directly or indirectly export the Evaluation Board to another country, and that it will comply with all applicable United States federal laws and regulations relating to exports. **GOVERNING LAW.** This Agreement shall be governed by and construed in accordance with the substantive laws of the Commonwealth of Massachusetts (excluding conflict of law rules). Any legal action regarding this Agreement will be heard in the state or federal courts having jurisdiction in Suffolk County, Massachusetts, and Customer hereby submits to the personal jurisdiction and venue of such courts. The United Nations Convention on Contracts for the International Sale of Goods shall not apply to this Agreement and is expressly disclaimed.