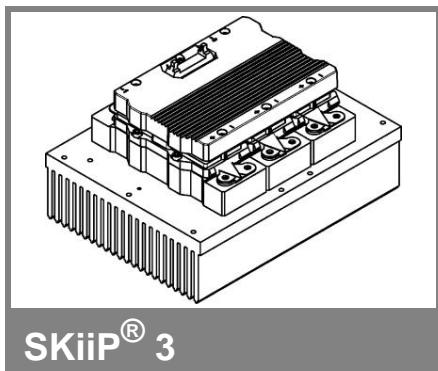


our personal.



2-pack-integrated intelligent Power System

**2-pack
integrated gate driver
SKiiP 1813GB123-3DL**

Data

Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protection against under voltage
- Interlock of top/bottom switch
- Isolation by transformers
- Fibre optic interface (option for GB-types only)
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute Maximum Ratings		$T_a = 25 \text{ }^{\circ}\text{C}$ unless otherwise specified	
Symbol	Conditions	Values	Units
V_{S2}	unstabilized 24 V power supply	30	V
V_i	input signal voltage (high)	15 + 0,3	V
dv/dt	secondary to primary side	75	kV/ μ s
V_{isolIO}	input / output (AC, rms, 2)	3000	V
V_{isolPD}	partial discharge extinction voltage, rms, $Q_{PD} \leq 10 \text{ pC}$	1170	V
V_{isol12}	output 1 / output 2 (AC, rms, 2 s)	1500	V
f_{sw}	switching frequency	10	kHz
f_{out}	output frequency for $I_{peak(1)} = I_C$	10	kHz
$T_{op} (T_{stg})$	operating / storage temperature	- 40 ... + 85	$^{\circ}\text{C}$

Characteristics ($T_a = 25 \text{ }^{\circ}\text{C}$)						
Symbol	Conditions	min.	typ.	max.		
V_{S2}	supply voltage non stabilized	13	24	30		
I_{S2}	$V_{S2} = 24 \text{ V}$	$278 + 37 * f/\text{kHz} + 0,00015 * (I_{AC}/\text{A})^2$				
V_{IT+}	input threshold voltage (High)	12,3				
V_{IT-}	input threshold voltage (Low)	4,6				
R_{IN}	input resistance	10				
C_{IN}	input capacitance	1				
$t_{d(on)IO}$	input-output turn-on propagation time	1,3				
$t_{d(off)IO}$	input-output turn-off propagation time	1,3				
$t_{pERRRESET}$	error memory reset time	9				
t_{TD}	top / bottom switch interlock time	3,3				
$I_{analogOUT}$	max. 5mA; 8 V corresponds to 15 V supply voltage for external components	1800				
I_{s1out}	max. load current	50				
I_{TRIPSC}	over current trip level ($I_{analog OUT} = 10 \text{ V}$)	2250				
T_{tp}	over temperature protection	110	120			
U_{DCTRIP}	U_{DC} -protection ($U_{analog OUT} = 9 \text{ V}$); (option for GB types)	not implemented				

For electrical and thermal design support please use SEMISEL.
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* The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our personal.

