

LOC		DIST		REVISIONS					
GP	00	P	LTR	DESCRIPTION		DATE	OWN	APVD	
			05	ADDED PRELIMINARY		26AUG2004	LS	JJC	
			A	REV PER ECO-06-005380		14MAR2006	BC	GG	
			B	REV PER ECO-07-006584		21MAR2007	BC	GG	

- 1

HOUSING: LCP, UL94V0, COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE
- 2

CONTACT: 0.76µm MIN GOLD IN CONTACT AREA.
0.5µm MIN TIN-LEAD ON COMPLIANT PORTION
OF PCB TAIL OVER 1.27µm MIN NICKEL OVER ALL.
POST: 1.27µm MIN NICKEL PLATED.
- 3

MANUFACTURING TOLERANCE FOR $\varnothing 0.46\pm 0.05$
DIAMETER FINISHED HOLE WITH Sn/PB PLATING:

DRILLED HOLE = $\varnothing 0.55\pm 0.02$

COPPER PLATING = 0.025-0.050

Sn PB PLATING = 0.0038-0.0124

OR

MANUFACTURING TOLERANCE FOR $\varnothing 0.475\pm 0.05$
DIAMETER FINISHED HOLE WITHOUT Sn/PB PLATING:

DRILLED HOLE = $\varnothing 0.55\pm 0.02$

COPPER PLATING = 0.025-0.050

4

SEE TABLE 1 FOR INTERCONNECTIONS TO
BACKPLANE CONNECTOR.

5

CONTACT: 0.76µm MIN GOLD IN CONTACT AREA.
0.5µm MIN TIN ON COMPLIANT PORTION
OF PCB TAIL OVER 1.27µm MIN NICKEL OVER ALL.
POST: 1.27µm MIN NICKEL PLATED.
- TABLE 1
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410135
- | TYPICAL INTERCONNECTIONS FOR
EACH EVEN-NUMBERED
COLUMN (WAFER): 2,4,6,8,10,12,14. | | |
|---|----------------------------------|-------------------------------|
| CONTACT
USAGE | DAUGHTERCARD
CONNECTOR
PIN | BACKPLANE
CONNECTOR
PIN |
| SIGNAL
PAIR | bx | cx |
| | cx | dx |
| SIGNAL
PAIR | ex | gx |
| | fx | hx |
| GROUND | ax,dx,gx,
(ALL COMMONED) | ax,bx,ex,
fx, ix |
- | TYPICAL INTERCONNECTIONS FOR
EACH ODD-NUMBERED
COLUMN (WAFER): 1,3,5,7,9,11,13,15 | | |
|---|----------------------------------|-------------------------------|
| CONTACT
USAGE | DAUGHTERCARD
CONNECTOR
PIN | BACKPLANE
CONNECTOR
PIN |
| SIGNAL
PAIR | ax | ax |
| | bx | bx |
| SIGNAL
PAIR | dx | ex |
| | ex | fx |
| SIGNAL | gx | ix |
| GROUND | cx,fx
(ALL COMMONED) | cx,dx,gx,hx |
- NOTE: "x" DESIGNATES THE COLUMN NUMBER
- | | |
|--------------|-----------|
| <div>5</div> | 1410147-2 |
| <div>2</div> | 1410147-1 |
| PLATING | PART NO |
- | | | | | | | | | | |
|-------------|--|---|--|-------------------------------|--|--|--|---|--|
| DIMENSIONS: | | TOLERANCES UNLESS
OTHERWISE SPECIFIED: | | DWN
R. TREA 23OCT2003 | | tyco
Electronics | | Tyco Electronics
Harrisburg, PA 17105-3608 | |
| mm | | 9 PLC ±
2 PLC ±0.5
3 PLC ±0.13
4 PLC ±
ANGLES ±1° | | CHK
J. CONSOLI 23OCT2003 | | NAME
RIGHT-ANGLE PLUG ASSEMBLY, VITA 41,
15 WAFER, CENTER, 20.3mm, MultiGig
RT2, DAUGHTERCARD CONNECTOR | | RESTRICTED TO | |
| MATERIAL | | FINISH | | APVD
G. GRIFFITH 14MAR2006 | | PRODUCT SPEC | | SIZE | |
| | | | | APPLICATION SPEC | | 114-13056 | | CAGE CODE | |
| | | | | WEIGHT | | 00779 | | DRAWING NO | |
| | | | | CUSTOMER DRAWING | | A1 | | C=1410147 | |
| | | | | | | SCALE | | 3:1 | |
| | | | | | | SHEET | | 1 OF 1 | |
| | | | | | | REV | | B | |
- AMP 4805 REV 31MAR2000
- Print/ENGINEER DRAWING