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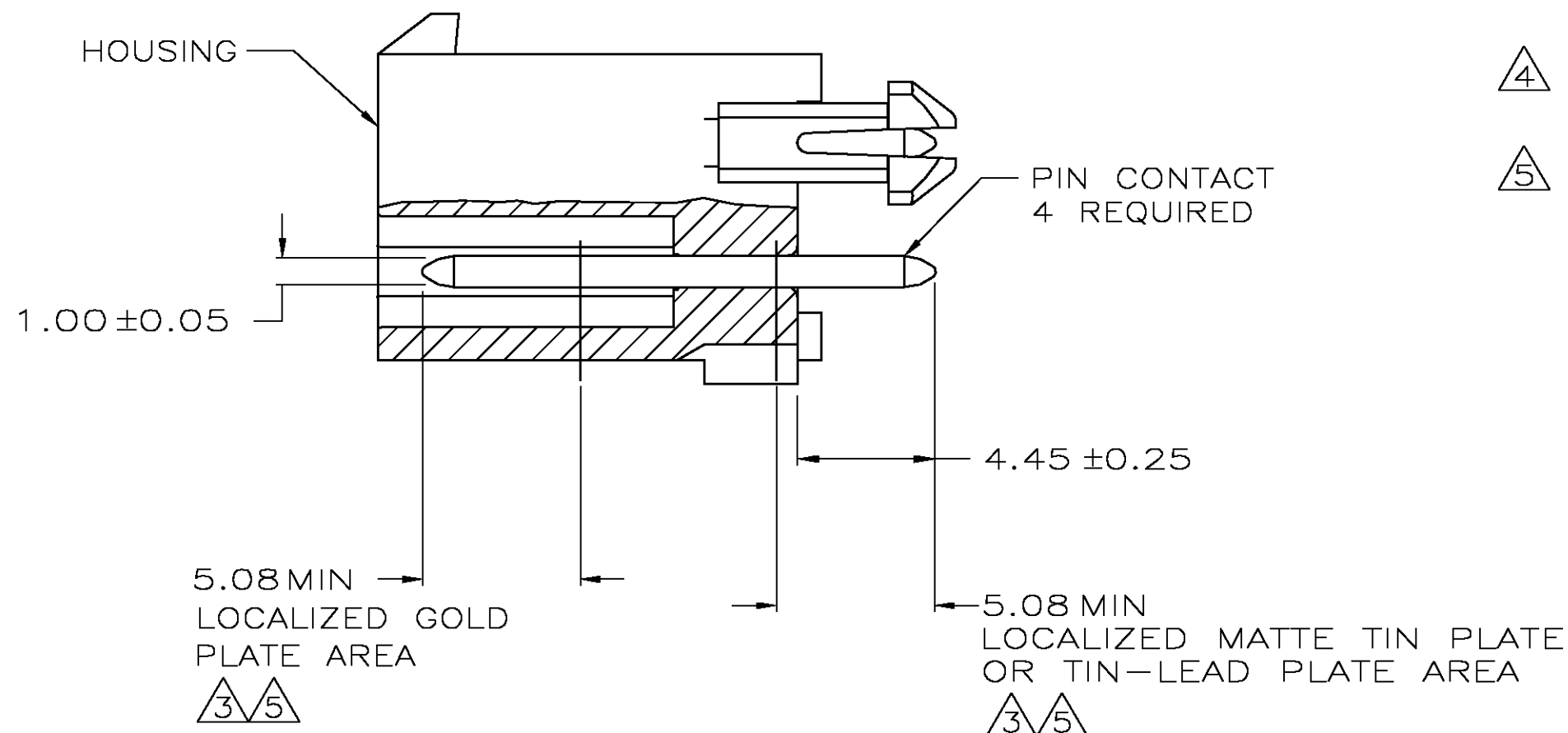
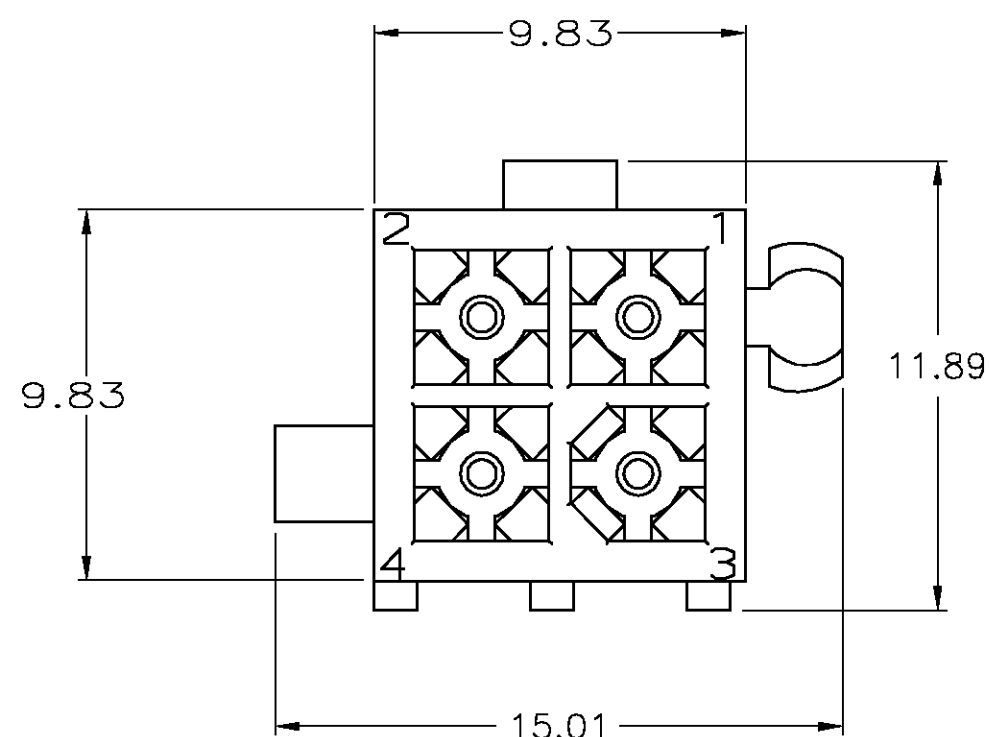
BY TYCO ELECTRONICS CORPORATION.

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LOC
CMDIST
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REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	L	REVISED PER 0G3B-0211-05	6/8/05	SS	RJ



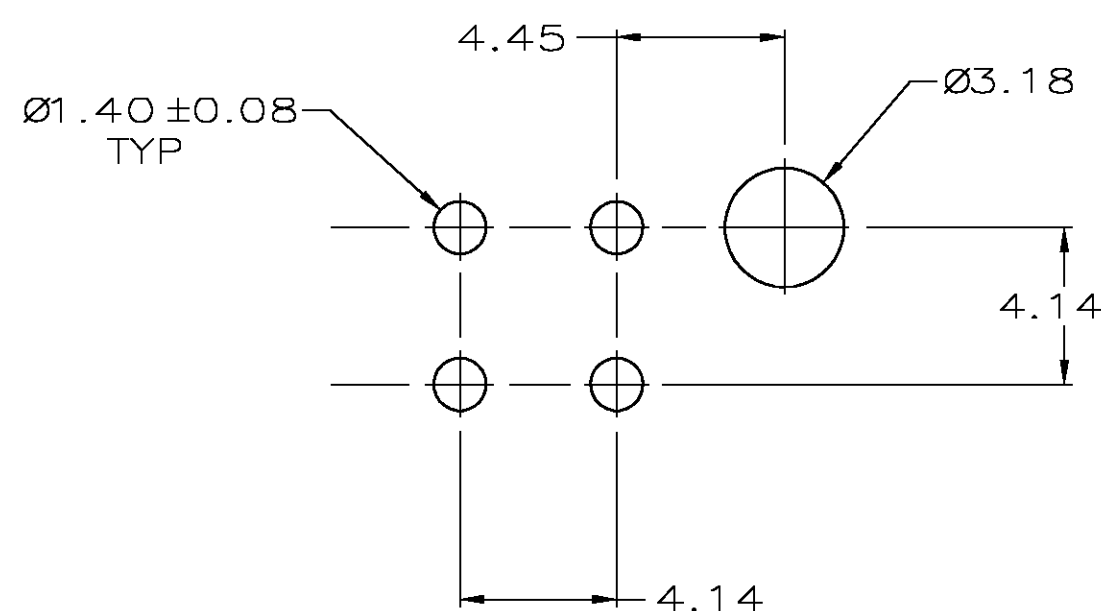
1. PINS TO COMPLY WITH AMP SOLDERABILITY SPEC. 109-11-3.

2 0.00381 MIN MATTE TIN LEAD PLATE OVER 0.00127 MIN NICKEL.

3 0.00076 MIN GOLD PLATE IN THE LOCALIZED PLATE AREA, 0.00381 MATTE TIN LEAD IN THE LOCALIZED PLATE AREA, BOTH OVER 0.00127 MIN NICKEL.

4 0.00381 MIN MATTE TIN PLATE OVER 0.00127 MIN NICKEL.

5 0.00076 MIN GOLD PLATE IN THE LOCALIZED PLATE AREA, 0.00381 MATTE TIN IN THE LOCALIZED PLATE AREA, BOTH OVER 0.00127 MIN NICKEL.



RECOMMENDED LAYOUT FOR 1.57 THICK BOARD
TOLERANCES NON-ACCUMULATIVE

BRASS, GOLD	5	NYLON, UL94V-0	WHITE	1-770174-1
BRASS, TIN	4	NYLON, UL94V-0	WHITE	1-770174-0
BRASS, GOLD	3	NYLON, UL94V-0	WHITE	770174-2
BRASS, TIN-LEAD	2	NYLON, UL94V-0	WHITE	770174-1
MATERIAL	FINISH	MATERIAL	COLOR	PART NO.

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN S CARPENTER CHK R JONES APVD R JONES PRODUCT SPEC APPLICATION SPEC —
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