

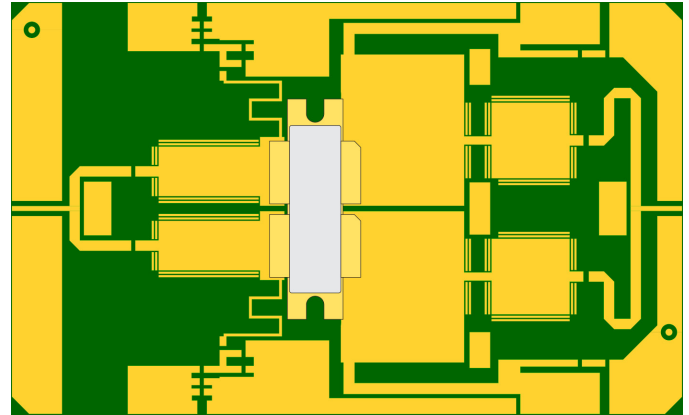
# L-Band, GaN/SiC, RF Power Transistor

1030 MHz | 6000 W typ | 73% Efficiency typ | 19 dB Gain typ | 125 V | 32 $\mu$ s Pulse Length, 4% Duty Cycle

IGNP1030S5000 is a high power GaN-on-SiC RF power module that has been designed to suit the unique needs of IFF/SSR avionics systems. Under 32 $\mu$ s, 4% duty cycle pulse conditions, it typically supplies 5000 W of peak output power, with 18.5dB of associated gain and 68% efficiency. It operates from a 125 V supply voltage.

## FEATURES

- GaN on SiC HEMT Technology
- Output Power >5000 W
- High Efficiency - up to 73% during the RF pulse
- 100% RF Tested
- REACH Compliant



## APPLICATIONS

- L-band Avionics IFF & SSR Systems

**Table 1. RF Electrical Characteristics (Case temperature = 30 °C unless otherwise stated)**

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Gain	G	18.0	19.0	22.0	dB	$P_{OUT} = 5000W$ $f = 1030 \text{ MHz}$ 32 $\mu$ s pulse length, 4% duty cycle $V_{DS} = 125V, I_{DS} = 75mA$ per side
Drain Efficiency	$\eta$	60	68	80	%	
Pulse Droop	D	-0.5	-0.4	+0.2	dB	
Input Return Loss	IRL	10	14	18	dB	
Load Mismatch Stability	VSWR-S	2:1				
VSWR Withstand	VSWR-LMT	3:1				

Note 1: Consult Integra Technologies Application Note 001 for information on how RF output power and pulse droop are measured.

**Table 2. DC Electrical Characteristics (Case temperature = 25 °C unless otherwise stated)**

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Gate Pinch-Off Voltage	$V_P$	-5.0			V	$V_{DS} = 125V, I_{DS} = 1mA$
Quiescent Gate Voltage	$V_Q$		-2.8		V	$V_{DS} = 125V, I_{DS} = 75mA$ per side

**Table 3. Absolute Maximum Ratings (Not Simultaneous)**

Parameter	Symbol	Value	Units	Test Conditions
DC Drain-Source Voltage	$V_{DS}$	400	V	25 °C
DC Gate-Source Voltage	$V_{GS}$	-8 to +1.0	V	25 °C
DC Drain Current	$I_D$	156	A	25 °C
DC Gate Current	$I_G$	156	mA	25 °C
RF Input Power	$P_{RFIN}$	100	W	25 °C
Operating Channel Temperature	$T_J$	-55 to +225	°C	
Storage Temperature	$T_{STG}$	-55 to +150	°C	
Soldering Temperature	$T_{SOLDER}$	260 for 60s	°C	

Note: Operation outside the limits given in this table may cause permanent damage to the transistor

**Table 4. Thermal Resistance (Case temperature = 85 °C unless otherwise stated)**

Parameter	Symbol	Typ	Units	Test Conditions
Peak Thermal Resistance (total device), Channel to Case	$R_{TH}$	0.05	°C/W	$P_{diss} = 2353W$ 32µs pulse length, 4% duty cycle $V_{DS} = 125V$

TYPICAL PERFORMANCE

Gain & Efficiency vs Output Power

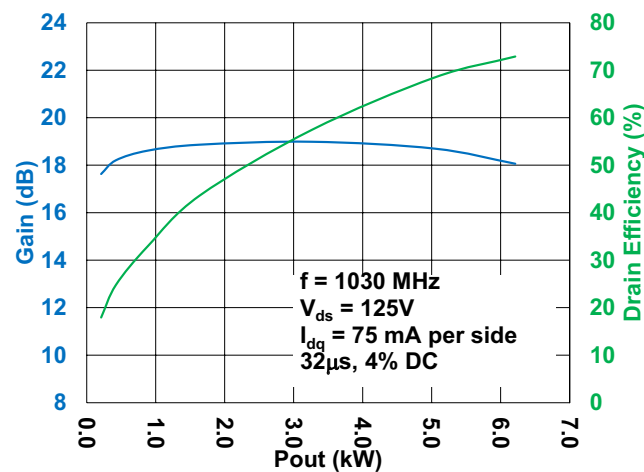
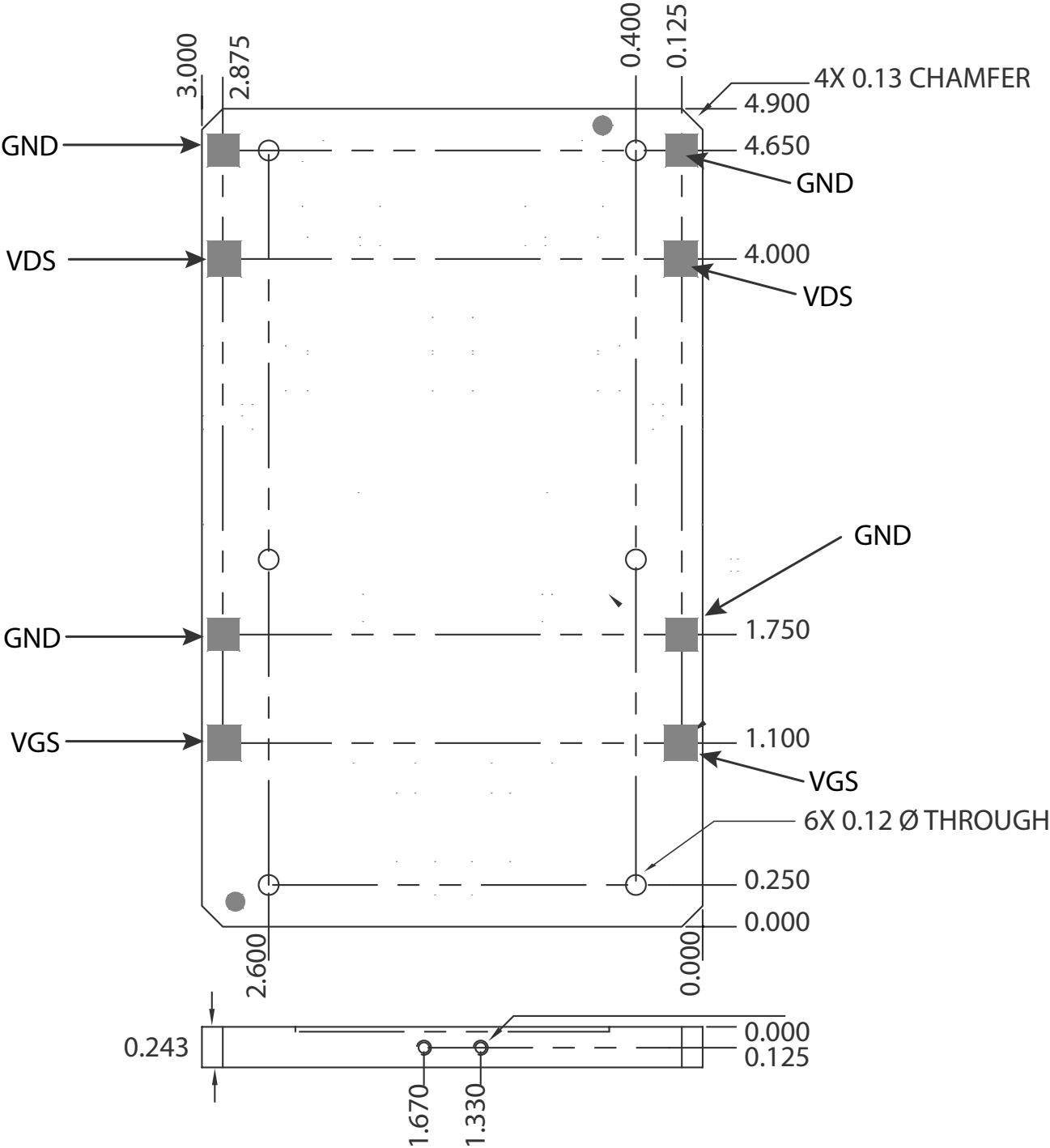


Figure 1

DIMENSIONS



All dimensions in inches

**ESD & MSL Rating**

Parameter	Rating	Standard
ESD Human Body Model (HBM)	TBD	ESDA/JEDEC JS-001-2012
ESD Charged Device Model (CDM)	TBD	JEDEC JESD22-C101F
Moisture Sensitivity Level (MSL)	Unlimited Shelf Life	IPC/JEDEC J-STD-020

**REACH Compliance**

Integra Technologies supports EU Regulation number 1907/2006 concerning the Registration, Evaluation, Authorization, and Restriction of Chemicals (REACH) as these apply to Integra semiconductor products, development tools, and shipping packaging.

In support of the REACH regulation, Integra will:

- Inform customers and recipients of Integra product if they contain any substances that are of very high concern (SVHC) per the European Chemical Agency (ECHA) website.
- Notify ECHA if any Integra product that contains any SVHCs which exceed guidelines for REACH chemicals by weight per part number and for total content weight per year for all products produced in or imported to the European market.
- Cease shipments of product containing REACH Annex XIV substances until authorization has been obtained.
- Cease shipment of product containing REACH Annex XVII chemicals when restrictions apply.

Integra has evaluated its materials, BOMs, and product specifications and product and has determined that this transistor conforms to all REACH and SVHC regulations and guidelines. Integra has implemented actions and control programs that will assure continued compliance.

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**DEFINITIONS:****DATA SHEET STATUS**

Advanced Specification - This data sheet contains Advanced specifications.

Preliminary Specification - This data sheet contains specifications based on preliminary measurements and data.

Final Specification - This data sheet contains final product specifications.

**MAXIMUM RATINGS** Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.

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