

DAR	REV		DESCRIPTION	APPVD	DATE
4708	С	SEE DAR		DG	12-13
6077	D	SEE DAR		TJD	3-16
6657	E	SEE DAR		DG	4-17

PORT CONFIGURATION

OUTPUT

J2

INPUT

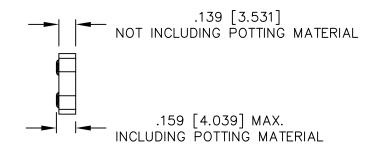
FORWARD REFLECTED

COUPLED | COUPLED

J4

J3

J3



## NOTES:

- 1. SEE SMD COUPLER APPLICATION NOTE FOR FURTHER INFORMATION.
- 2. SHIELDING OF 50 OHM LAUNCH LINES MAY BE REQUIRED TO MAINTAIN ISOLATION BETWEEN PORTS.
- 3. AVAILABLE IN TAPE AND REEL. SEE SMD COUPLER APPLICATION NOTE FOR FURTHER INFORMATION.
- 4. DO NOT EXCEED 220°C WHEN INSTALLING COUPLER.

## **ELECTRICAL SPECIFICATIONS**

FREQUENCY: 800 - 4200 MHz.

**INSERTION LOSS:** < 0.30 dB MAIN LINE VSWR: <1.30:1 DIRECTIVITY: >18dB COUPLING: 32±1.5dB

COUPLED FLATNESS:  $\pm 0.8 dB$ 

POWER: 150 WATT TEMPERATURE: -55°C TO 85°C

							J2	J1	J4	J3
MATERIAL					_					
		DWN S.Cornell DATE		4-26-13		Innovative Products				
FINICII		ENG	DG	DATE	4-13		<u> </u>	ovauv	Prod	ucts
FINISH TIN/I FA			-	DATE		TITLE		OUTL	INE	

TIN/LEAD		MANUF	DATE	IIILE	OUTLINE		
TOLERANCE		CAGE COD	E: 437R2	SMD COUPLER, DDC, 800 - 4200 MHz. 150W			
	.XX ±.02 [.50]	SHEET OF 1 1	SCALE 2:1	SIZE	P/N IPP-8078*	REV. E	