





# MMB362A - PRELIMINARY Band 48 (3.55-3.7GHz) MMB Series TDD BPF

#### **Features**

- Meets FCC CBRS emission requirements for 1.0W@Ant systems
- Low Loss with High Rejection
- Universal footprint across family for all TDD bands

#### **Applications**

- Wireless Infrastructure applications
- High-performance carrier-grade TDD Pico-cells.

Part Dimensions: 40.0 × 6.4 × 9.3 mm • 6.3 g
Materials: Ag plated ceramic block with tin plated brass shield

#### Description

Surface mount ceramic bandpass filter supports a universal footprint across all TDD frequency bands enabling the use of a common system PCB. Superior rejection, insertion loss, reliability, as well as both peak and average power handling compared to other bandpass filter technologies.

#### **Electrical Specifications**

-				
Parameter	Frequency (MHz)	Typical at 25°C	Spec. at 25°C	Spec. over -40°C to +85°C
Nominal Impedance	-	50 ohms	-	-
Average Input Power	-	-	-	8.0 Watt max
Peak Input Power	-	-	-	80 Watt max
Input-Output Response				
Passband Insertion Loss (10 MHz avg)	3550-3700	1.8 dB	2.0 dB max	2.1 dB max
Passband Ripple (over 10 MHz)	3550-3700	0.7 dB	1.0 dB max	1.0 dB max
Passband Return Loss	3550-3700	14 dB	13 dB min	12 dB min
Attenuation:	1-2690	67 dB	65 dB min	65 dB min
	2690-3300	65 dB	40 dB min	40 dB min
	3300-3530	17 dB	15 dB min	14 dB min
	3720-4200	16 dB	15 dB min	14 dB min
	4200-4900	56 dB	52 dB min	52 dB min
	4900-5850	50 dB	45 dB min	45 dB min

Note: CTS tests each unit to the critical specifications above. Subsequent audits may deviate due to repeatability among different test systems which shall not exceed these allowances. Specification Allowance
Insertion Loss 0.1 dB
Return Loss 1.0 dB
Attenuation 1.0 dB

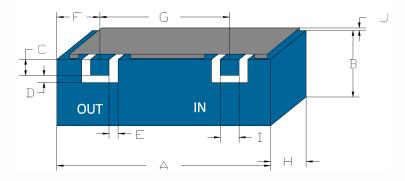
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#### **PRELIMINARY - MMB362A**

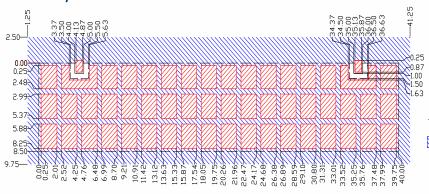
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#### **Mechanical Drawing**



Nominal (mm)	Tolerance (±mm or Max)	
40.0	max	
4.8	max	
1.0	0.13	
0.5	0.13	
0.5	0.13	
4.5	0.25	
31.0	0.13	
9.3	max	
1.0	0.13	
1.4	0.2	
	(mm) 40.0 4.8 1.0 0.5 0.5 4.5 31.0 9.3 1.0	

#### **PCB Layout**

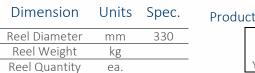


Combined 40mm & 50mm universal footprint PCB layout is also available.



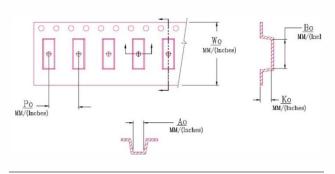
## Exposed conductor Solder resist over Dielectric

#### Packaging and Marking









$W_{o}$	$A_{o}$	Bo	Ko	Po
2.205 in	0.256 in	1.587 in	0.378 in	0.630 in
56.0 mm	6.5 mm	40.3 mm	9.6 mm	16.0 mm

### **Electrical Response**

