



Product Change Notification / SYST-01DHDJ305

Date:

04-Oct-2021

Product Category:

Microprocessors

PCN Type:

Document Change

Notification Subject:

Data Sheet - SAM9G25 Data Sheet

Affected CPNs:

[SYST-01DHDJ305_Affected_CPN_10042021.pdf](#)

[SYST-01DHDJ305_Affected_CPN_10042021.csv](#)

Notification Text:

SYST-01DHDJ305

Microchip has released a new Product Documents for the SAM9G25 Data Sheet of devices. If you are using one of these devices please read the document located at [SAM9G25 Data Sheet](#).

Notification Status: Final

Description of Change:

- Template update: Moved from Atmel to Microchip Template.- The datasheet is assigned a new document number (DS60001731) and revision letter is reset to A.
- Document number DS60001731 revision A corresponds to what would have been 11032 revision H.
- ISBN number assigned.
- Section 10. "Boot Strategies"
- Updated Table 10-4 "PIO Driven During Boot Program Execution"
- Section 14. "Real-time Clock (RTC)"

- Updated Section 14.6.1 "RTC Control Register"
- Section 14.6.2 "RTC Mode Register", Section 14.6.5 "RTC Time Alarm Register", Section 14.6.6 "RTC Calendar Alarm Register": removed sentence on write protection
- Section 29. "DDR SDR SDRAM Controller (DDRSDC)"
- Section 29.7.4 "DDRCDC Timing Parameter 0 Register": updated TWTR field description
- Section 43. "Image Sensor Interface (ISI)"
- Section 43.4.2 "Power Management": added note
- Section 45. "Electrical Characteristics"
- Updated:
 - Table 45-7 "Main Oscillator Characteristics", Figure 45-2 "Main Oscillator Schematics"
 - Table 45-13 "XIN32 Clock Characteristics"
 - Table 45-44 "ISI Timings with Peripheral Supply 3.3V", Table 45-45 "ISI Timings with Peripheral Supply 1.8V"
 - Figure 45-19 "SSC Transmitter, TK and TF in Input", Figure 45-21 "SSC Receiver, RK in Input and RF in Output", Figure 45-22 "SSC Receiver, RK and RF in Output"
- Section 48. "Ordering Information"
- Updated Table 48-1 "SAM9G25 Ordering Information"
- Section 49. "Errata"
- Added:
 - Section 49.8 "Universal Synchronous Asynchronous Receiver Transmitter (USART)"
 - Section 49.9 "Power Management Controller (PMC)"

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 4 Oct 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAM9G25 Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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