



Product Change Notification / SYST-10BNQU415

Date:

11-Mar-2021

Product Category:

16-Bit - Microcontrollers and Digital Signal Controllers

PCN Type:

Document Change

Notification Subject:

ERRATA - PIC24FJ256GA705 Family Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-10BNQU415_Affected_CPN_03112021.pdf](#)

[SYST-10BNQU415_Affected_CPN_03112021.csv](#)

Notification Text:

SYST-10BNQU415

Microchip has released a new Product Documents for the PIC24FJ256GA705 Family Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [PIC24FJ256GA705 Family Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change:

1. Removed A5 from the Affected Silicon Revisions table in silicon errata issue 4 (Primary XT and HS Oscillator (POSC) because this issue only affects silicon revisions A3 and A4.
2. The I2C standard uses the terminology "Master" and "Slave." The equivalent Microchip terminology used in this document is "Host" and "Client", respectively.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 11 March 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[PIC24FJ256GA705 Family Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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