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Product Change Notification / SYST-10QRAR381

Date:

16-Feb-2021

Product Category:

8-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - ATmega4808/4809 Silicon Errata and Data Sheet Clarification

Affected CPNs:

SYST-10QRAR381_Affected_CPN_02162021.pdf
SYST-10QRAR381_Affected_CPN_02162021.csv

Notification Text:

SYST-10QRAR381

Microchip has released a new Product Documents for the ATmega4808/4809 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at ATmega4808/4809 Silicon Errata and Data Sheet Clarification.

Notification Status: Final

Description of Change: 1) Added new errata:
   – Device: 2.2.1 Writing the OSCLOCK Fuse in FUSE.OSCCFG to '1' Prevents Automatic Loading of Calibration Values
   – TCA: 2.8.1 Restart Will Reset Counter Direction in NORMAL and FRQ Mode
   – TCB: 2.9.3 CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode
   – USART:
      • 2.10.2 Open-Drain Mode Does Not Work When TXD is Configured as Output
      • 2.10.3 Start-of-Frame Detection Can Unintentionally Be Triggered in Active Mode
2) New hold place for CPUINT errata: After Device errata

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 16 Feb 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ATmega4808/4809 Silicon Errata and Data Sheet Clarification

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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