



Product Change Notification / SYST-19GUZD071

Date:

22-Mar-2021

Product Category:

32-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - SAM D11 Series Family Silicon Errata and Data Sheet Clarifications

Affected CPNs:

[SYST-19GUZD071_Affected_CPN_03222021.pdf](#)

[SYST-19GUZD071_Affected_CPN_03222021.csv](#)

Notification Text:

SYST-19GUZD071

Microchip has released a new Product Documents for the SAM D11 Series Family Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at [SAM D11 Series Family Silicon Errata and Data Sheet Clarifications](#).

Notification Status: Final

Description of Change: This revision includes numerous typographical updates, along with the following new information: The I2C standard uses the terminology "master" and "slave". The equivalent Microchip terminology used in this document is "Host" and "Client" respectively.

The following Errata were added:

- 2. Module: "SERCOM I2C STATUS.CLKHOLD bit"
- 3. Module: "Device Standby Wakeup"

Updated the following Data Sheet Clarifications with new images and descriptions:

- 2. Module: "OSC32K and XOSC32K EN1K bit"
- 4. Module: "Brown-Out Detectors (BOD) Characteristics"
- 5. Module: "ADC Power Management"
- 9. Module: "NVMCTRL - NVM User Configuration"
- 11. Module: "SYSCTRL - XOSC Register" The following Data Sheet Clarifications were added:
- 14. Module: "USB - STATUS.SPEED"

- 15. Module: "SERCOM I2C INTFLAG.DRDY bit"
- 16. Module: "NVMCTRL AUX1 Device Configuration Register"
- 17. Module: "DMAC SRCADDR and DSTADDR Registers' Descriptions"
- 18. Module: "RTC and TC READREQ.RCONT bit fields"
- 19. Module: "DMAC BASEADDR, WRBADDR and DESCADDR registers alignment"
- 20. Module: "DAC Maximum Input Clock Frequency"
- 21. Module: "DAC Start Conversion Event"
- 22. Module: "DAC Electrical Characteristics"
- 23. Module: "ADC Electrical Characteristics at 85°C"
- 24. Module: "Bandgap Electrical Characteristics at 85°C"
- 25. Module: "ADC Electrical Characteristics at 105°C"
- 26. Module: "Bandgap Electrical Characteristics at 105°C"

The following Data Sheet Clarifications were removed:

- Module: 32 kHz Ultra-Low Power Internal Oscillator (OSCULP32K) Operation

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 22 Mar 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAM D11 Series Family Silicon Errata and Data Sheet Clarifications](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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