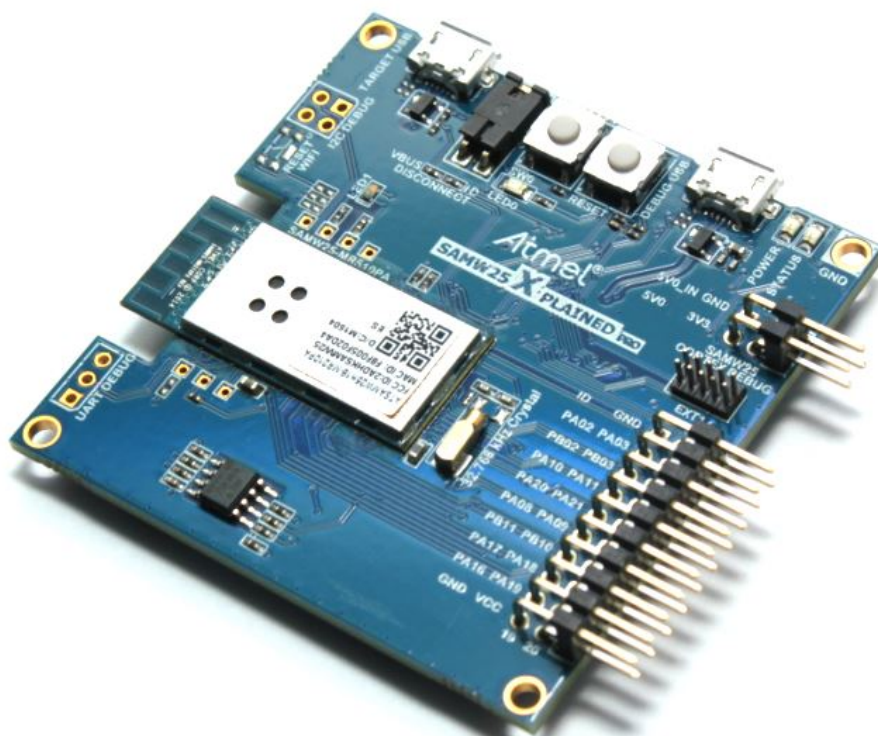


### Atmel SAM W25 Xplained Pro



## Preface

The Atmel® SAM W25 Xplained Pro evaluation kit is a hardware platform to evaluate the ATSAMW25H18-MR510PB SmartConnect Wi-Fi Module.

Supported by the Atmel Studio integrated development platform, the kit provides easy access to the features of the Atmel ATSAMW25H18-MR510PB and explains how to integrate the device in a custom design.

The Xplained Pro series evaluation kits include an on-board Embedded Debugger, and no external tools are necessary to program or debug the ATSAMW25H18-MR510PB.

The Xplained Pro extension kits offer additional peripherals to extend the features of the board and ease the development of custom designs.

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# 1. Introduction

## 1.1 Features

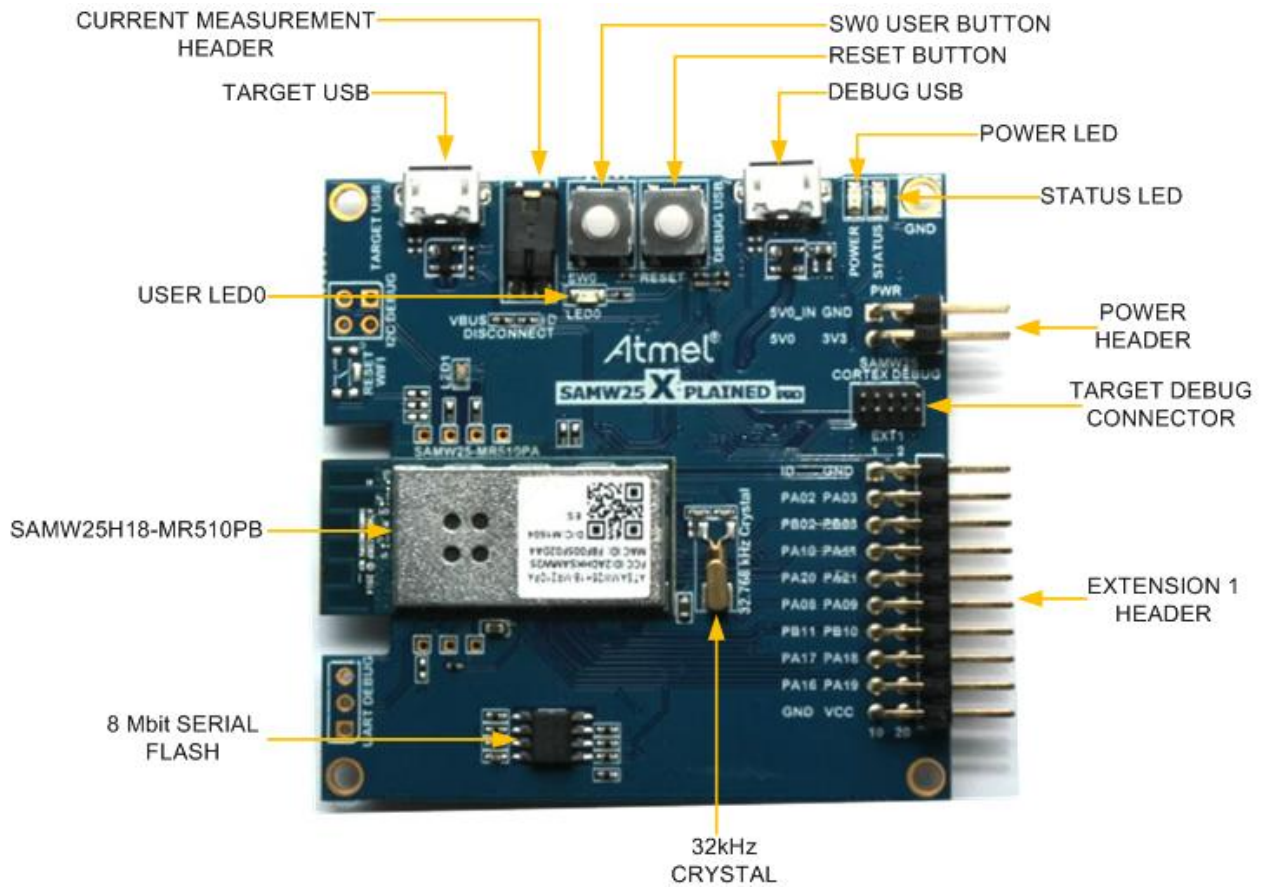
- Atmel ATSAMW25H18-MR510PB SmartConnect Wi-Fi Module
  - SoC ATSAMW25H18A MCU and ATWINC1500 Wi-Fi chip
  - IEEE 802.11 b/g/n 20MHz SISO in 2.4GHz ISM band
  - On-board Printed Antenna
  - ATECC108 Crypto Chip
  - Integrated 26MHz crystal
- Embedded debugger (EDBG)
  - USB interface
  - Programming and debugging on board SAM W25 through Serial Wire Debug (SWD)
  - Virtual COM-port interface to target via UART
  - Atmel Data Gateway Interface (DGI) to target via SPI and TWI
  - Four GPIOs connected to target for code instrumentation
  - One yellow status LED and one green board power LED
- Digital I/O
  - Two mechanical buttons (user and reset button)
  - One user LED
  - One extension header
- Three possible power sources
  - External power
  - Embedded debugger USB
  - Target USB
- 32KHz crystal
- USB interface, device and reduced host mode
- 8Mbit Serial Flash

## 1.2 Kit Overview

The Atmel SAM W25 Xplained Pro evaluation kit is a hardware platform to evaluate the Atmel ATSAMW25H18-MR510PB.

The kit offers a set of features of ATSAMW25H18-MR510PB that enables the user to get started using the ATSAMW25H18-MR510PB peripherals right away and to get an understanding of how to integrate the device in their own design.

Figure 1-1. SAM W25 Xplained Pro Evaluation Kit Overview



## 2. Getting Started

### 2.1 Quick-start

Three steps to start exploring the Atmel Xplained Pro Platform

- Download and install [Atmel Studio](#)<sup>1</sup>
- Launch Atmel Studio
- Connect a Micro-B cable to the DEBUG USB port

### 2.2 Connecting the Kit

When connecting Atmel SAM W25 Xplained Pro to your computer for the first time, the operating system will do a driver software installation. The driver file supports both 32-bit and 64-bit versions of Microsoft® Windows® XP and Windows 7.

Once connected the green power LED will be lit and Atmel Studio will autodetect which Xplained Pro evaluation- and extension kit(s) that's connected. You'll be presented with relevant information like datasheets and kit documentation. You also have the option to launch Atmel Software Framework (ASF) example applications. The target device is programmed and debugged by the on-board Embedded Debugger and no external programmer or debugger tool is needed. Refer to the [Atmel Studio user guide](#)<sup>2</sup> for information regarding how to compile and program the kit.

### 2.3 Design Documentation and Related Links

The following list contains links to the most relevant documents and software for SAM W25 Xplained Pro.

1. [Xplained Pro products](#)<sup>3</sup> - Atmel Xplained Pro is a series of small-sized and easy-to-use evaluation kits for Atmel AVR® 8- and 32-bit microcontrollers / SoC RF modules. It consists of a series of low cost MCU and wireless boards for evaluation and demonstration of features and capabilities of different MCU / wireless families.
2. [SAM W25 Xplained Pro User Guide](#)<sup>4</sup> - PDF version of this User Guide.
3. [SAM W25 Xplained Pro Design Documentation](#)<sup>5</sup> - Package containing schematics, BOM, assembly drawings, 3D plots, layer plots etc.
4. [EDBG User Guide](#)<sup>6</sup> - User guide containing more information about the onboard Embedded Debugger.
5. [Atmel Studio](#)<sup>7</sup> - Free Atmel IDE for development of C/C++ and assembler code for Atmel microcontrollers.
6. [IAR Embedded Workbench](#)<sup>8</sup> for **ARM**®. This is a commercial C/C++ compiler that is available for ARM. There is a 30 day evaluation version as well as a code size limited kick-start version available from their website. The code size limit is 16KB for devices with M0, M0+, and M1 cores and 32kB for devices with other cores.
7. [Atmel sample store](#)<sup>9</sup> - Atmel sample store where you can order samples of devices.

<sup>1</sup> <http://www.atmel.com/atmelstudio>

<sup>2</sup> <http://www.atmel.com/atmelstudio>

<sup>3</sup> <http://www.atmel.com/XplainedPro>

<sup>4</sup> [http://www.atmel.com/Images/Atmel-42463-SAM\\_W25\\_Xplained\\_Pro\\_User-Guide.pdf](http://www.atmel.com/Images/Atmel-42463-SAM_W25_Xplained_Pro_User-Guide.pdf)

<sup>5</sup> [http://www.atmel.com/Images/Atmel-42463-SAM\\_W25\\_Xplained\\_Pro\\_User-Guide.zip](http://www.atmel.com/Images/Atmel-42463-SAM_W25_Xplained_Pro_User-Guide.zip)

<sup>6</sup> [http://www.atmel.com/Images/Atmel-42096-Microcontrollers-Embedded-Debugger\\_User-Guide.pdf](http://www.atmel.com/Images/Atmel-42096-Microcontrollers-Embedded-Debugger_User-Guide.pdf)

<sup>7</sup> <http://www.atmel.com/atmelstudio>

<sup>8</sup> <http://www.iar.com/en/Products/IAR-Embedded-Workbench/ARM/>

<sup>9</sup> <http://www.atmel.com/system/samplesstore>

## 3. Xplained Pro

Xplained Pro is an evaluation platform that provides the complete Atmel microcontroller / SoC RF experience. The platform consists of a series of Microcontroller (MCU), wireless and extension boards that are integrated with Atmel Studio, have Atmel Software Framework (ASF) drivers and demo code, support data streaming and more. Xplained Pro boards support a wide range of Xplained Pro extension boards that are connected through a set of standardized headers and connectors. Each extension board has an identification (ID) chip to uniquely identify which boards are connected to a Xplained Pro board. This information is used to present relevant user guides, application notes, datasheets, and example code through Atmel Studio. Available Xplained Pro MCU and extension boards can be purchased in the [Atmel Web Store](#)<sup>1</sup>.

### 3.1 Embedded Debugger

The SAM W25 Xplained Pro contains the Atmel Embedded Debugger (EDBG) for on-board debugging. The EDBG is a composite USB device of three interfaces; a debugger, Virtual COM Port, and Data Gateway Interface (DGI).

In conjunction with Atmel Studio, the EDBG debugger interface can program and debug the ATSAMW25H18-MR510PB. On the SAM W25 Xplained Pro, the SWD interface is connected between the EDBG and the ATSAMW25H18-MR510PB.

The Virtual COM Port is connected to a UART port on the ATSAMW25H18-MR510PB (see section “[Embedded Debugger Implementation](#)” on page 11 for pinout), and provides an easy way to communicate with the target application through simple terminal software. It offers variable baud rate, parity, and stop bit settings. Note that the settings on the target device UART must match the settings given in the terminal software.

The DGI consists of several physical data interfaces for communication with the host computer. See section “[Embedded Debugger Implementation](#)” on page 11 for available interfaces and pinout. Communication over the interfaces are bidirectional. It can be used to send events and values from the ATSAMW25H18-MR510PB, or as a generic printf-style data channel. Traffic over the interfaces can be timestamped on the EDBG for more accurate tracing of events. Note that timestamping imposes an overhead that reduces maximal throughput. The DGI uses a proprietary protocol, and is thus only compatible with Atmel Studio.

The EDBG controls two LEDs on SAM W25 Xplained Pro; a power LED and a status LED. [Table 3-1, “EDBG LED Control” on page 6](#) shows how the LEDs are controlled in different operation modes.

**Table 3-1. EDBG LED Control**

| Operation Mode                     | Power LED                                                          | Status LED                                                                |
|------------------------------------|--------------------------------------------------------------------|---------------------------------------------------------------------------|
| Normal operation                   | Power LED is lit when power is applied to the board.               | Activity indicator, LED flashes every time something happens on the EDBG. |
| Bootloader mode (idle)             | The power LED and the status LED blinks simultaneously.            |                                                                           |
| Bootloader mode (firmware upgrade) | The power LED and the status LED blinks in an alternating pattern. |                                                                           |

For further documentation on the EDBG, see the [EDBG User Guide](#)<sup>2</sup>.

### 3.2 Hardware Identification System

All Xplained Pro compatible extension boards have an Atmel ATSHA204A CryptoAuthentication™ chip mounted. This chip contains information that identifies the extension with its name and some extra data. When an Xplained Pro extension board is connected to an Xplained Pro board the information is read and sent to Atmel Studio. The Atmel Kits extension, installed with Atmel Studio, will give relevant information, code examples, and links to relevant documents. [Table 3-2, “Xplained Pro ID Chip Content” on page 6](#) shows the data fields stored in the ID chip with example content.

**Table 3-2. Xplained Pro ID Chip Content**

| Data Field       | Data Type    | Example Content              |
|------------------|--------------|------------------------------|
| Manufacturer     | ASCII string | Atmel\0'                     |
| Product Name     | ASCII string | Segment LCD1 Xplained Pro\0' |
| Product Revision | ASCII string | 02\0'                        |

<sup>1</sup> <http://www.atmel.com/buy/default.aspx>

<sup>2</sup> [http://www.atmel.com/Images/Atmel-42096-Microcontrollers-Embedded-Debugger\\_User-Guide.pdf](http://www.atmel.com/Images/Atmel-42096-Microcontrollers-Embedded-Debugger_User-Guide.pdf)



| Data Field            | Data Type    | Example Content      |
|-----------------------|--------------|----------------------|
| Product Serial Number | ASCII string | 1774020200000010'\0' |
| Minimum Voltage [mV]  | uint16_t     | 3000                 |
| Maximum Voltage [mV]  | uint16_t     | 3600                 |
| Maximum Current [mA]  | uint16_t     | 30                   |

### 3.3 Power Supply

The SAM W25 Xplained Pro kit can be powered either by USB or by an external power source through the 4-pin power header, marked PWR. This connector is described in [“Xplained Pro Power Header” on page 8](#). The available power sources and specifications are listed in [Table 3-3, “Power Sources for SAM W25 Xplained Pro” on page 7](#).

**Table 3-3. Power Sources for SAM W25 Xplained Pro**

| Power Input           | Voltage Requirements                                                                                      | Current Requirements                                                                                                                                                                                | Connector Marking |
|-----------------------|-----------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------|
| External power        | 5V $\pm$ 2 % ( $\pm$ 100mV) for USB host operation.<br>4.3V to 5.5V if USB host operation is not required | Recommended minimum is 1A to be able to provide enough current for connected USB devices and the board itself. Recommended maximum is 2A due to the input protection maximum current specification. | PWR               |
| Embedded debugger USB | 4.4V to 5.25V (according to USB spec)                                                                     | 500mA (according to USB spec)                                                                                                                                                                       | DEBUG USB         |
| Target USB            | 4.4V to 5.25V (according to USB spec)                                                                     | 500mA (according to USB spec)                                                                                                                                                                       | TARGET USB        |

The kit will automatically detect which power sources are available and choose which one to use according to the following priority:

1. External power.
2. Embedded debugger USB.
3. Target USB.

#### Note

External power is required when the 500mA through the USB connector is not enough to power a connected USB device in a USB host application.

#### 3.3.1 Measuring SAM W25 Power Consumption

As part of an evaluation of the SAM W25 it can be of interest to measure its power consumption. Because the device has a separate power plane (VCC\_MCU\_P3V3) on this board it is possible to measure the current consumption by measuring the current that is flowing into this plane. The VCC\_MCU\_P3V3 plane is connected via a jumper to the main power plane (VCC\_TARGET\_P3V3) and by replacing the jumper with an ammeter it is possible to determine the current consumption. To locate the current measurement header, refer to [Figure 1-1, “SAM W25 Xplained Pro Evaluation Kit Overview” on page 4](#).

#### Warning

Do not power the board without having the jumper or an ammeter mounted. This can cause the SAM W25 to be powered through its I/O pins and cause undefined operation of the device.

### 3.4 Standard Headers and Connectors

#### 3.4.1 Xplained Pro Standard Extension Header

All Xplained Pro kits have one or more dual row, 20-pin, 100mil extension headers. Xplained Pro boards have male headers while Xplained Pro extensions have their female counterparts. Note that all pins are not always

connected. However, all the connected pins follow the defined pin-out described in [Table 3-4, “Xplained Pro Extension Header” on page 8](#). The extension headers can be used to connect a wide variety of Xplained Pro extensions to Xplained Pro boards and to access the pins of the target MCU on Xplained Pro board directly.

**Table 3-4. Xplained Pro Extension Header**

| Pin Number | Name          | Description                                                                                 |
|------------|---------------|---------------------------------------------------------------------------------------------|
| 1          | ID            | Communication line to the ID chip on extension board.                                       |
| 2          | GND           | Ground.                                                                                     |
| 3          | ADC(+)        | Analog to digital converter, alternatively positive part of differential ADC.               |
| 4          | ADC(-)        | Analog to digital converter, alternatively negative part of differential ADC.               |
| 5          | GPIO1         | General purpose I/O.                                                                        |
| 6          | GPIO2         | General purpose I/O.                                                                        |
| 7          | PWM(+)        | Pulse width modulation, alternatively positive part of differential PWM.                    |
| 8          | PWM(-)        | Pulse width modulation, alternatively negative part of differential PWM.                    |
| 9          | IRQ/GPIO      | Interrupt request line and/or general purpose I/O.                                          |
| 10         | SPI_SS_B/GPIO | Slave select for SPI and/or general purpose I/O.                                            |
| 11         | TWI_SDA       | Data line for two-wire interface. Always implemented, bus type.                             |
| 12         | TWI_SCL       | Clock line for two-wire interface. Always implemented, bus type.                            |
| 13         | USART_RX      | Receiver line of Universal Synchronous and Asynchronous serial Receiver and Transmitter.    |
| 14         | USART_TX      | Transmitter line of Universal Synchronous and Asynchronous serial Receiver and Transmitter. |
| 15         | SPI_SS_A      | Slave select for SPI. Should be unique if possible.                                         |
| 16         | SPI_MOSI      | Master out slave in line of Serial peripheral interface. Always implemented, bus type.      |
| 17         | SPI_MISO      | Master in slave out line of Serial peripheral interface. Always implemented, bus type.      |
| 18         | SPI_SCK       | Clock for Serial peripheral interface. Always implemented, bus type.                        |
| 19         | GND           | Ground.                                                                                     |
| 20         | VCC           | Power for extension board.                                                                  |

### 3.4.2 Xplained Pro Power Header

The power header can be used to connect external power to the SAM W25 Xplained Pro kit. The kit will automatically detect and switch to the external power if supplied. The power header can also be used as supply for external peripherals or extension boards. Care must be taken not to exceed the total current limitation of the on-board regulator for the 3.3V regulated output. To locate the power header, refer to [Figure 1-1, “SAM W25 Xplained Pro Evaluation Kit Overview” on page 4](#)

**Table 3-5. Power Connector PWR**

| Pin Number PWR header | Pin Name     | Description                                                    |
|-----------------------|--------------|----------------------------------------------------------------|
| 1                     | VCC_EXT_P5V0 | External 5V input                                              |
| 2                     | GND          | Ground                                                         |
| 3                     | VCC_P5V0     | Unregulated 5V (output, derived from one of the input sources) |



| Pin Number PWR header | Pin Name        | Description                                             |
|-----------------------|-----------------|---------------------------------------------------------|
| 4                     | VCC_TARGET_P3V3 | Regulated 3.3V (output, used as main power for the kit) |

#### Note

If the board is powered from a battery source it is recommended to use the PWR connector. If there is a power source connected to EDBG USB, the EDBG is activated and it will consume more power.

## 4. Hardware User Guide

### 4.1 Connectors

This chapter describes the implementation of the relevant connectors and headers on SAM W25 Xplained Pro and their connection to the ATSAMW25H18-MR510PB. The tables of connections in this chapter also describes which signals are shared between the headers and on-board functionality.

#### 4.1.1 I/O Extension Headers

The SAM W25 Xplained Pro header EXT1 offers access to the I/O of the integrated microcontroller in order to expand the board e.g. by connecting extensions to the board. These headers all comply with the standard extension header specified in [Xplained Pro Standard Extension Header on page 7](#). All headers have a pitch of 2.54mm.

**Table 4-1. Extension Header EXT1**

| Pin on EXT1        | SAM W25 pin | Function                            | Shared functionality                              |
|--------------------|-------------|-------------------------------------|---------------------------------------------------|
| 1 [ID]             | -           | -                                   | Communication line to ID chip on extension board. |
| 2 [GND]            | -           | -                                   | GND                                               |
| 3 [ADC(+)]         | PA02        | AIN[0]                              |                                                   |
| 4 [ADC(-)]         | PA03        | AIN[1]                              |                                                   |
| 5 [GPIO1]          | PB02        | GPIO                                |                                                   |
| 6 [GPIO2]          | PB03        | GPIO                                |                                                   |
| 7 [PWM(+)]         | PA10        | TCC0/WO[2]                          |                                                   |
| 8 [PWM(-)]         | PA11        | TCC0/WO[3]                          |                                                   |
| 9 [IRQ/GPIO]       | PA20        | EXTINT[4]                           |                                                   |
| 10 [SPI_SS_B/GPIO] | PA21        | GPIO/CS for SPI                     |                                                   |
| 11 [TWI_SDA]       | PA08        | SERCOM0 PAD[0] I <sup>2</sup> C SDA | EDBG                                              |
| 12 [TWI_SCL]       | PA09        | SERCOM0 PAD[1] I <sup>2</sup> C SCL | EDBG                                              |
| 13 [USART_RX]      | PB11        | SERCOM4 PAD[3] UART RX              | EDBG                                              |
| 14 [USART_TX]      | PB10        | SERCOM4 PAD[2] UART TX              | EDBG                                              |
| 15 [SPI_SS_A]      | PA17        | SERCOM1 PAD[1] SPI SS               |                                                   |
| 16 [SPI_MOSI]      | PA18        | SERCOM1 PAD[2] SPI MOSI             | EDBG and Serial Flash                             |
| 17 [SPI_MISO]      | PA16        | SERCOM1 PAD[0] SPI MISO             | EDBG and Serial Flash                             |
| 18 [SPI_SCK]       | PA19        | SERCOM1 PAD[3] SPI SCK              | EDBG and Serial Flash                             |
| 19 [GND]           | -           | -                                   | GND                                               |
| 20 [VCC]           | -           | -                                   | VCC                                               |

### 4.2 Peripherals

#### 4.2.1 Crystal

The SAM W25 Xplained Pro kit contains two crystals; One is used as clock source for the SAM W25 device and the second one is used as clock for EDBG. The crystal connected with SAM W25 has a cut-strap next to it that can be used to measure the oscillator safety factor. This is done by cutting the strap and adding a resistor across the strap. More information about oscillator allowance and safety factor can be found in appnote [AVR4100<sup>1</sup>](#).

**Table 4-2. External 32.768kHz Crystals**

| Pin on SAM W25 | Function |
|----------------|----------|
| PA00           | XIN32    |

<sup>1</sup> <http://www.atmel.com/images/doc8333.pdf>

| Pin on SAM W25 | Function |
|----------------|----------|
| PA01           | XOUT32   |

## 4.2.2 Mechanical Buttons

SAM W25 Xplained Pro contains two mechanical buttons. One button is the RESET button connected to the SAM W25 reset line and the other is a generic user configurable button. When a button is pressed it will drive the I/O line to GND.

**Table 4-3. Mechanical Buttons**

| Pin on SAM W25 | Silkscreen text |
|----------------|-----------------|
| RESETN HOST    | RESET           |
| PB23           | SW0             |

## 4.2.3 LED

There is one yellow LED available on the SAM W25 Xplained Pro board that can be turned on and off. The LED can be activated by driving the connected I/O line to GND.

**Table 4-4. LED Connections**

| Pin on SAM W25 | LED         |
|----------------|-------------|
| PA23           | Yellow LED1 |

## 4.2.4 USB

The SAM W25 Xplained Pro has a micro USB receptacle for use with the SAM W25 USB module. To be able to detect when a USB cable is connected, a GPIO is used to detect the VBUS voltage on the connector. The USB ID line is used to detect if a host mode cable is connected and will automatically switch on the power to the USB connector. The I/O connected to USB ID can be used to read the status of the line or manually override the voltage output. For other modes on the USB ID lines, refer to the USB organization specifications.

**Table 4-5. USB Connections**

| Pin on SAM W25 | USB            |
|----------------|----------------|
| PA05           | VBUS Detection |
| PA04           | USB ID         |
| PA24           | USB D-         |
| PA25           | USB D+         |

## 4.2.5 Serial Flash

The SAM W25 Xplained Pro has an onboard 8Mbit serial flash for non-volatile storage of data.

**Table 4-6. Serial Flash Connections**

| Pin on SAM W25 | Function                | Serial Flash | Shared functionality |
|----------------|-------------------------|--------------|----------------------|
| PA07           | GPIO                    | #CS          |                      |
| PA18           | SERCOM1 PAD[2] SPI MOSI | SI           | EXT1 and EDBG        |
| PA16           | SERCOM1 PAD[0] SPI MISO | SO           | EXT1 and EDBG        |
| PA19           | SERCOM1 PAD[3] SPI SCK  | SCK          | EXT1 and EDBG        |

## 4.3 Embedded Debugger Implementation

SAM W25 Xplained Pro contains an Embedded Debugger (EDBG) that can be used to program and debug the ATSAMW25H18-MR510PB using Serial Wire Debug (SWD). The Embedded Debugger also include a Virtual Com port interface over UART, an Atmel Data Gateway Interface over SPI and TWI and it monitors four of the SAM W25 GPIOs. Atmel Studio can be used as a front end for the Embedded Debugger.

### 4.3.1 Serial Wire Debug

The Serial Wire Debug (SWD) use two pins to communicate with the target. For further information on how to use the programming and debugging capabilities of the EDBG, see [“Embedded Debugger” on page 6](#).

**Table 4-7. SWD Connections**

| Pin on SAM W25 | Function  |
|----------------|-----------|
| PA30           | SWD clock |
| PA31           | SWD data  |

### 4.3.2 Virtual COM Port

The Embedded Debugger acts as a Virtual Com Port gateway by using one of the ATSAMW25H18-MR510PB UARTs. For further information on how to use the Virtual COM port see [“Embedded Debugger” on page 6](#).

**Table 4-8. Virtual COM Port Connections**

| Pin on SAM W25 | Function                                  |
|----------------|-------------------------------------------|
| PA22           | SERCOM3 PAD[0] UART TXD (SAM W25 TX line) |
| PA23           | SERCOM3 PAD[1] UART RXD (SAM W25 RX line) |

### 4.3.3 Atmel Data Gateway Interface

The Embedded Debugger features an Atmel Data Gateway Interface (DGI) by using either a SPI or I<sup>2</sup>C port. The DGI can be used to send a variety of data from the SAM W25 to the host PC. For further information on how to use the DGI interface see [“Embedded Debugger” on page 6](#).

**Table 4-9. DGI Interface Connections when using SPI**

| Pin on SAM W25 | Function                                                 |
|----------------|----------------------------------------------------------|
| PA06           | SERCOM1 PAD[1] SPI SS (Slave select) (SAM W25 is Master) |
| PA16           | SERCOM1 PAD[0] SPI MISO (Master In, Slave Out)           |
| PA18           | SERCOM1 PAD[2] SPI MOSI (Master Out, Slave in)           |
| PA19           | SERCOM1 PAD[3] SPI SCK (Clock Out)                       |

**Table 4-10. DGI Interface Connections when using I<sup>2</sup>C**

| Pin on SAM W25 | Function                        |
|----------------|---------------------------------|
| PA08           | SERCOM0 PAD[0] SDA (Data line)  |
| PA09           | SERCOM0 PAD[1] SCL (Clock line) |

Four GPIO lines are connected to the Embedded Debugger. The EDBG can monitor these lines and time stamp pin value changes. This makes it possible to accurately time stamp events in the SAM W25 application code. For further information on how to configure and use the GPIO monitoring features see [“Embedded Debugger” on page 6](#).

**Table 4-11. GPIO Lines Connected to the EDBG**

| Pin on SAM W25 | Function |
|----------------|----------|
| PB22           | GPIO0    |
| PB23           | GPIO1    |
| PA22           | GPIO2    |
| PA23           | GPIO3    |

## 5. Hardware Revision History and Known Issues

### 5.1 Identifying Product ID and Revision

The revision and product identifier of Xplained Pro boards can be found in two ways; through Atmel Studio or by looking at the sticker on the bottom side of the PCB.

By connecting an Xplained Pro board to a computer with Atmel Studio running, an information window will pop up. The first six digits of the serial number, which is listed under kit details, contain the product identifier and revision. Information about connected Xplained Pro extension boards will also appear in the Atmel Kits window.

The same information can be found on the sticker on the bottom side of the PCB. Most kits will print the identifier and revision in plain text as *A09-nnnn\rr* where *nnnn* is the identifier and *rr* is the revision. Boards with limited space have a sticker with only a QR-code which contains a serial number string.

The serial number string has the following format:

```
"nnnnrrssssssssss"
n = product identifier
r = revision
s = serial number
```

The kit identifier for SAM W25 Xplained Pro is 2449.

### 5.2 Revision 2

Revision 2 of SAM W25 Xplained Pro is the initial released version.

## 6. Document Revision History

| Document revision | Date    | Comment                  |
|-------------------|---------|--------------------------|
| 42463A            | 06/2015 | Initial document release |

## 7. Evaluation Board/Kit Important Notice

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