



RF360
Europe GmbH

SAW components

SAW duplexer

WCDMA / LTE band 3

Series/type: B8672
Ordering code: B39182B8672P810

Date: March 03, 2016
Version: 2.4

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SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

Table of contents

1	Application	3
2	Features	3
3	Package	4
4	Pin configuration	4
5	Matching circuit	5
6	Characteristics	6
7	Maximum ratings	11
8	Transmission coefficients	12
9	Reflection coefficients	14
10	EVMS	15
11	Packing material	17
12	Marking	20
13	Soldering profile	22
14	Annotations	23
15	Cautions and warnings	24
	Important notes	25

Data sheet

1 Application

- Low-loss SAW duplexer for mobile telephone
LTE and WCDMA Band 3 systems
- Low insertion attenuation
- Low amplitude ripple
- Usable pass band 75 MHz

2 Features

- Package size $1.8 \pm 0.1 \text{ mm} \times 1.4 \pm 0.1 \text{ mm}$
- Package height 0.475 mm (max.)
- Approximate weight 4 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 3 (MSL3)

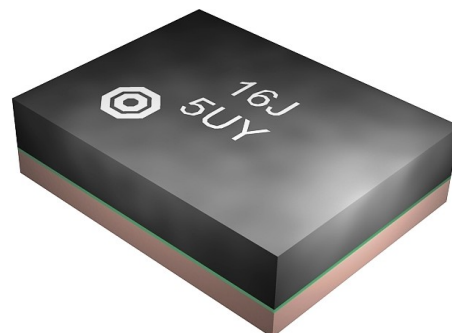
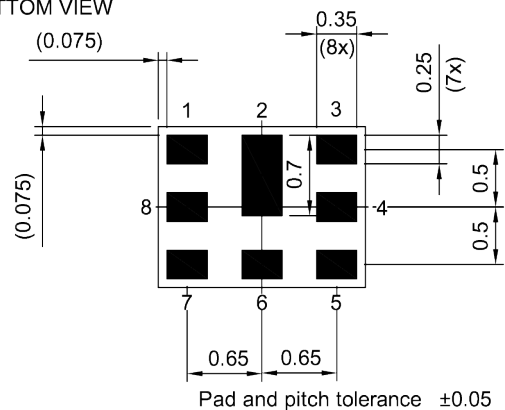
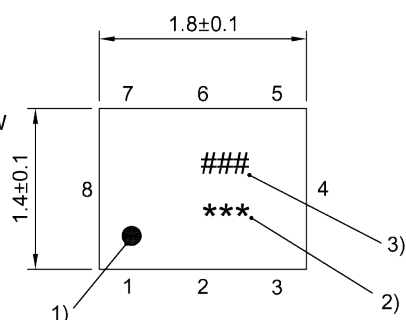


Figure 1: Picture of component with example of product marking.

Data sheet
3 Package
BOTTOM VIEW

SIDE VIEW

TOP VIEW


- 1) Marking for pad number 1
- 2) Encoded lot number
- 3) Please refer to caption below

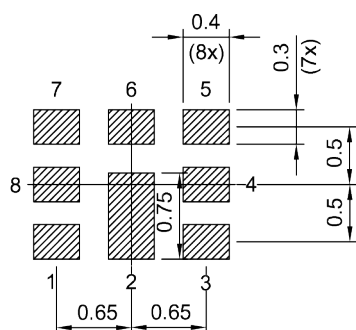
Land pattern THRU VIEW

 Landing pad tolerance -0.02

Figure 2: Drawing of package with package height A = 0.475 mm (max.). See Simplified drawings (p. 24).

4 Pin configuration

- 1 RX
- 3 TX
- 6 ANT
- 2, 4, 5, 7, 8 Ground

Data sheet

5 Matching circuit

$$\blacksquare L_{p6} = 3.5 \text{ nH}$$

$$\blacksquare L_{s3} = 0.5 \text{ nH}$$

$$\blacksquare L_{s1} = 2.0 \text{ nH}$$

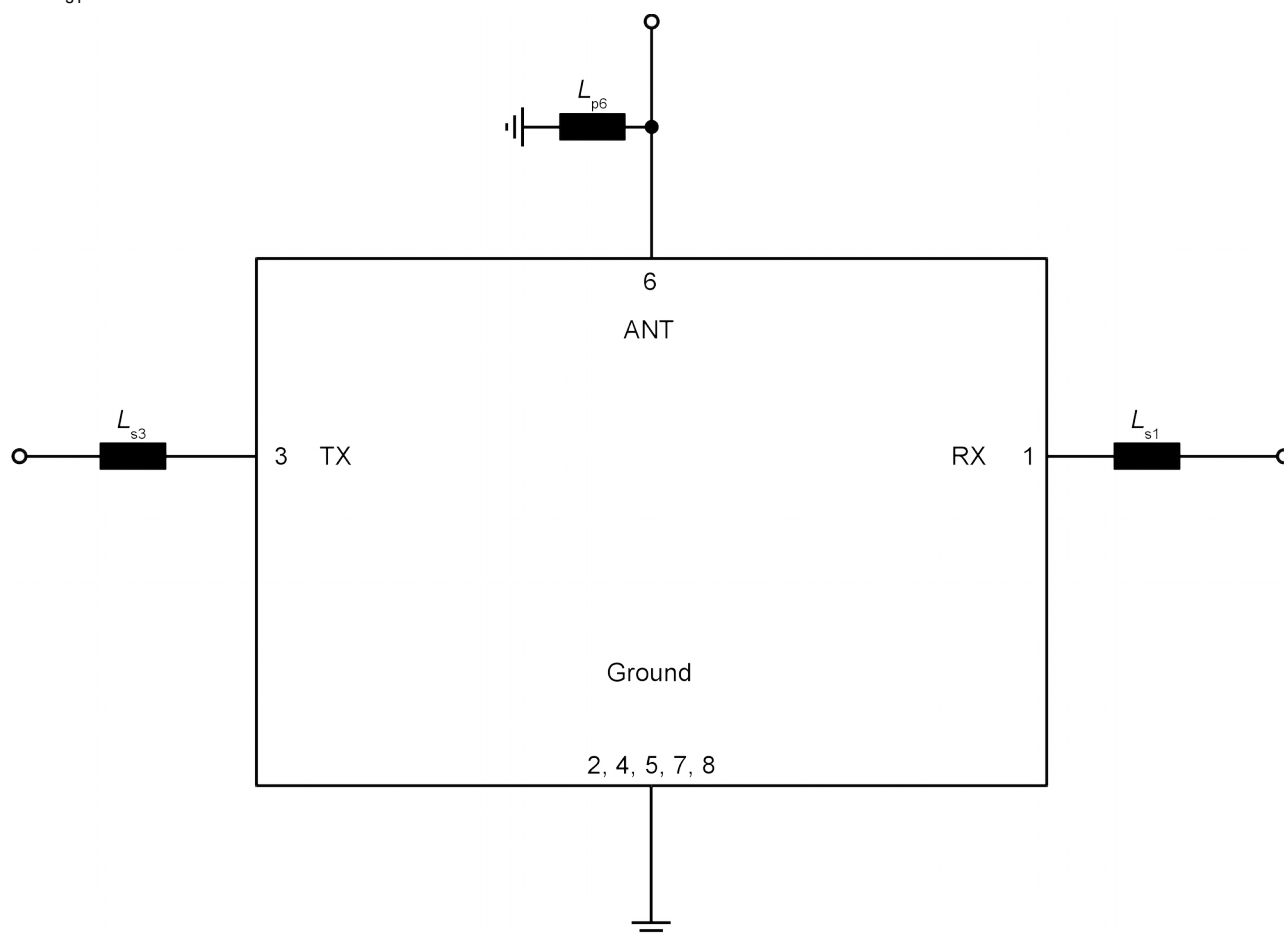


Figure 3: Schematic of matching circuit.

SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

6 Characteristics

6.1 TX – ANT

Temperature range for specification	T_{SPEC}	= -30 °C ... +85 °C
TX terminating impedance	Z_{TX}	= 50 Ω with ser. 0.5 nH ¹⁾
ANT terminating impedance	Z_{ANT}	= 50 Ω with par. 3.5 nH ¹⁾
RX terminating impedance	Z_{RX}	= 50 Ω with ser. 2.0 nH ¹⁾

Characteristics TX – ANT ²⁾				min. for T_{SPEC}	typ. @+25 °C	max. for T_{SPEC}	
Center frequency		f_{C}		—	1747.5	—	MHz
Maximum insertion attenuation		$\alpha_{\text{LTE,max}}^{3)}$		—	1.9	2.9	dB
	1712.5... 1782.5	MHz		—	1.9	2.9	dB
Amplitude ripple (p-p) (over any 5 MHz)		$\Delta\alpha_{\text{LTE}}^{3)}$		—	0.5	1.5	dB
	1712.5... 1782.5	MHz		—	0.5	1.5	dB
Maximum VSWR		VSWR _{max}		—	1.6	2.0	
@ TX port	1710.24... 1784.76	MHz		—	1.6	2.0	
@ ANT port	1710.24... 1784.76	MHz		—	1.5	2.0	
Maximum error vector magnitude		EVM _{max} ⁴⁾		—	1.5	6.0	%
	1712.4... 1782.6	MHz		—	1.5	6.0	%
Minimum attenuation		α_{min}		32	36	—	dB
	10... 1565.5	MHz		32	36	—	dB
	703... 748	MHz		40	44	—	dB
	716... 756	MHz		40	44	—	dB
	814... 849	MHz		37	42	—	dB
	824... 849	MHz		37	42	—	dB
	830... 845	MHz		37	42	—	dB
	832... 862	MHz		37	42	—	dB
	880... 915	MHz		36	40	—	dB
	925... 960	MHz		35	39	—	dB
	1226... 1250	MHz		32	36	—	dB
	1496... 1511	MHz		33	39	—	dB
	1559... 1563	MHz		40	45	—	dB
	1565.42... 1573.37	MHz		40	47	—	dB
	1573.37... 1577.47	MHz		40	49	—	dB
	1577.47... 1585.42	MHz		40	48	—	dB
	1597.55... 1605.89	MHz		37	44	—	dB
	1605.89... 1680	MHz		18	44	—	dB

SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

Characteristics TX – ANT ²⁾				min. for T_{SPEC}	typ. @+25 °C	max. for T_{SPEC}	
1807.5... 1877.5	MHz	$\alpha_{LTE,min}^{3)}$		44	51	—	dB
1920... 1980	MHz	α_{min}		25	34	—	dB
2110... 2170	MHz	α_{min}		27	33	—	dB
2400... 2500	MHz	α_{min}		26	33	—	dB
2440... 2494	MHz	α_{min}		26	33	—	dB
2496... 2690	MHz	α_{min}		23	30	—	dB
2500... 2570	MHz	α_{min}		25	32	—	dB
2620... 2690	MHz	α_{min}		23	30	—	dB
3420... 3570	MHz	α_{min}		20	26	—	dB
4900... 5950	MHz	α_{min}		10	20	—	dB
5100... 5385	MHz	α_{min}		10	24	—	dB
5130... 5355	MHz	α_{min}		10	24	—	dB

¹⁾ See Matching circuit (p. 5).

²⁾ Specified min/max values are valid for a testing power of +10 dBm.

³⁾ LTE – Averaged value of linear S-parameter over 5 MHz.

⁴⁾ Error Vector Magnitude (EVM) based on definition given in 3GPP TS 25.141.

SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

6.2 ANT – RX

Temperature range for specification

T_{SPEC} = -30 °C ... +85 °C

TX terminating impedance

Z_{TX} = 50 Ω with ser. 0.5 nH¹⁾

ANT terminating impedance

Z_{ANT} = 50 Ω with par. 3.5 nH¹⁾

RX terminating impedance

Z_{RX} = 50 Ω with ser. 2.0 nH¹⁾

Characteristics ANT – RX ²⁾				min. for T_{SPEC}	typ. @+25 °C	max. for T_{SPEC}	
Center frequency		f_C		—	1842.5	—	MHz
Maximum insertion attenuation		$\alpha_{LTE,max}^{3)}$					
	1807.5... 1877.5	MHz		—	2.2	2.9 ⁴⁾	dB
	1807.5... 1877.5	MHz		—	2.2	3.4	dB
Amplitude ripple (p-p) (over any 5 MHz)		$\Delta\alpha_{LTE}^{3)}$					
	1807.5... 1877.5	MHz		—	0.5	2.0	dB
Maximum VSWR		VSWR _{max}					
@ ANT port	1805.24... 1879.76	MHz		—	1.4	2.0	
@ RX port	1805.24... 1879.76	MHz		—	1.7	2.1	
Maximum error vector magnitude		EVM _{max} ⁵⁾					
	1807.4... 1877.6	MHz		—	2.3	6.0	%
Minimum attenuation							
	10... 200	MHz	α_{min}	50	70	—	dB
	50... 95	MHz	α_{min}	50	70	—	dB
	95... 1710	MHz	α_{min}	40	46	—	dB
	200... 1615	MHz	α_{min}	40	46	—	dB
	718... 748	MHz	α_{min}	40	57	—	dB
	814... 849	MHz	α_{min}	40	56	—	dB
	832... 862	MHz	α_{min}	40	55	—	dB
	880... 915	MHz	α_{min}	40	54	—	dB
	1447... 1463	MHz	α_{min}	40	47	—	dB
	1615... 1690	MHz	α_{min}	45	49	—	dB
	1712.5... 1782.5	MHz	$\alpha_{LTE,min}^{3)}$	45	54	—	dB
	1920... 1980	MHz	α_{min}	40	50	—	dB
	1980... 2400	MHz	α_{min}	32	39	—	dB
	2400... 2500	MHz	α_{min}	37	48	—	dB
	2496... 2690	MHz	α_{min}	40	53	—	dB
	2500... 2570	MHz	α_{min}	45	53	—	dB
	2570... 3515	MHz	α_{min}	40	49	—	dB

SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

Characteristics ANT – RX ²⁾				min. for T_{SPEC}	typ. @+25 °C	max. for T_{SPEC}	
	3515... 3665	MHz	α_{min}	47	54	—	dB
	3665... 3760	MHz	α_{min}	40	54	—	dB
	3760... 6000	MHz	α_{min}	37	46	—	dB
	4900... 5950	MHz	α_{min}	37	46	—	dB
	5205... 5660	MHz	α_{min}	37	48	—	dB

¹⁾ See Matching circuit (p. 5).

²⁾ Specified min/max values are valid for a testing power of +10 dBm.

³⁾ LTE – Averaged value of linear S-parameter over 5 MHz.

⁴⁾ Valid for temperature $T_{SPEC} = +25\text{ °C}...+85\text{ °C}$.

⁵⁾ Error Vector Magnitude (EVM) based on definition given in 3GPP TS 25.141.

SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

6.3 TX – RX

Temperature range for specification	T_{SPEC}	= -30 °C ... +85 °C
TX terminating impedance	Z_{TX}	= 50 Ω with ser. 0.5 nH ¹⁾
ANT terminating impedance	Z_{ANT}	= 50 Ω with par. 3.5 nH ¹⁾
RX terminating impedance	Z_{RX}	= 50 Ω with ser. 2.0 nH ¹⁾

Characteristics TX – RX ²⁾				min. for T_{SPEC}	typ. @+25 °C	max. for T_{SPEC}	
Minimum isolation							
	1712.5... 1782.5	MHz	$\alpha_{LTE,min}^{3)}$	52	56	—	dB
	1715... 1780	MHz	$\alpha_{LTE,min}^{4)}$	53	56	—	dB
	1807.5... 1877.5	MHz	$\alpha_{LTE,min}^{3)}$	50	57	—	dB

- ¹⁾ See Matching circuit (p. 5).
²⁾ Specified min/max values are valid for a testing power of +10 dBm.
³⁾ LTE – Averaged value of linear S-parameter over 5 MHz.
⁴⁾ LTE – Averaged value of linear S-parameter over 10 MHz.

SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

7 Maximum ratings

Storage temperature	$T_{\text{STG}} = -40\text{ °C} \dots +90\text{ °C}$	
DC voltage	$V_{\text{DC}} = 0\text{ V (max.)}^{1)}$	
ESD voltage		
	$V_{\text{ESD}}^{2)}$ 50 V (max.)	Machine model.
	$V_{\text{ESD}}^{3)}$ 300 V (max.)	Human body model.
	$V_{\text{ESD}}^{4)}$ 500 V (max.)	Charged device model.
Input power @ TX port: 1712.5 ... 1782.5 MHz	$P_{\text{IN}} = 29\text{ dBm}$	5 MHz LTE uplink @ 50 °C, 5000h.

¹⁾ DC resistance at RX output might be less than 100 MΩ at elevated temperatures. Hence, using blocking capacitors is recommended.

²⁾ According to JESD22-A115B (MM – Machine Model), 10 negative & 10 positive pulses.

³⁾ According to JESD22-A114F (HBM – Human Body Model), 1 negative & 1 positive pulse.

⁴⁾ According to JESD22-C101C (CDM – Field Induced Charged Device Model), 3 negative & 3 positive pulses.

Data sheet

8 Transmission coefficients

8.1 TX – ANT

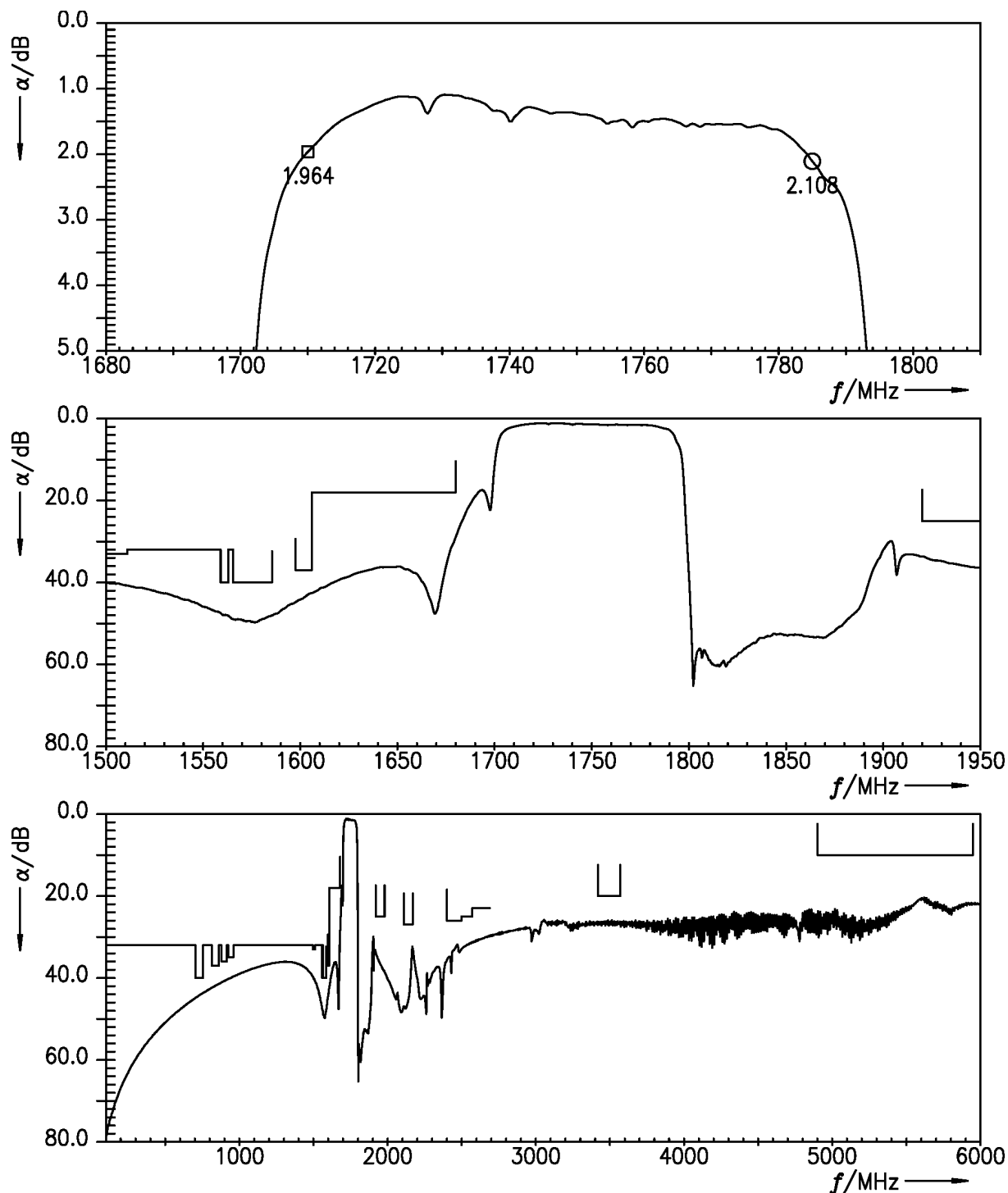


Figure 4: Attenuation TX – ANT.

Data sheet

8.2 ANT – RX

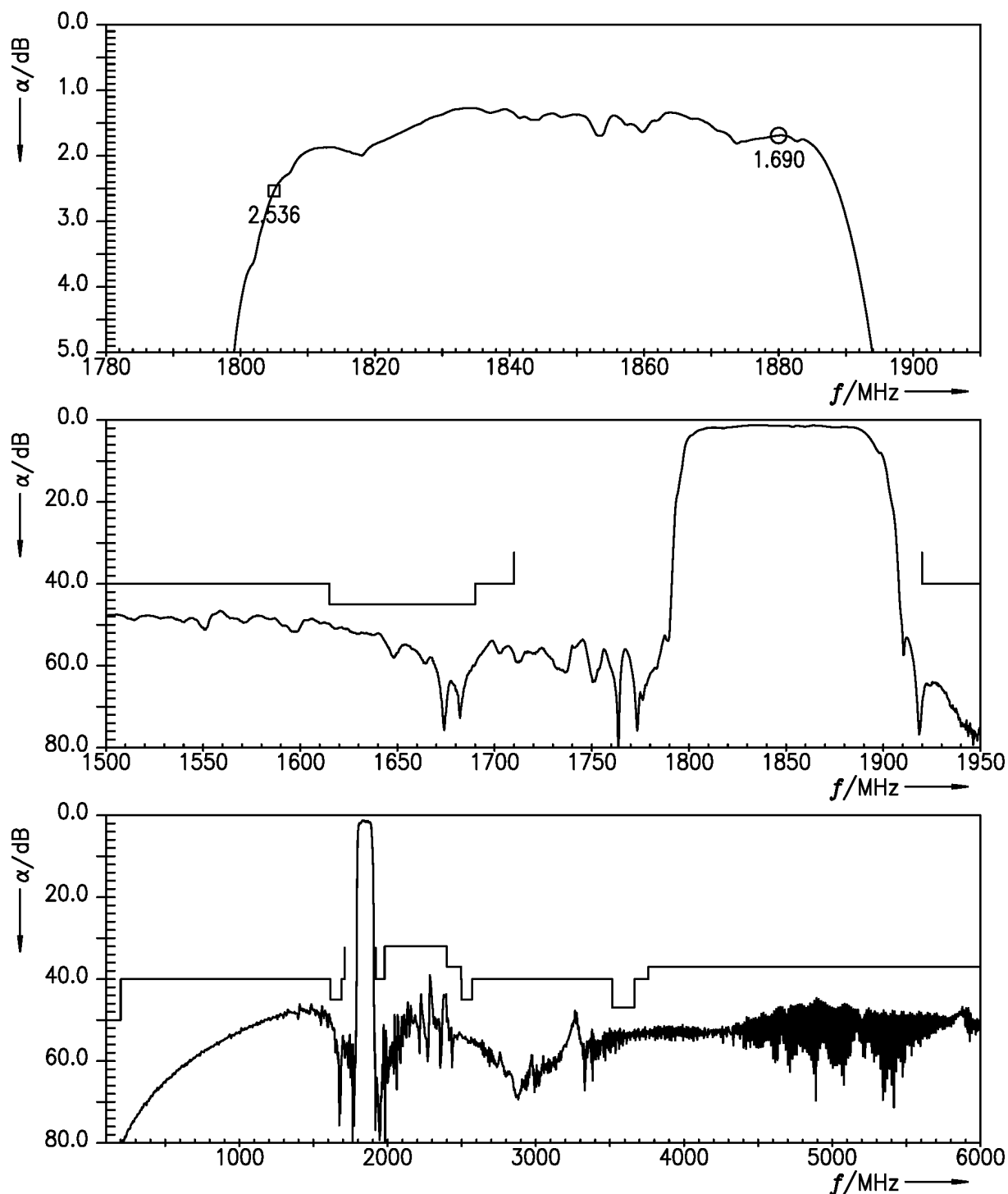


Figure 5: Attenuation ANT – RX.

Data sheet

9 Reflection coefficients

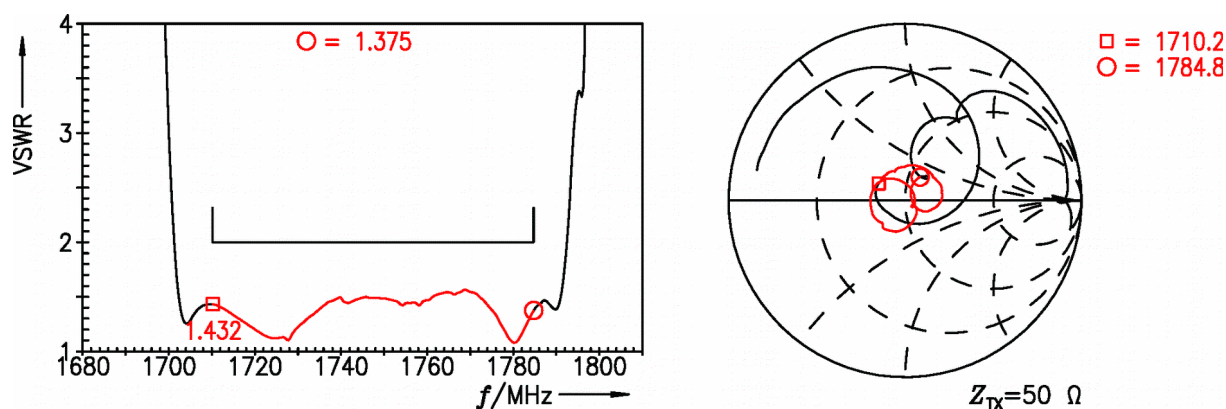


Figure 6: Reflection coefficient at TX port.

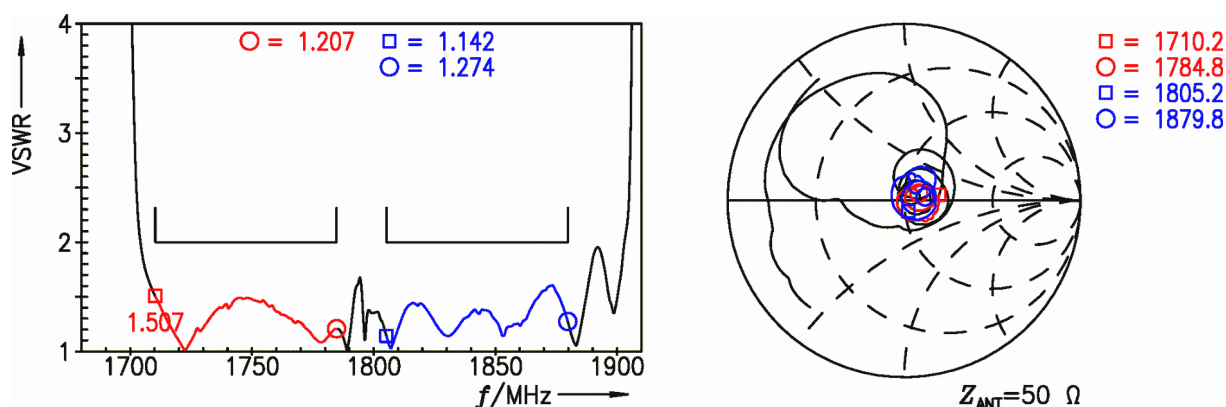


Figure 7: Reflection coefficient at ANT port (TX and RX frequencies).

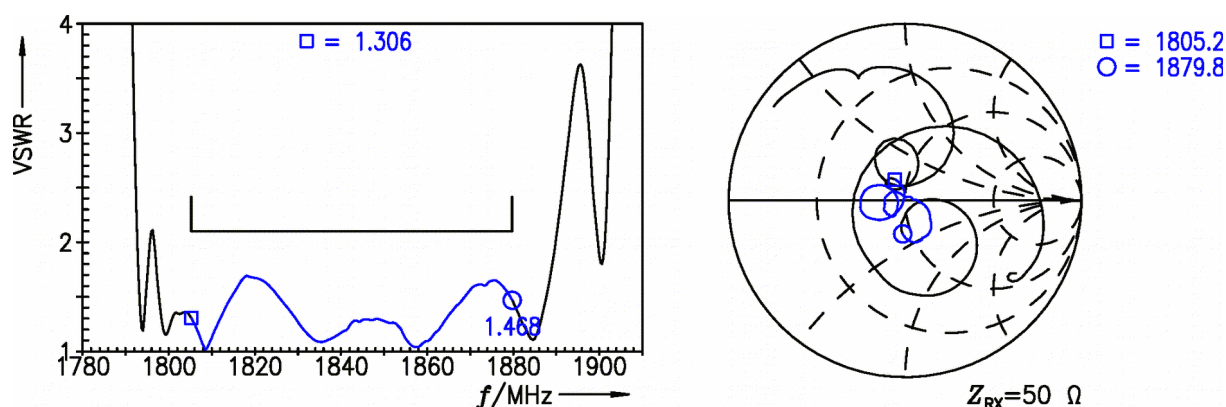


Figure 8: Reflection coefficient at RX port.

Data sheet

10 EVMs

10.1 TX – ANT

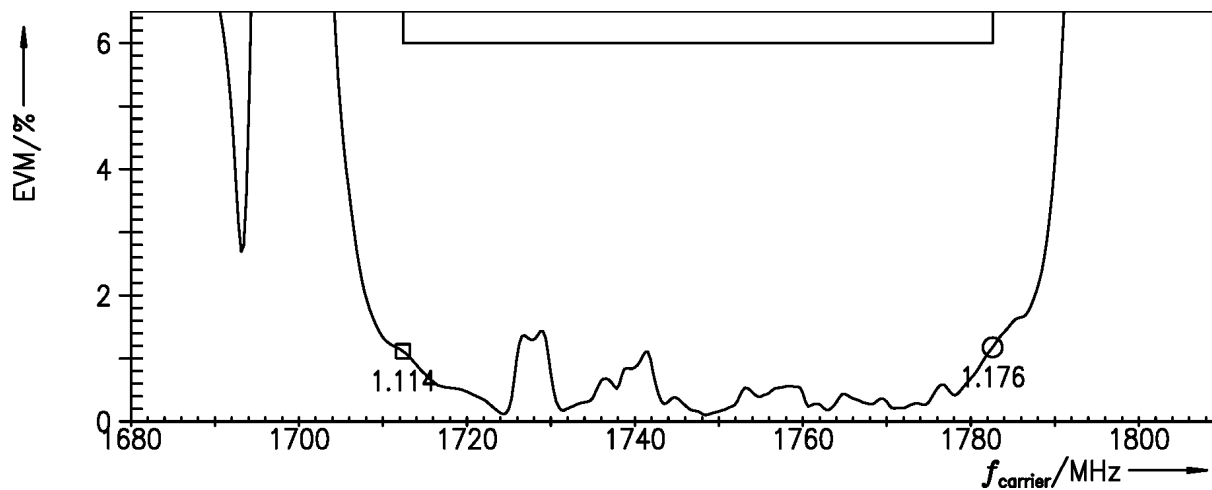


Figure 9: Error vector magnitude TX – ANT.

Data sheet

10.2 ANT – RX

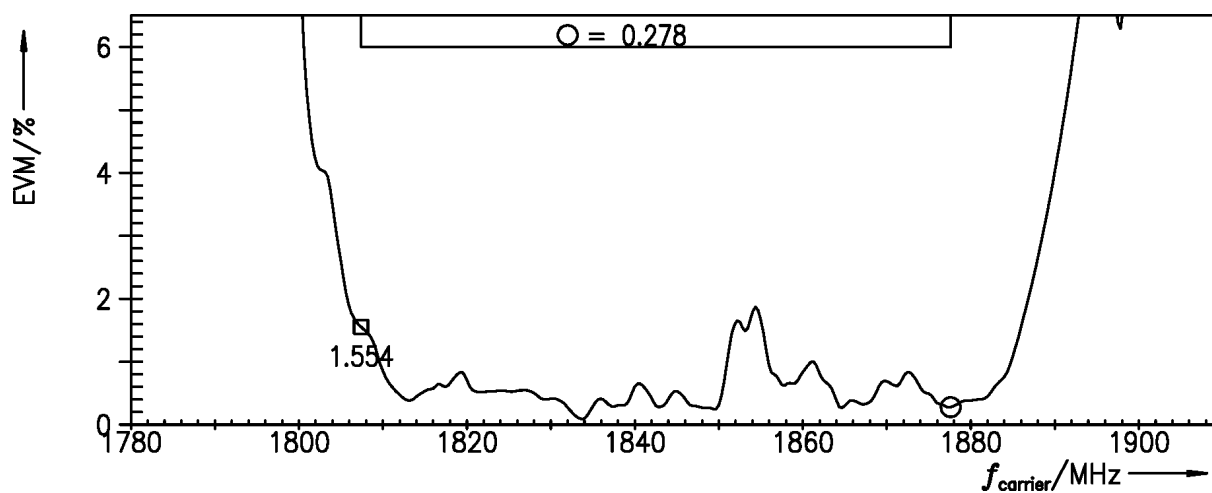


Figure 10: Error vector magnitude ANT – RX.

Data sheet

11 Packing material

11.1 Tape

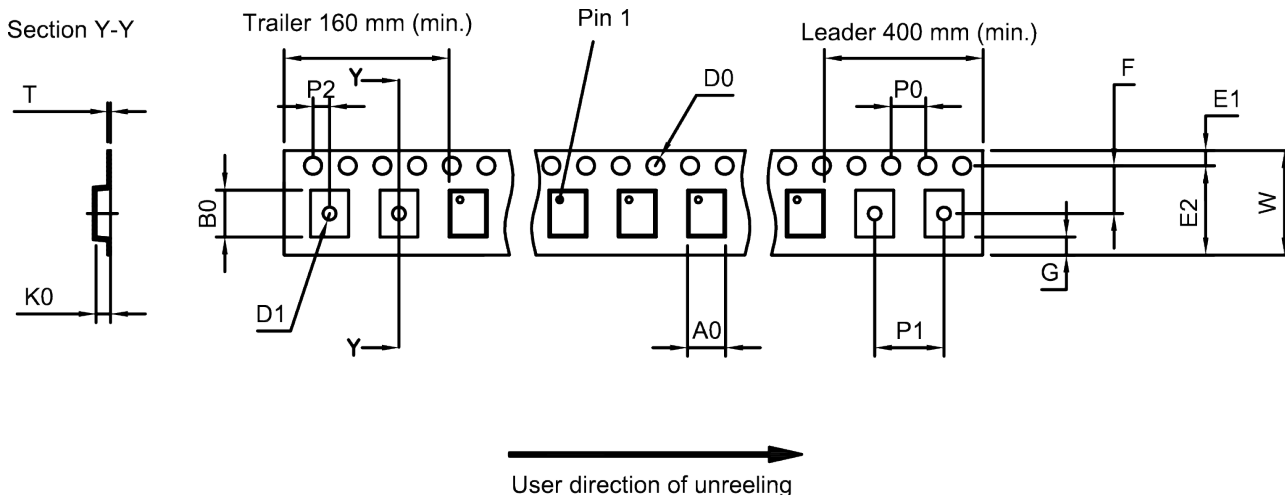


Figure 11: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A_0	1.62 ± 0.05 mm	E_2	6.25 mm (min.)	P_1	4.0 ± 0.1 mm
B_0	2.04 ± 0.05 mm	F	3.5 ± 0.05 mm	P_2	2.0 ± 0.05 mm
D_0	1.5 ± 0.05 mm	G	0.75 mm (min.)	T	0.25 ± 0.02 mm
D_1	0.8 ± 0.05 mm	K_0	0.62 ± 0.05 mm	W	8.0 ± 0.1 mm
E_1	1.75 ± 0.1 mm	P_0	4.0 ± 0.1 mm		

Table 1: Tape dimensions.

11.2 Reel with diameter of 180 mm

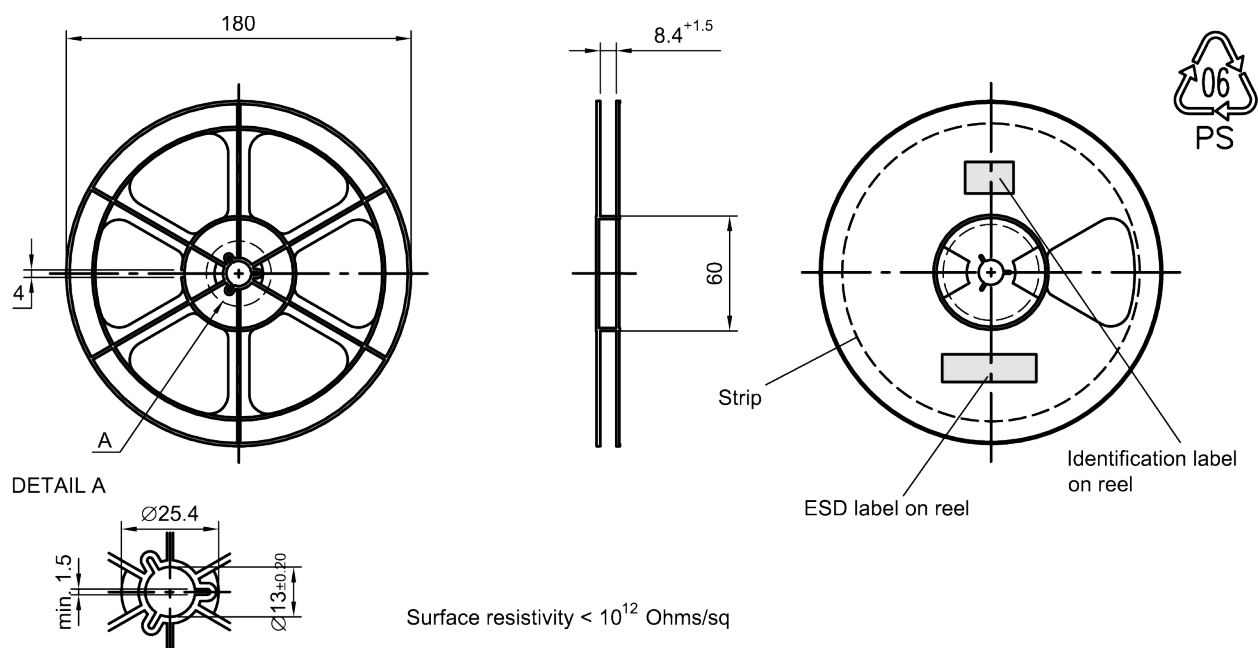


Figure 12: Drawing of reel (first-angle projection) with diameter of 180 mm.

Data sheet

Dimensions [mm]

X = 220±5

Y = 235±5

Sealing area 10±3

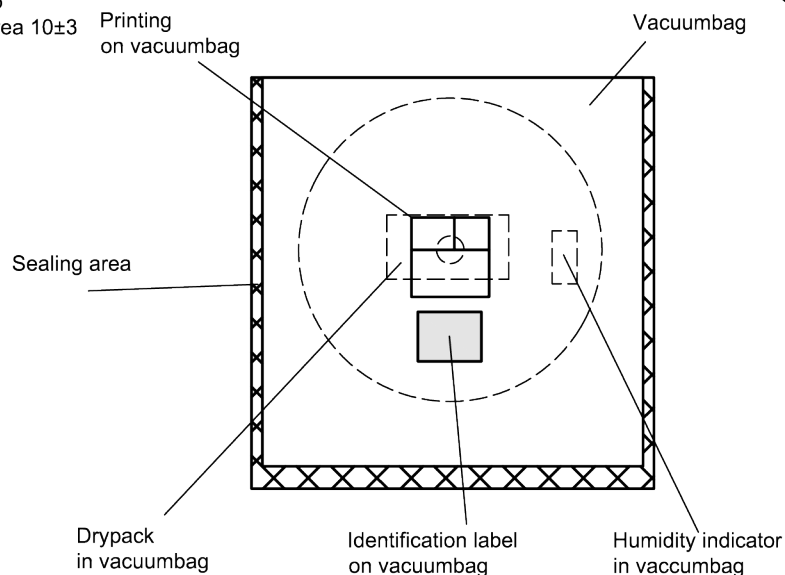


Figure 13: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

Dimensions [mm]

L = 188

B = 188

H = 30

Tolerance ±5

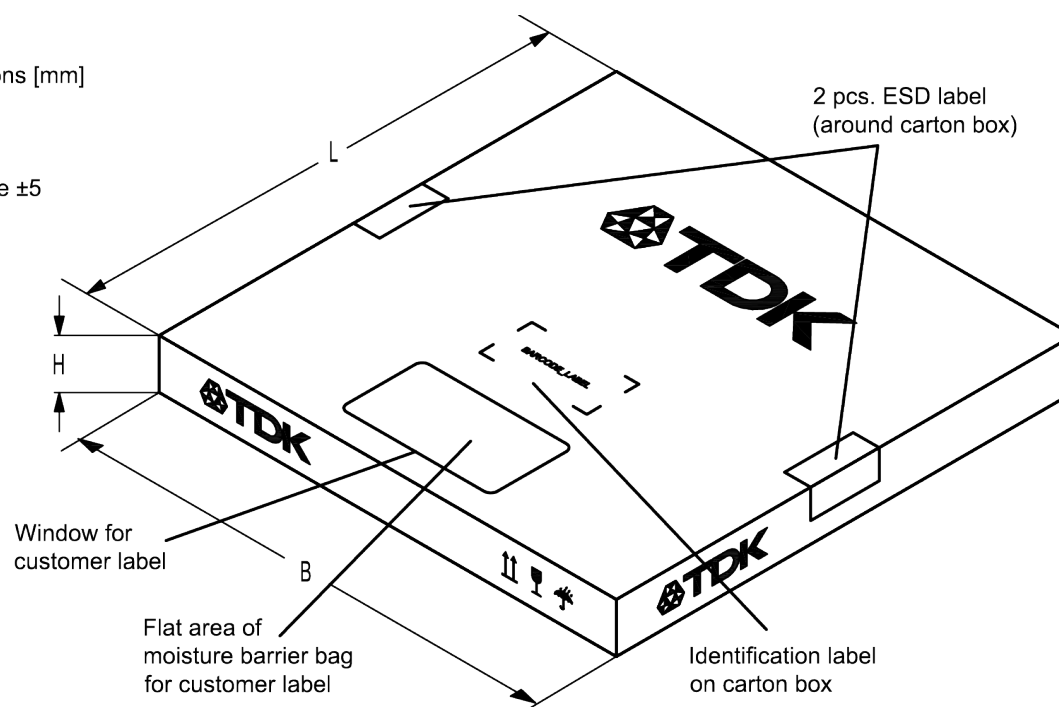


Figure 14: Drawing of folding box for reel with diameter of 180 mm.

Data sheet

11.3 Reel with diameter of 330 mm

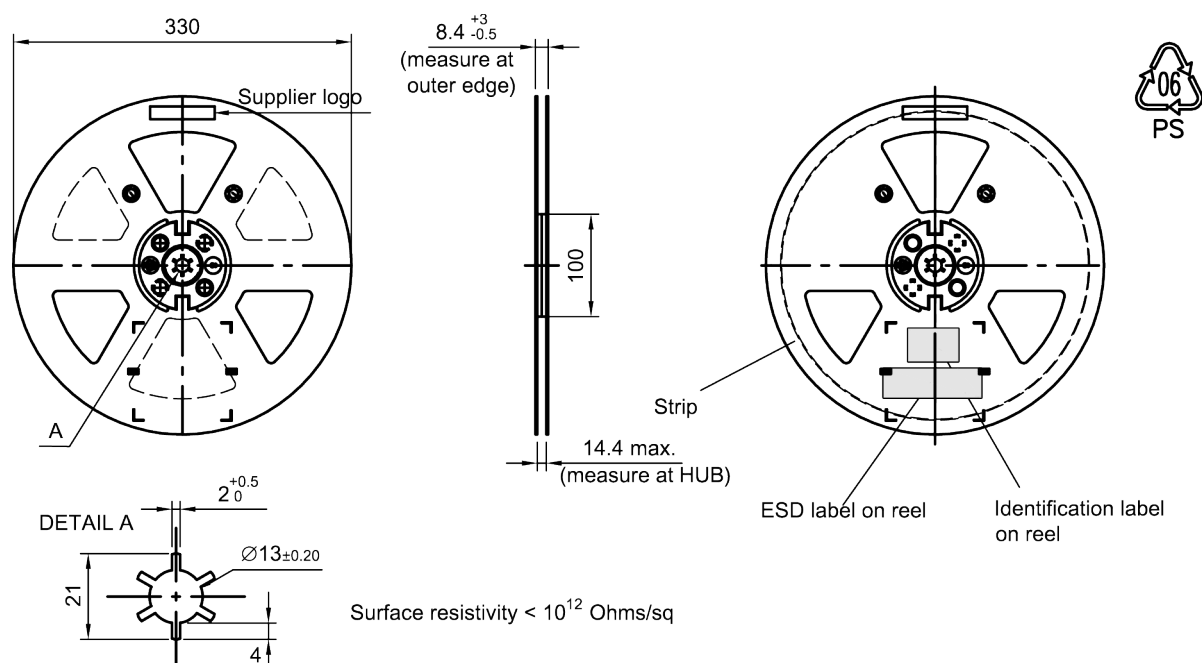


Figure 15: Drawing of reel (first-angle projection) with diameter of 330 mm.

Dimensions [mm]

X = 400+5

Y = 418+5

Sealing area 10±3

Printing on vacuum bag

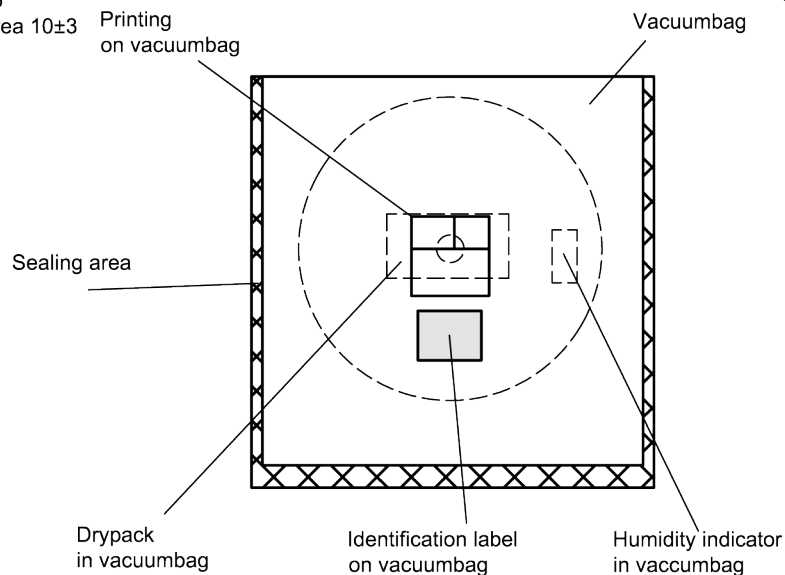


Figure 16: Drawing of moisture barrier bag (MBB) for reel with diameter of 330 mm.

Data sheet

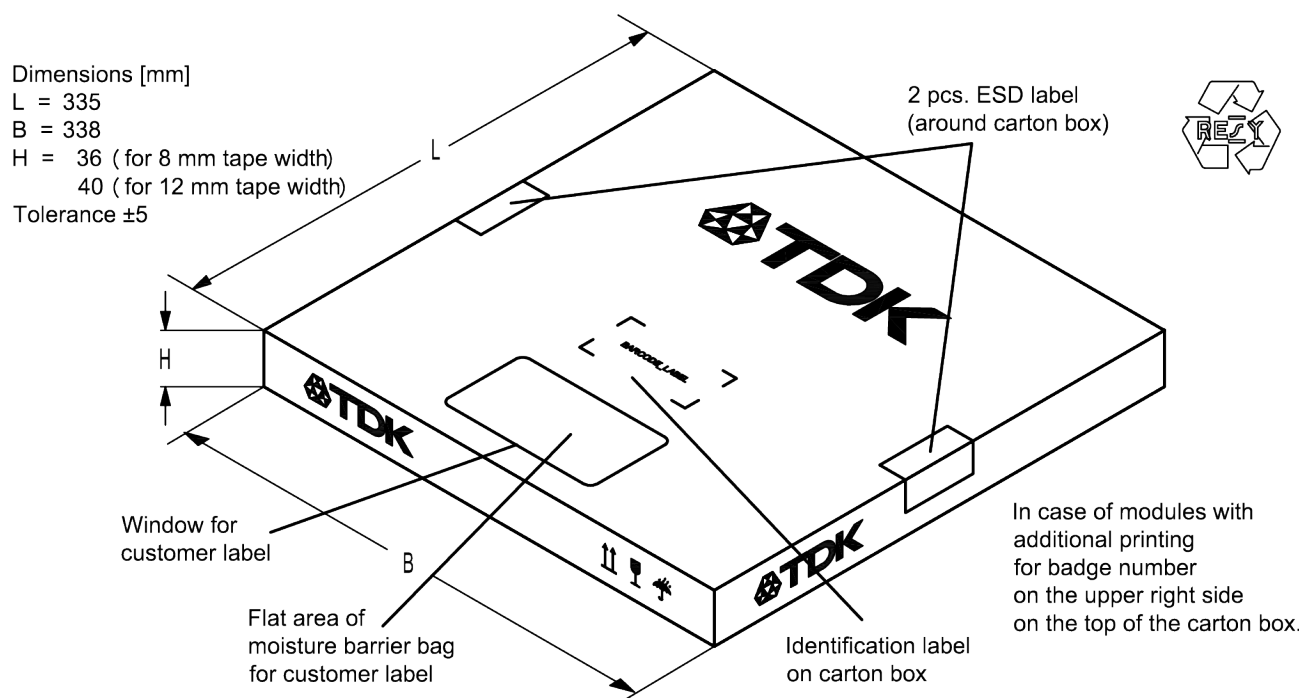


Figure 17: Drawing of folding box for reel with diameter of 330 mm.

12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

■ Type number:

The 4 digit type number of the ordering code, is encoded by a special BASE32 code into a 3 digit marking.

e.g., B3xxxxB**1234**xxxx,

Example of decoding type number marking on device

in decimal code.

16J

=>

1234

$1 \times 32^2 + 6 \times 32^1 + 18 (=J) \times 32^0$

=

1234

The BASE32 code for product type B8672 is 8F0.

■ Lot number:

The last 5 digits of the lot number, are encoded based on a special BASE47 code into a 3 digit marking.

e.g.,

12345,

Example of decoding lot number marking on device

in decimal code.

5UY

=>

12345

$5 \times 47^2 + 27 (=U) \times 47^1 + 31 (=Y) \times 47^0$

=

12345

Data sheet

Adopted BASE32 code for type number			
Decimal value	Base32 code	Decimal value	Base32 code
0	0	16	G
1	1	17	H
2	2	18	J
3	3	19	K
4	4	20	M
5	5	21	N
6	6	22	P
7	7	23	Q
8	8	24	R
9	9	25	S
10	A	26	T
11	B	27	V
12	C	28	W
13	D	29	X
14	E	30	Y
15	F	31	Z

Adopted BASE47 code for lot number			
Decimal value	Base47 code	Decimal value	Base47 code
0	0	24	R
1	1	25	S
2	2	26	T
3	3	27	U
4	4	28	V
5	5	29	W
6	6	30	X
7	7	31	Y
8	8	32	Z
9	9	33	b
10	A	34	d
11	B	35	f
12	C	36	h
13	D	37	n
14	E	38	r
15	F	39	t
16	G	40	v
17	H	41	\
18	J	42	?
19	K	43	{
20	L	44	}
21	M	45	<
22	N	46	>
23	P		

Table 2: Lists for encoding and decoding of marking.

SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

13 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
$T > 220\text{ °C}$	30 s to 70 s
$T > 230\text{ °C}$	min. 10 s
$T > 245\text{ °C}$	max. 20 s
$T \geq 255\text{ °C}$	–
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

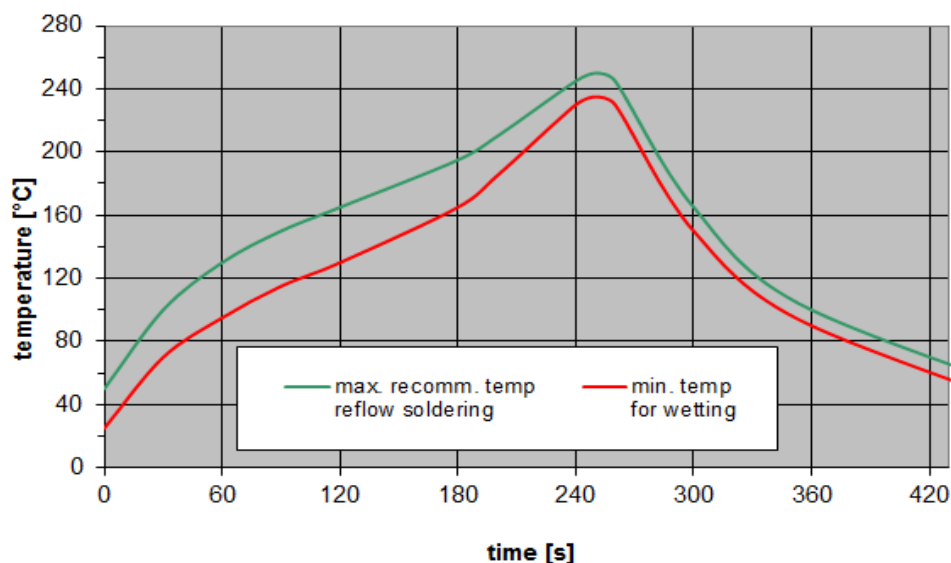


Figure 18: Recommended reflow profile for convection and infrared soldering – lead-free solder.

SAW components	B8672
SAW duplexer	1747.5 / 1842.5 MHz

Data sheet

14 Annotations

14.1 Matching coils

See TDK inductor pdf-catalog <http://www.tdk.co.jp/tefe02/coil.htm#aname1> and Data Library for circuit simulation <http://www.tdk.co.jp/etvcl/index.htm>.

14.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local EPCOS sales office.

14.4 Ordering codes and packing units

Ordering code	Packing unit
B39182B8672P810S45	15000 pcs

Table 4: Ordering codes and packing units.

Data sheet

15 Cautions and warnings

15.1 Display of ordering codes for EPCOS products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of EPCOS, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.epcos.com/orderingcodes.

15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local EPCOS sales office.

15.4 Simplified drawings

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on EPCOS internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of EPCOS, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Projection method

Unless otherwise specified first-angle projection is applied.

Important notes

The following applies to all products named in this publication:

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