



This version (05 Feb 2019 16:17) was **approved** by Brandon.  
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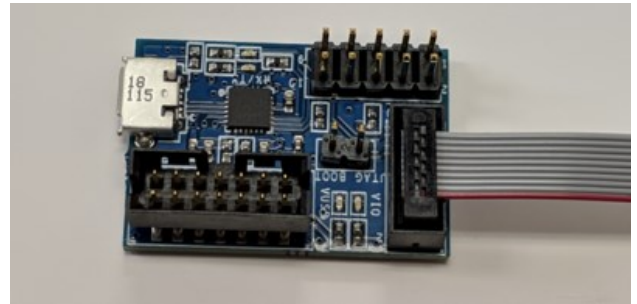
## ADALM-UARTJTAG

In a few of the designs we have done, like the [PlutoSDR](#) and [M2k](#) there hasn't been enough room on the PCB for serial and JTAG, so we put it on a small breakout board, which communicates to the main platform with a small 10 pin ribbon cable.

This does include the Silicon Labs [CP2103-GM](#) USB to UART bridge, which you can plug a [USB](#) cable into.

This also includes connection to Xilinx JTAG cable as well. You will need a [Xilinx JTAG Cable](#) or [equivalent](#) to use it.

Anyone is free to use the same pinout on their platform - or copy the schematics and make it themselves.



1. [Hardware](#) including schematics.
2. [CP2103 Drivers](#)

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