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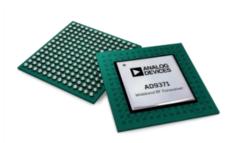




This version (05 Jun 2018 16:17) was approved by mhennerich. The Previously approved version (22 Nov 2017 17:00) is available.



AD9371 & AD9375 Prototyping Platform User Guide



The ►ADRV9371-W/PRBZ, ►ADRV9371-N/PCBZ and ► ADRV9375-N/PCBZ are FMC radio cards for the ► AD9371 respectively ► AD9375, a highly integrated RF Transceiver $^{\rm TM}$. While the complete chip level design package can be found on the the the ADI web site, information on the card and how to use it, the design package that surrounds it, and the software which can make it work can be found here.

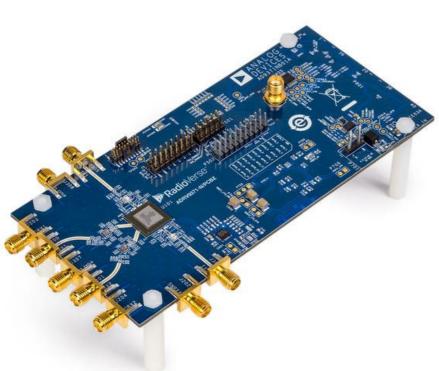


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People who follow the flow that is outlined, have a much better experience with things. However, like many things, documentation is never as complete as it should be. If you have any questions, feel free to ask.

- 1. Use the board to better understand the AD9371/AD9375
 - 1. What you need to get started
 - 2. Quick Start Guides
 - 1. Linux on ZC706
 - 2. Configure a pre-existing SD-Card
 - 3. Update the old card you received with your hardware
 - 3. Linux Applications
 - 1. IIO Scope
 - 1. AD9371/AD9375 IIO Scope View
 - 2. AD9371/AD9375 Control IIO Scope Plugin









- 3. Advanced AD9371/AD9375 Control IIO Scope Plugin
- 2. FRU EEPROM Utility
- 4. Push custom data into/out of the AD9371/AD9375
 - 1. Basic Data files and formats
 - 2. Stream data into/out of MATLAB
- 2. Design with the AD9371/AD9375
 - 1. Understanding the AD9371/AD9375
 - 1. ► AD9371 Product page
 - 2. AD9375 Product page
 - 3. Full Datasheet and chip design package
 - 4. MATLAB Filter Wizard / Profile Generator for AD9371
 - 2. Hardware in the Loop / How to design your own custom BaseBand
 - 1. GNU Radio
 - 3. Design a custom AD9371/AD9375 based platform
 - 1. Linux software
 - 1. AD9371/AD9375 Linux Device Driver
 - 1. Customizing the devicetree on the target
 - 2. AD9528 Low Jitter Clock Generator Linux Driver
 - 3. AD7291 IIO ADC Linux Driver
 - 4. JESD204B Transmit Linux Driver
 - 5. JESD204B Receive Linux Driver
 - 6. AXI JESD204B GT HDL Linux Driver
 - 1. JESD204 Eye Scan
 - 7. AXI ADC HDL Linux Driver
 - 8. AXI DAC HDL Linux Driver
 - 2. AD9371/AD9375 No-OS System Level Design Setup
 - 3. HDL Reference Design which you must use in your FPGA.
- 3. Additional Documentation about SDR Signal Chains The math behind the RF
- 4. Help and Support

Videos

Software Defined Radio using the Linux IIO Framework



ADI Articles

- Four Quick Steps to Production: Using Model-Based Design for Software-Defined Radio
 - ▶ Part 1—the Analog Devices/Xilinx SDR Rapid Prototyping Platform: Its Capabilities, Benefits, and Tools
 - Part 2—Mode S Detection and Decoding Using MATLAB and Simulink
 - Part 3—Mode S Signals Decoding Algorithm Validation Using Hardware in the Loop
 - Part 4 Rapid Prototyping Using the Zynq SDR Kit and Simulink Code Generation Workflow

MathWorks Webinars

- Modelling and Simulating Analog Devices' RF Transceivers with MATLAB and SimRF
- Getting Started with Software-Defined Radio using MATLAB and Simulink

Warning



All the products described on this page include ESD (electrostatic discharge) sensitive devices. Electrostatic charges as high as 4000V readily accumulate on the human body or test equipment and can discharge without detection.

Although the boards feature ESD protection circuitry, permanent damage may occur on devices subjected to highenergy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality. This includes removing static charge on external equipment, cables, or antennas before connecting to the device.

resources/eval/user-guides/mykonos.txt · Last modified: 05 Jun 2018 16:03 by mhennerich

15,000

Problem Solvers

4,700+

Patents

125,000

50+

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