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Overview

Part Number: VSC8584EV

VSC8584 Evaluation Board ☆



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Overview

The VSC8584EV evaluation board provides the user a way to evaluate the VSC8584 device in multiple configurations. Four RJ-45 connectors are provided for copper media interfaces. The four SFP cages allow for evaluation of the fiber media interconnects. The MAC interface is provided via SMA connectors. For access to all of the features of the device, an external microcontroller is used to configure the on-board clock chip via a two wire serial bus and the VSC8584 via the MDIO bus. The graphical user interface (GUI) enables the user to access the registers. The evaluation board uses a Zarlink device to synthesize a 125MHz reference clock signal from a 20MHz crystal which serves as the REFCLK input.

All Application Notes



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