

1500 Watt Low Capacitance Surface Mount Transient Voltage Suppressor

MSMCGLCE6.5A – MXLSMCGLCE170Ae3, MSMCJLCE6.5A – MXLSMCJLCE170Ae3



Product Overview

This high-reliability surface mount Transient Voltage Suppressor (TVS) product family includes a rectifier diode in series with and in the opposite direction to the primary TVS protection diode. The circuit being protected sees only the rectifier diode's low 100 pF capacitance. They are available in either a DO-215AB (gull-wing) or DO-214AB (J-bend) package, and RoHS compliant versions are available. The low capacitance of these TVS devices allows them to be applied to the protection of high-frequency signal and communication lines in inductive switching environments or systems exposed to the secondary effects of lightning per IEC61000-4-5, as well as RTCA/DO-160G or ARINC 429 for airborne avionics. They also protect from ESD and EFT per IEC61000-4-2 and IEC61000-4-4. If bipolar transient capability is required, two of these low capacitance TVS devices may be used in parallel and opposite directions (anti-parallel) for complete AC protection (Figure 5-3).

Features

- All devices are 100% surge tested.
- Low capacitance of 100 pF or less
- 3σ (sigma) lot norm screening performed on standby current (I_D) for all M prefix devices
- Molding compound flammability rating: UL94V-O
- Unidirectional versions available only
- Two different terminations available in C-bend (modified J-bend with DO-214AB) or Gull-wing (DO-215AB)
- Moisture classification is Level 1 with no dry pack required per IPC/JEDEC J-STD-020B for all M prefix devices
- Suppress transients up to 1,500W at 10/1000 μ s (see Figure 4-1)
- Enhanced reliability screening options with M prefix are available in reference to MIL-PRF-19500. Refer to [High Reliability Non-Hermetic Products Portfolio](#) for more details on the screening options.
(See [Part Nomenclature](#) for all available options.)
- RoHS compliant versions available
- Axial-lead equivalent packages for thru-hole mounting are available as MLCE6.5 to MLCE170Ae3 with 1500W rating (contact Microchip for other surface mount options.)

Applications/Benefits

- Available in working stand-off voltage range of 6.5 to 170V
- Low capacitance for high frequency data line protection to 1 MHz
- Protection for aircraft fast data rate lines up to level 5 waveform 4 and level 2 waveform 5A in RTCA/DO-160G (also see [MicroNote 130](#)) and ARINC 429 with bit rates of 100 kb/s (per ARINC 429, Part 1, par 2.4.1.1)
- IEC61000-4-2 ESD 15 kV (air), 8 kV (contact)
- IEC61000-4-5 (lightning) as further detailed in LCE6.5 thru LCE170A [data sheet](#)
- T1/E1 line cards
- Base stations, WAN and XDSL interfaces
- CSU/DSU equipment

Figure 1. DO-215AB (SMCG) Package

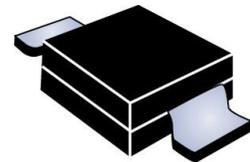


Figure 2. DO-214AB (SMCJ) Package



All SMC series are equivalent to prior SMM package identifications.

Also available in:

Commercial Grade: [SMCG\(J\)LCE6.5 – SMCG\(J\)LCE170Ae3](#)

Case 1 Package (Axial-Leaded): [MLCE6.5 – MLCE170Ae3](#)

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1. Maximum Ratings

Table 1-1. Maximum Ratings at 25 °C Unless Otherwise Stated

Parameters/Test Conditions	Symbol	Value	Unit	
Junction and storage temperature	T_J and T_{STG}	-65 to +150	°C	
Thermal resistance junction-to-lead ¹	$R_{\theta JL}$	20	°C/W	
Peak pulse power dissipation at 25 °C (at 10/1000 μ s, see Figure 4-1 , Figure 4-2 , and Figure 4-3) ²	P_{PP}	1500	W	
Impulse repetition rate (duty factor)	df	0.01 or less	%	
$t_{clamping}$ (0 volts to $V_{(BR)}$ min.)	$t_{clamping}$	$< 5 \times 10^{-9}$	s	
Rated average power dissipation	$T_L = +50$ °C $T_A = 25$ °C	$P_{M(AV)}$	5.0	W
			1.5	
Solder temperature at 10 s	T_{SP}	260	°C	

Notes:

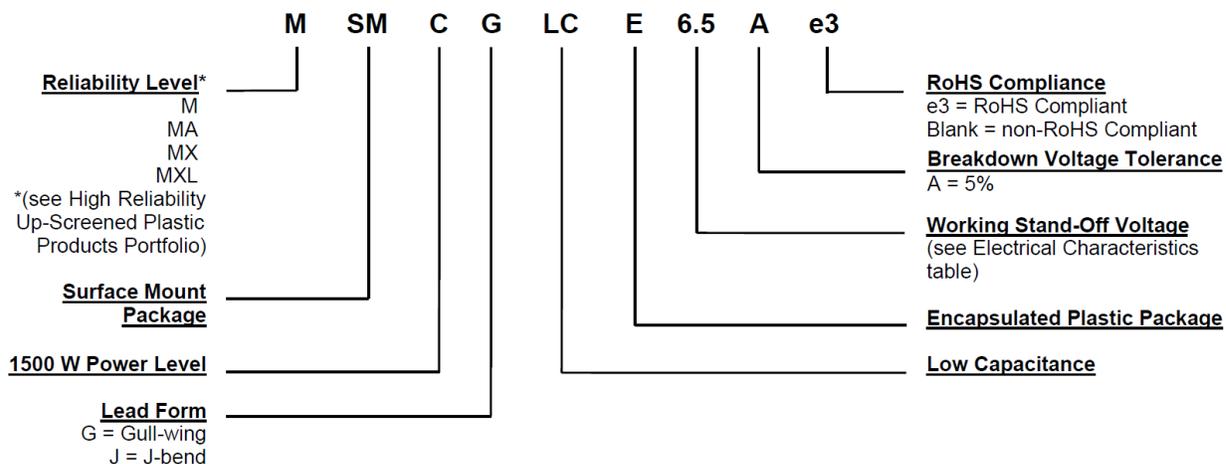
1. Typical junction to lead (tab) at mounting plane
2. With a repetition rate of 0.01% or less. When pulse testing, do not pulse in opposite direction (see [Application Schematics](#) herein and [Figure 5-2](#) and [Figure 5-3](#) for further protection in both directions.)

1.1 Mechanical and Packaging

- Case: Void-free transfer molded thermosetting epoxy body meeting UL94V-0
- Terminals: Tin-lead or RoHS compliant annealed matte-tin plating. Solderable to MIL-STD-750, method 2026
- Marking: Part number with abbreviated prefix (MCLC6.5A, MCLC6.5Ae3, MCLC33, MCLC33e3, etc.)
- Polarity: Cathode indicated by band
- Tape and reel option: Standard per EIA-481-B with 16 mm tape (add "TR" suffix to part number). Consult factory for quantities.
- Weight: Approximately 0.25 grams
- See [Package Dimensions](#)

2. Part Nomenclature

Figure 2-1. Part Nomenclature



2.1 Symbols and Definitions

Table 2-1. Symbols and Definitions

Symbol	Definition
$\alpha_{V(BR)}$	Temperature coefficient of breakdown voltage: The change in breakdown voltage divided by the change in temperature that caused it expressed in %/°C or mV/°C.
C_T	Total capacitance: The total small signal capacitance between the diode terminals of a complete device.
$I_{(BR)}$	Breakdown current: The current used for measuring Breakdown Voltage $V_{(BR)}$.
I_D	Standby current: The current through the device at working standoff voltage.
I_{IB}	Inverse blocking leakage current: The current through a unidirectional-blocking low capacitance device at working inverse blocking voltage (V_{WIB})
I_{PP}	Peak impulse current: The peak current during an impulse.
P_{PP}	Peak pulse power: The peak power that can be applied for a specific pulse width and waveform. The product of I_{PP} and V_C .
$V_{(BR)}$	Breakdown voltage: The voltage across the device at a specified current $I_{(BR)}$ in the breakdown region.
V_C	Clamping voltage: The voltage across the device in a region of low differential resistance during the application of an impulse current (I_{PP}) for a specified waveform.
V_{PIB}	Peak inverse blocking voltage: Minimum breakdown voltage of the series low capacitance rectifier.
V_{WIB}	Working inverse blocking voltage: The maximum-rated value of dc or peak blocking voltage that may be applied to a unidirectional-blocking low-capacitance diode in the inverse direction. Above this rated voltage, the diode is not to be surge or impulse tested for any reason.
V_{WM}	Working standoff voltage: The maximum-rated value of DC or repetitive peak positive cathode-to-anode voltage that may be continuously applied over the standard operating temperature.

3. Electrical Characteristics

Table 3-1. Electrical Characteristics at 25 °C Unless Otherwise Stated

Part Number	Working Standoff Voltage V_{WM} Volts	Breakdown Voltage $V_{(BR)}$ at $I_{(BR)}$ Volts			Maximum Standby Current at V_{WM} I_D μA	Maximum Clamping Voltage at I_{PP} V_C Volts	Maximum Peak Pulse Current I_{PP} at 10/1000 Amps	Working Inverse Blocking Voltage V_{WIB} Volts	Peak Inverse Blocking Voltage V_{PIB} Volts
		Min.	Max.	mA					
MSMCxLCE6.5A	6.5	7.22	7.98	10	1000	11.2	100	75	100
MSMCxLCE7.0A	7.0	7.78	8.60	10	500	12.0	100	75	100
MSMCxLCE7.5A	7.5	8.33	10.2	10	250	12.9	100	75	100
MSMCxLCE8.0A	8.0	8.89	9.83	1	100	13.6	100	75	100
MSMCxLCE8.5A	8.5	9.44	10.4	1	50	14.4	100	75	100
MSMCxLCE9.0A	9.0	10.0	11.1	1	10	15.4	97	75	100
MSMCxLCE10A	10	11.1	12.3	1	5	17.0	88	75	100
MSMCxLCE11A	11	12.2	13.5	1	5	18.2	82	75	100
MSMCxLCE12A	12	13.3	14.7	1	5	19.9	75	75	100
MSMCxLCE13A	13	14.4	15.9	1	5	21.5	70	75	100
MSMCxLCE14A	14	15.6	17.2	1	5	23.2	65	75	100
MSMCxLCE15A	15	16.7	18.5	1	5	24.4	61	75	100
MSMCxLCE16A	16	17.8	19.7	1	5	26.0	57	75	100
MSMCxLCE17A	17	18.9	20.9	1	5	27.6	54	75	100
MSMCxLCE18A	18	20.0	22.1	1	5	29.2	51	75	100
MSMCxLCE20A	20	22.2	24.5	1	5	32.4	46	75	100
MSMCxLCE22A	22	24.4	26.9	1	5	35.5	42	75	100
MSMCxLCE24A	24	26.7	29.5	1	5	38.9	39	75	100
MSMCxLCE26A	26	28.9	31.9	1	5	42.1	36	75	100
MSMCxLCE28A	28	31.1	34.4	1	5	45.5	33	75	100
MSMCxLCE30A	30	33.3	36.8	1	5	48.4	31	75	100
MSMCxLCE33A	33	36.7	40.6	1	5	53.3	28.1	75	100
MSMCxLCE36A	36	40.0	44.2	1	5	58.1	25.8	75	100
MSMCxLCE40A	40	44.4	49.1	1	5	64.5	23.3	75	100
MSMCxLCE43A	43	47.8	52.8	1	5	69.4	21.6	150	200
MSMCxLCE45A	45	50.0	55.3	1	5	72.7	20.6	150	200
MSMCxLCE48A	48	53.3	58.9	1	5	77.4	19.4	150	200
MSMCxLCE51A	51	56.7	62.7	1	5	82.4	18.2	150	200
MSMCxLCE54A	54	60.0	66.3	1	5	87.1	17.2	150	200
MSMCxLCE58A	58	64.4	71.2	1	5	93.6	16.0	150	200
MSMCxLCE60A	60	66.7	73.7	1	5	96.8	15.5	150	200
MSMCxLCE64A	64	71.1	78.6	1	5	103	14.6	150	200
MSMCxLCE70A	70	77.8	85.0	1	5	113	13.3	150	200
MSMCxLCE75A	75	83.3	92.1	1	5	121	12.4	150	200
MSMCxLCE80A	80	88.7	98.0	1	5	129	11.6	150	200
MSMCxLCE90A	90	100	111	1	5	146	10.3	300	200

.....continued

Part Number	Working Standoff Voltage V_{WM} Volts	Breakdown Voltage $V_{(BR)}$ at $I_{(BR)}$ Volts			Maximum Standby Current at V_{WM} I_D μA	Maximum Clamping Voltage at I_{PP} V_C Volts	Maximum Peak Pulse Current I_{PP} at 10/1000 Amps	Working Inverse Blocking Voltage V_{WIB} Volts	Peak Inverse Blocking Voltage V_{PIB} Volts
		Min.	Max.	mA					
MSMCxLCE100A	100	111	123	1	5	162	9.3	300	200
MSMCxLCE110A	110	122	135	1	5	178	8.4	300	400
MSMCxLCE120A	120	133	147	1	5	193	7.8	300	400
MSMCxLCE130A	130	144	159	1	5	209	7.2	300	400
MSMCxLCE150A	150	167	185	1	5	243	6.2	300	400
MSMCxLCE160A	160	178	197	1	5	259	5.8	300	400
MSMCxLCE170A	170	189	209	1	5	275	5.4	300	400

Notes:

1. Normal selection criteria for TVS devices is by working standoff voltage (V_{WM}) and should be equal or greater than DC or continuous peak operating voltage.
2. TVS devices are tested to maximum peak pulse current (I_{PP}) with clamping voltage monitored. This surge capability is one of the most significant electrical characteristics of the device and should be considered as part of customer quality inspections. Test in TVS avalanche direction. Do not pulse in "forward" direction. See [Application Schematics](#).
3. Maximum capacitance of MLCE series at 0 Volts:
 - is 100 pF for V_{WM} 6.5 to 58V
 - is 90 pF for V_{WM} 60 to 170V
4. V_{WIB} is at Inverse Blocking Leakage Current (I_{IB}) of 10 μA .

4. Graphs

Figure 4-1. Peak Pulse Power Vs. Pulse Time

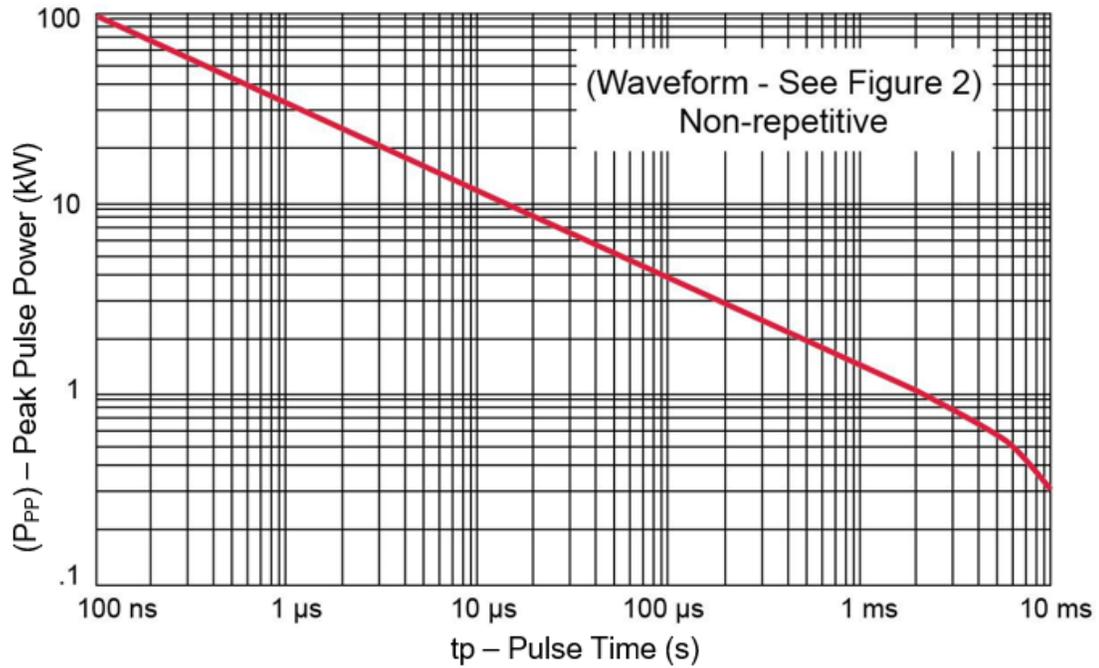


Figure 4-2. Pulse Waveform

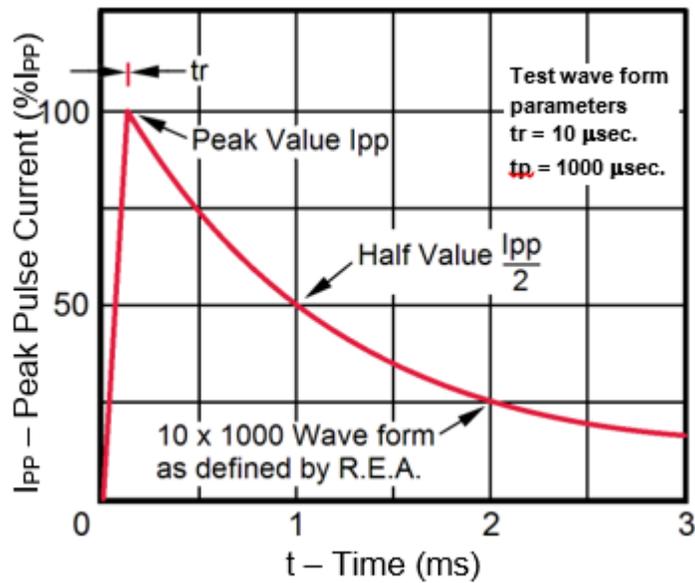
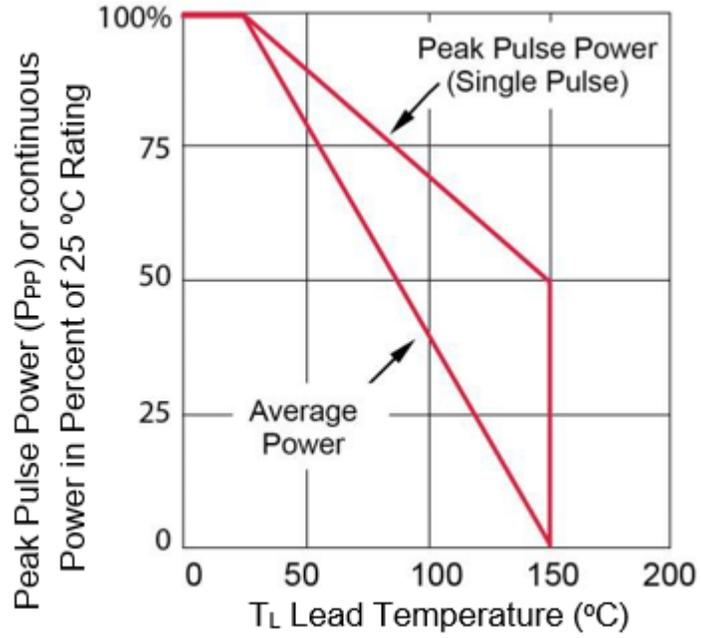


Figure 4-3. Derating Curve



5. Application Schematics

The TVS low capacitance device configuration is shown in [Figure 5-1](#). As a further option for unidirectional applications, an additional low capacitance rectifier diode may be used in parallel in the same polarity direction as the TVS as shown in [Figure 5-2](#). In applications where random high voltage transients occur, this will prevent reverse transients from damaging the internal low capacitance rectifier diode and also provide a low voltage conducting direction. The added rectifier diode should be of similar low capacitance and also have a higher reverse voltage rating than the TVS clamping voltage V_C . The Microchip recommended rectifier part number for the application in [Figure 5-2](#) is the “SMBJLCR80” or “SMBGLCR80” depending on the terminal configuration desired. If using two (2) low capacitance TVS devices in anti-parallel for bidirectional applications, this added protective feature for both directions (including the reverse of each rectifier diode) is inherently provided in [Figure 5-3](#). The unidirectional and bidirectional configurations in [Figure 5-2](#) and [Figure 5-3](#) will both result in twice the capacitance of [Figure 5-1](#).

Figure 5-1. TVS With Internal Low Capacitance Rectifier Diode

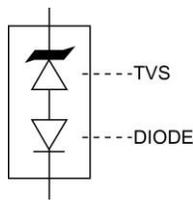


Figure 5-2. Optional Unidirectional Configuration (TVS and Separate Rectifier Diode in Parallel)

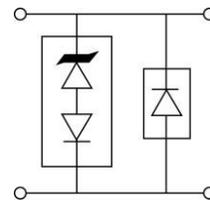
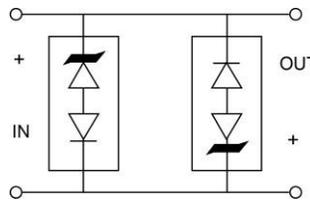
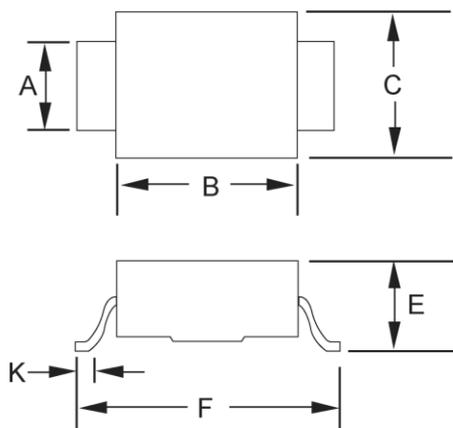


Figure 5-3. Optional Bidirectional Configuration (Two TVS Devices in Anti-Parallel)



6. Package Dimensions

Figure 6-1. SMCG (DO-215AB)

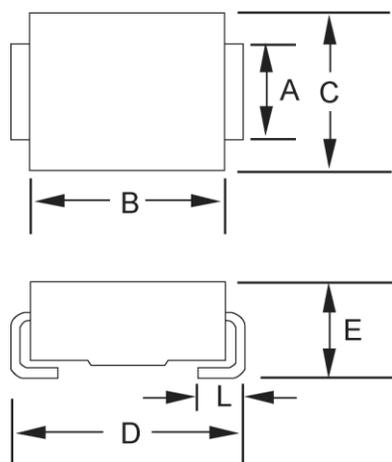


Ltr	Dimensions			
	Inch		Millimeters	
	Min.	Max.	Min.	Max.
A	0.115	0.121	2.92	3.07
B	0.260	0.280	6.60	7.11
C	0.220	0.245	5.59	6.22
E ¹	0.077	0.110	1.95	2.80
F	0.380	0.400	9.65	10.16
K	0.025	0.040	0.635	1.016

Note:

1. Dimension "E" exceeds the JEDEC outline as shown. Typical stand-off height: 0.004"–0.008" (0.1 mm – 0.2 mm).

Figure 6-2. SMCJ (DO-214AB)



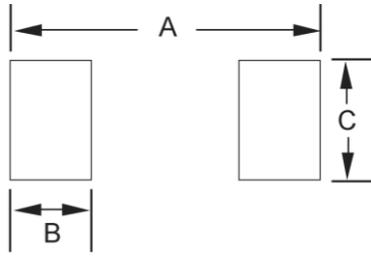
Ltr	Dimensions			
	Inch		Millimeters	
	Min.	Max.	Min.	Max.
A	0.115	0.121	2.92	3.07
B	0.260	0.280	6.60	7.11
C	0.220	0.245	5.59	6.22
D	0.305	0.320	7.75	8.13
E	0.077	0.110	1.95	2.80
L	0.030	0.060	0.760	1.52

Note:

1. Dimension "E" exceeds the JEDEC outline in height as shown. Typical stand-off height: 0.004"–0.008" (0.1 mm – 0.2 mm).

7. Pad Layout

Figure 7-1. Pad Layout



SMCG (DO-215AB)		
Ltr	Inch	Millimeters
A	0.510	12.95
B	0.110	2.79
C	0.150	3.81

SMCJ (DO-214AB)		
Ltr	Inch	Millimeters
A	0.390	9.90
B	0.110	2.79
C	0.150	3.81

8. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description
B	02/2024	Updated Table 2-1 and Table 3-1 .
A	10/2023	Initial revision.

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