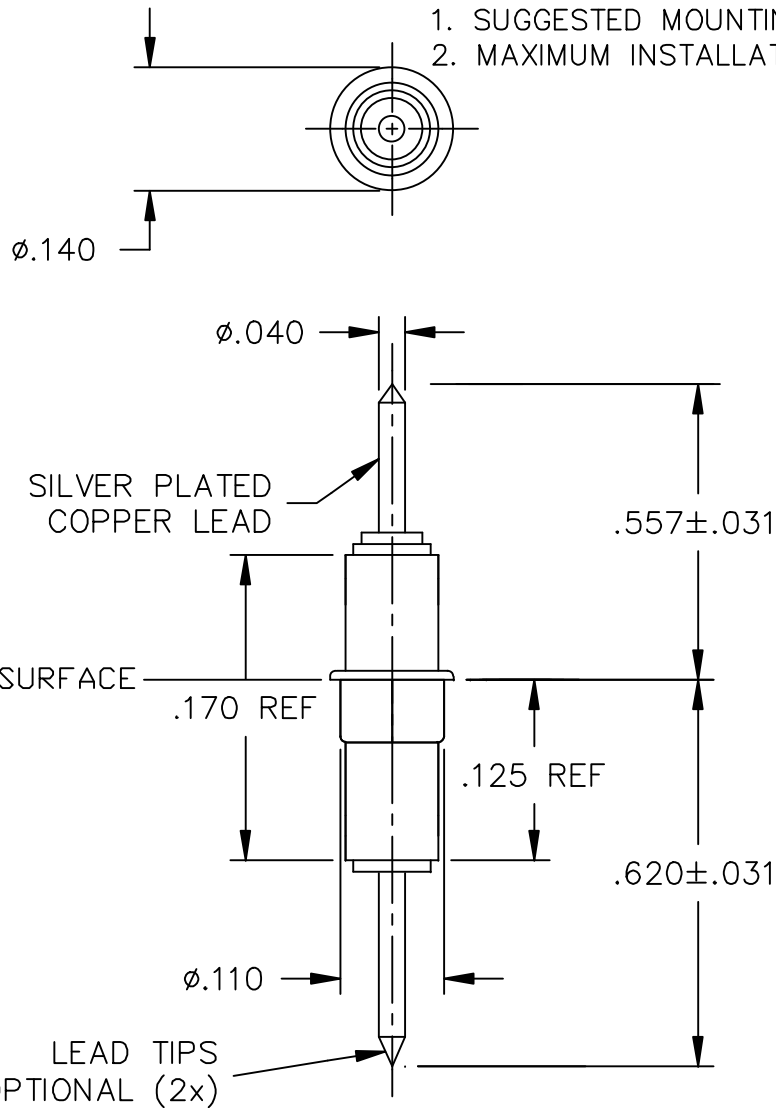
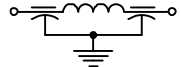
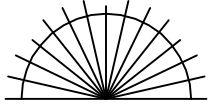


NOTES:

1. SUGGESTED MOUNTING HOLE = $\phi.117 \pm .002$
2. MAXIMUM INSTALLATION TEMP.: 500°F



LEAD FINISH: SILVER	
EYELET FINISH: SOLDER	
CAPACITANCE	1000pF-3000pF
TEMP. RANGE	-25°C TO 85°C
CURRENT	15 AMP MAX.
WORKING VOLTAGE	85°C 100 VDC MAX.
D.C. RESISTANCE	.01ohms MAX.
MIN. I.L. PER MIL-STD-220 AT 25°C NO LOAD	
FREQ. MHz	I.L. (db)
25	10
500	50
1000	55
SCHEMATIC	
	
MARKING: PART: NONE	
BAG: SCI	
51-713-093	
DATE CODE	
LOT#	
QTY/BAG (200 MAX.)	

		0	REV. NO.	TOLERANCES UNLESS OTHERWISE SPECIFIED	 ERIE, FAIRVIEW PENNSYLVANIA SPECTRUM CONTROL INC.		
				Fractions $\pm 1/64$		Dwn. KRDZALIC	Description
				Dec. .xx±		Ckd. 2/7/O1	Pi SOLDER-IN FILTER
				Dec. .xxx±.010		Apvd.	51-713-093
				Angles $\pm 0^{\circ} 30'$		Scale NONE	
		CN 200086	CHANGE NOTICE	CODE IDENT. NO. DLSP2			