

65 GHz Broadband Amplifier Module

Features

- 23 dBm saturated output power
- 30 dB gain (to 50 GHz)
- 2.7 W power dissipation
- Useful gain to 65 GHz
- Small size package
- ECCN 3A001.b.4.e

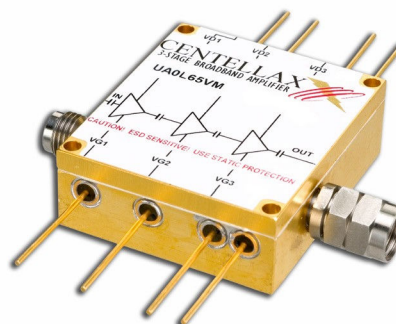
Description

The UA0L65VM Amplifier is a general-purpose broadband amplifier designed for microwave communications, test equipment, and military systems. Its small size and exceptional performance make it a versatile gain block which can improve power and gain in a single hermetically sealed package potentially replacing 2 or 3 narrower band amplifiers.

The UA0L65VM provides a complete amplifier module package with a wide frequency range of 100 kHz to 65 GHz, low power dissipation, ample output power, low noise figure and gain control.

Application

- mm-wave systems
- High frequency test instrumentation
- Broadband gain amplifier



Frequency Domain

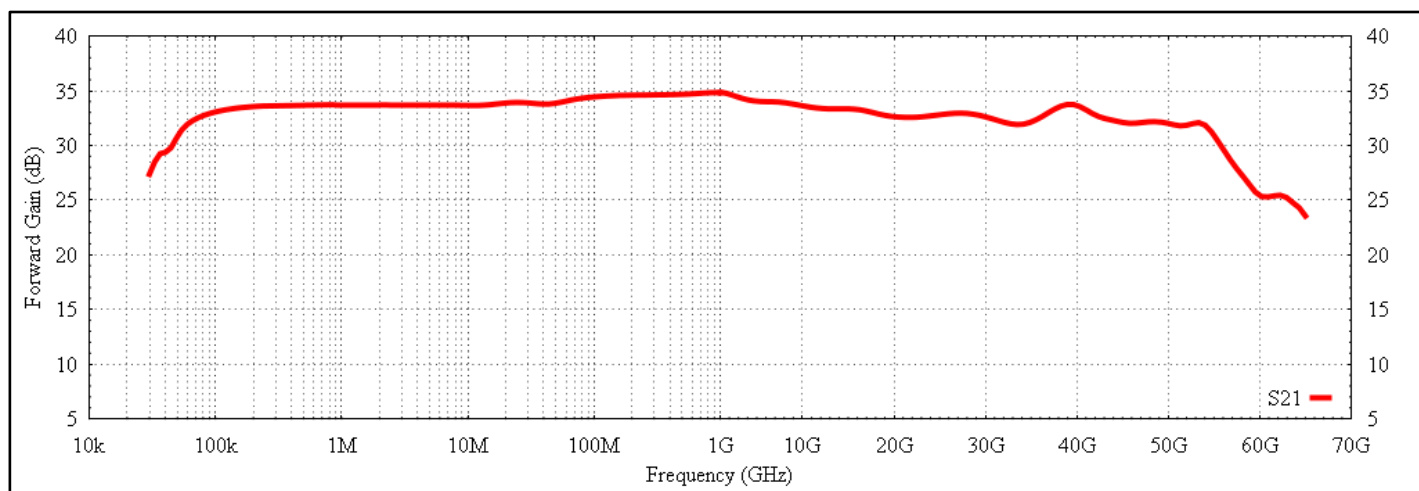
Key Characteristics: (Specifications pertain to case temperature range 0 to +75°C, and standard 2.4mm connectors)

$V_{d1}=V_{d2}=V_{d3}=7V \pm 5\%$, $V_{g1}=V_{g2}= -0.15V$, $V_{g3}= -0.05V$; $Z_o=50\Omega$

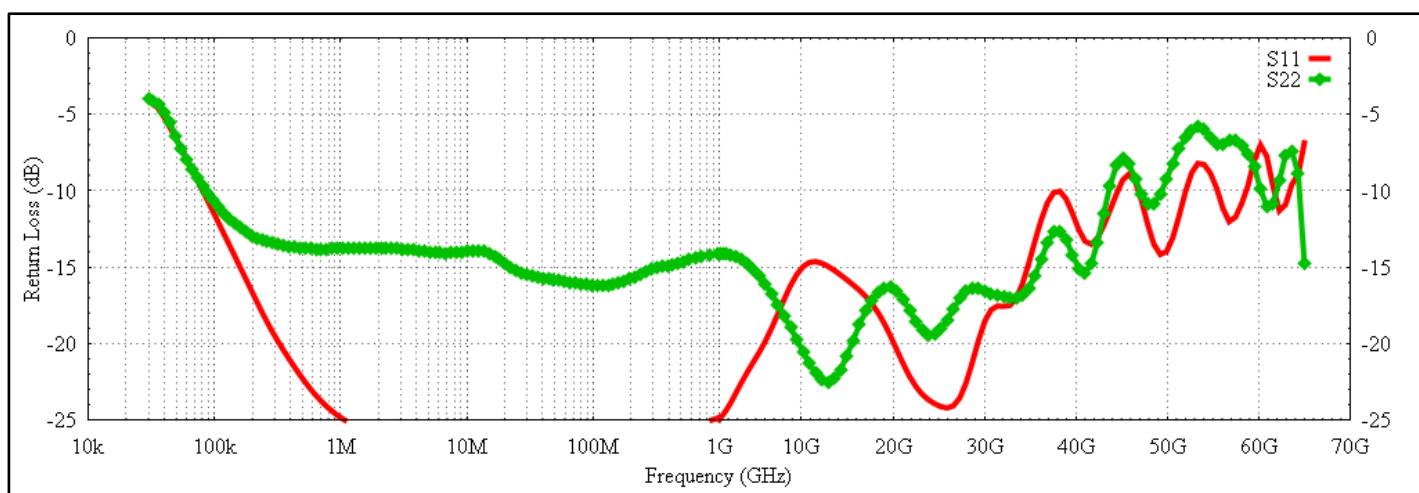
		100kHz - 30GHz			30 - 50GHz		
Parameter	Description	Min	Typ	Max	Min	Typ	Max
S21 (dB)	Small Signal Gain	27	30	-	24	30	-
S11 (dB)	Input Match	-	-15	-10	-	-12	-4
S22 (dB)	Output Match	-	-15	-10	-	-8	-4

* $V_{g1}/V_{g2}/V_{g3}$ adjusted for peak gm

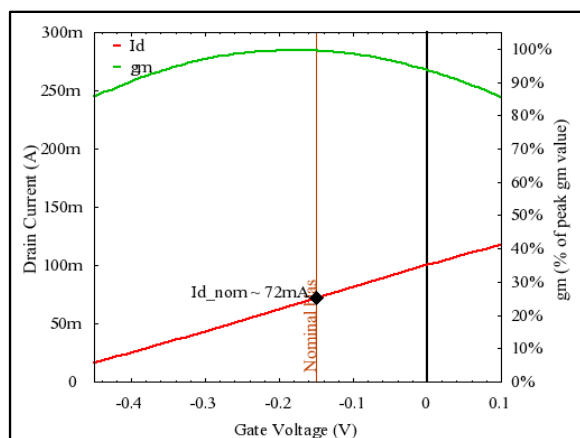
Typical Performance



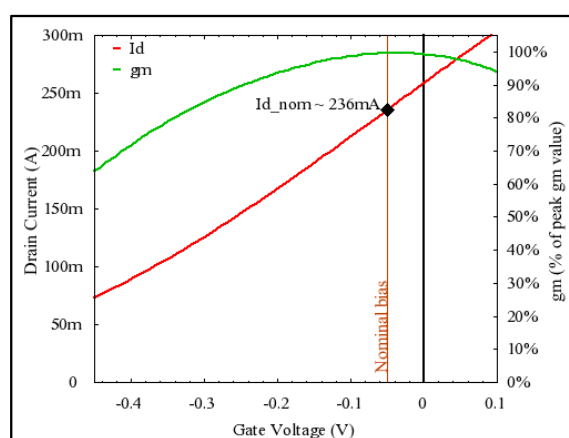
Small Signal, forward gain (S21) vs. Frequency



Small signal, input (S11) & output return (S22) loss vs. Frequency



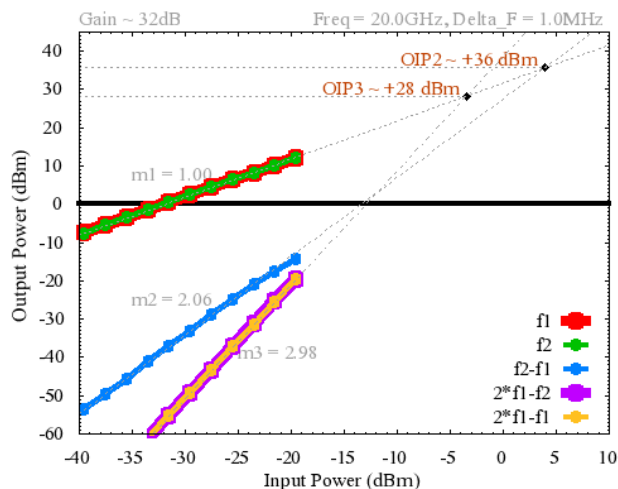
Drain Current and gm vs. Gate Voltage 1st and 2nd amplifier stages



Drain Current and gm vs. Gate Voltage 3rd amplifier stage

Typical Performance

Two Tone Performance @ 20 GHz
Delta frequency = 1MHz



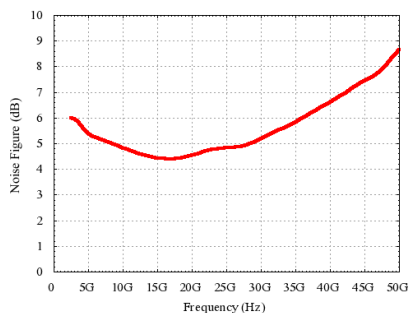
Absolute Maximum Ratings*

Parameter	Description	Minimum	Maximum
Vd1 (V)	First Drain Voltage	-	9
Vd2 (V)	Second Drain Voltage	-	9
Vd3 (V)	Third Drain Voltage	-	9
Id1 (mA)	First Drain Current	-	250
Id3 (mA)	Second Drain Current	-	250
Id3 (mA)	Third Drain Current	-	400
Vg1 (V)	First Gate Voltage	-1.5	1
Vg2 (V)	Second Gate Voltage	-1.5	1
Vg3 (V)	Third Gate Voltage	-1.5	1
Storage Temperature (C)		-55	125
Operating Case Temperature (C)		-25	85
Lead Soldering** (C)		-	260° for 3 sec.
RF Input Power (dBm)		-	20
RF connector torque requirement (in-lb)		-	8

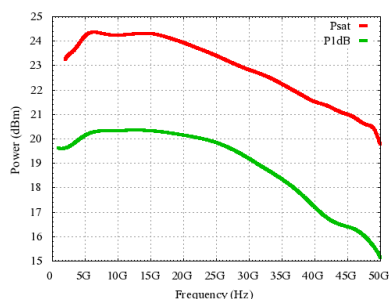
Recommended Operating Bias

Parameter	Typical
Vd1=7V, Vg1= -0.15V	Id1=72mA
Vd2=7V, Vg2= -0.15V	Id2=72mA
Vd3=7V, Vg3= -0.05	Id3=236mA
Power Dissipation	2.7W

Noise Figure vs. Frequency



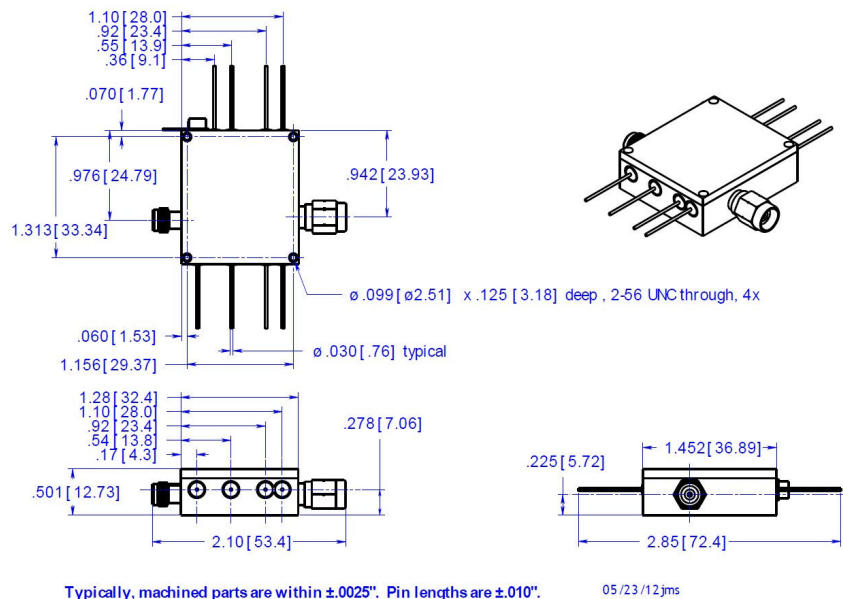
P1db and Psat vs. Frequency



*Operation beyond the values listed under the Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the recommended Operating Bias is not implied. Prolonged use at the absolute maximum rating conditions may affect device reliability.

**The use of a heat sink between the component body and the solder joint is highly recommended.

Physical Dimensions and Pin Assignment



Physical Characteristics

(all measurements in inches[mm])

Tolerance typically
 $\pm 0.0025\text{in}$
 $(\pm 0.0635\text{mm})$

DC pin diameter is
 0.03in [0.76mm]

Table 1: UA0L65VM Pin Definition

Pin	Function	Operational Notes
RFin	RF Input	2.4mm Connector (f) standard, other options available
RFout	RF Output	2.4mm Connector (m) standard, other options available
1 (Vg1)	1st stage gate bias	Adjust for optimum gain
2 (Vg2)	2nd stage gate bias	Adjust for optimum gain
3 (Vg3)	3rd stage gate bias	Adjust for optimum gain
4	NC	Not Connected
5 (Vd1)	1st stage drain bias	Set at typical operating specification
6 (Vd2)	2nd stage drain bias	Set at typical operating specification
7 (Vd3)	3rd stage drain bias	Set at typical operating specification
8	NC	Not Connected

Bias Recommendations (in order):

1) Set gate bias to recommended values; 2) Apply Bias Drains; 3) Adjust bias for optimum gain (maximum gm)

Versatile Bias Board (TE1B) Available.

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