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Dual Channel Multirate Signal Conditioner and Retimer

The VSC8228 is a dual repeater/retimer for Fibre Channel, Gigabit Ethernet, SONET/SDH, and Infiniband applications. The VSC8228 contains dual FibreTimerTM clock recovery units (CRU) for bidirectional signal conditioning in system interconnect and serial backplane applications. The device supports rates from 125 Mbps up to 4.25 Gbps. Using a single reference clock for acquisition, the VSC8228 retransmits the incoming serial data synchronously to the reference clock in retimer mode or to the incoming data in repeater mode. In the retimer mode, add/drop elasticity buffers insert/delete Fibre Channel fill words to account for timing differences in the incoming data and local reference clock.

Product Variants	Description
VSC8228XRC-01	64-pin, 10 x 10mm QFP package, lead-free second-level interconnect
VSC8228RC-01	[Discontinued] 64-pin, 10 x 10mm QFP package

Add to Cart	Category	Title	Access	Revision	Posted	Type	Size
	Product Brief	VSC8228 Product Brief	🔓	2.0	08/11/2005	📄	231 kB
	Datasheet	VSC8228-01 Datasheet	🔒	4.1	02/10/2009	📄	701 kB
	Evaluation System	VSC8228RCEV-01 Evaluation Board User Guide	🔓	1.0	05/11/2006	📄	597 kB
	Model	VSC8228 Spice Model	🔒	2.0	04/13/2005	📄	720 kB
	Reports	VSC8228RC Material Composition Declaration	🔒	3.0	01/22/2008	📄	1 MB
	Reports	VSC8228XRC-01 Material Composition Declaration	🔒	2.0	08/29/2008	📄	1 MB

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Key Features

- Dual clock and data recovery architecture for GbE and Fibre Channel applications
- FibreTimer™ configurable clock recovery unit (CRU): repeater, retimer, or bypassed
- Programmable input signal equalization, output de-emphasis, and output drive levels
- Analog signal detect and protocol monitor indicators
- Optional half-rate SGMII clock and repeated reference clock output

Applications

- Gigabit Ethernet Line Cards
- Fibre Channel Switches, Disk Arrays, RAID Subsystems, Host Bus Adapters
- Blade Server Backplanes

Related Products

- SONET/SDH PHYs

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